

A Scintillating Fibre Tracker

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A scintillating fibre tracker for LHCb [1][2]

The LHCb detector will continue collecting data until the second long shutdown (LS2) of the LHC. At this point the detector will be upgraded with several new sub-detectors. The current Inner and Outer Tracker will be replaced with a scintillating fibre tracker (SciFi).

Motivation:

increase luminosity to 2x10³³cm⁻²s⁻¹

Scintillating fibres Kuraray SCSF-78, 250µm diameter [3]



- Polystyrene, double cladded, blue emitting (peak at 440nm), fast decay time (2.8ns)
- Fibre mats, **Six** layers, 2.5m long, tightly packed
- One sided readout with mirror on the opposite side





Trigger-less readout at 40MHz lacksquare



The SciFi requirements are:

- Hit detection efficiency of >98% lacksquare
- Spatial resolution of better than 100 μ m. \bullet
- Operation in radiation environment (6x10¹¹ \bullet 1MeV n_{eq}/cm² at the SiPM)
- Fast recovery time <50ns \bullet
- Minimal dead zones with no overlapping planes \bullet



Charged particle

Position [cm] Attenuation length measured for different fibre batches

Silicon photomultiplier Hamamatsu 128 channel array [4]





- Customised SiPM from Hamamatsu (H2015): (Benchmarked at $\Delta V=3.5V$)
- Photon detection efficiency (PDE) = 50% and excellent match to the fibres emission
- Dark count rate (DCR) = 15MHz/ch (at -40°C) after irradiation with neutrons at $6x10^{11}$ 1MeV n_{eq}/cm²
- Pixel recovery time = 50ns
- Cross-talk = 9% (trenches for optical crosstalk suppression) After-pulse = 6%





PDE and correlated noise as a function of ΔV





SiPM QA

SiPM functional test and breakdown voltage measurement ΔV_{bd} =50mV



Temperature[°C] DCR as a function of T

PDE as a function of wavelength

Readout electronics [5]

- Custom LHCb front-end ASIC (PACIFIC)
- 64 channel current mode input
- High bandwidth = 250MHz
- Adjustable input anode DC voltage (4-bit DAC) (0-1V)
- Configurable fast shaper
- Interleaved two gated-integrator (no dead time)
- 2-bit non-linear digitalisation per channel (3 thresholds)
- Zero suppression and clusterisation on FPGA, data transmission with optical link

Cooling box

- SiPM cooled down to -40°C
- SiPM is glued to the cooling pipe and optically aligned
- 3D printed Ti cooling pipe
- Integration into a vapour tight cooling enclosure, vacuum insulated cooling pipes
- Cooling with a liquid chiller (Novec or C_6F_{14})







Goal: Quality assurance and characterisation of breakdown voltage for each SiPM channel

- Light tight box, easy access for detectors, cooling for temperature control with homogenous light injection
- Test of all electrical connections
- Breakdown voltage measurement for 8 detectors in less than 10min

