

ISOTDAQ 8TH EDITION 2017

INTERNATIONAL SCHOOL OF
TRIGGER & DATA ACQUISITION

TOPICS

TRIGGER

- NIM ELECTRONICS
- FRONT-END ELECTRONICS
- INTELLIGENT TRIGGERING: ASSOCIATIVE MEMORIES
- FPGA PROGRAMMING

DAQ

- ADC, TDC, DETECTOR READOUT
- MICRO CONTROLLERS
- VMEBUS, XTCA, PCI, PCIE
- DATA NETWORKS
- EVENT AND BUFFER MANAGEMENT
- C/C++ PROGRAMMING FOR DAQ

APPLICATION EXAMPLES

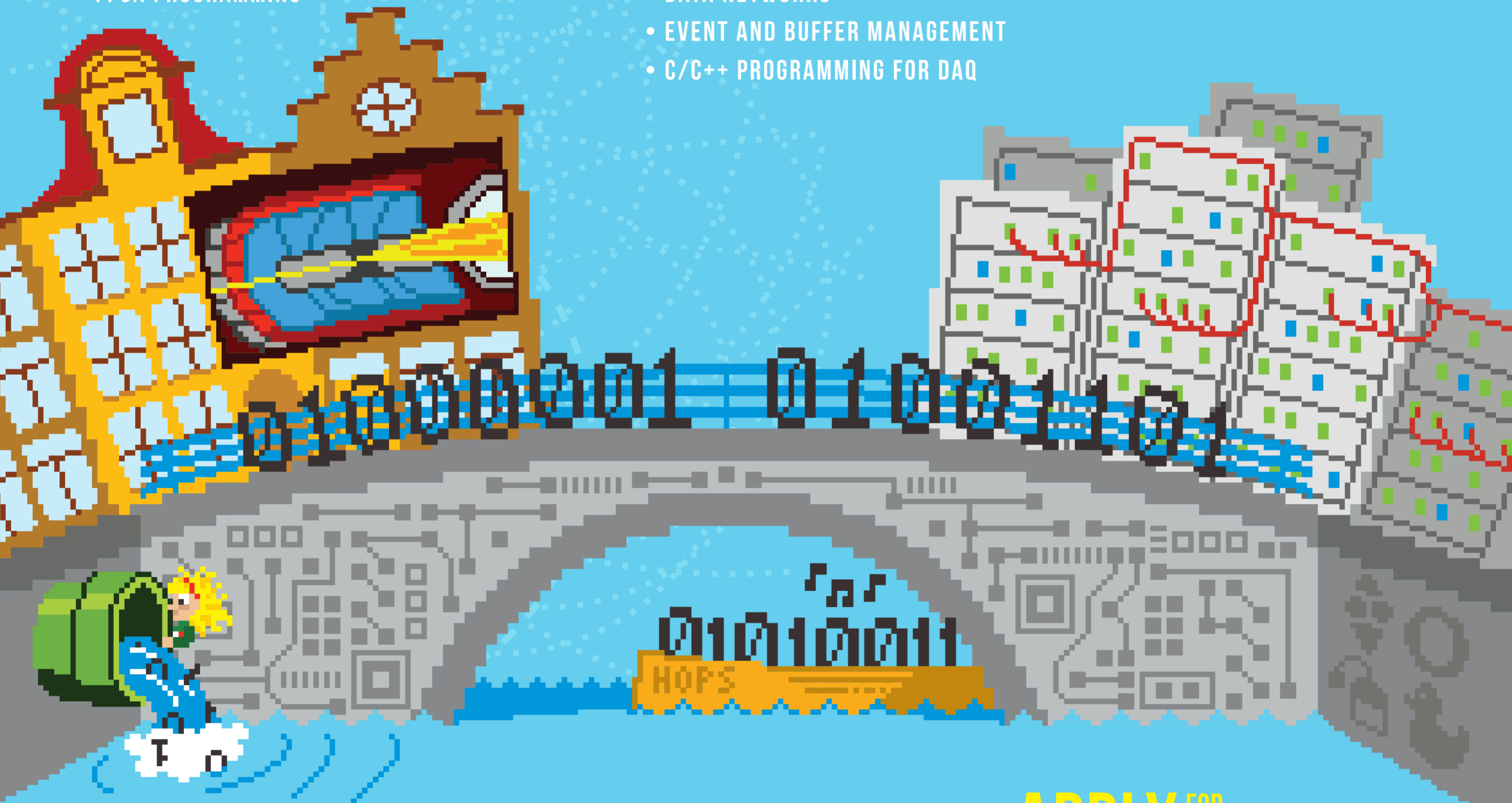
- GENERAL CONCEPTS OF TDAQ
- INSIGHT ON LHC TDAQ
- NON-LHC SYSTEMS



WANT TO KNOW MORE?
CHECK OUT OUR WEBSITE!

[HTTP://INDICO.CERN.CH/EVENT/557251](http://indico.cern.ch/event/557251)

JANUARY 30TH - FEBRUARY 7TH 2017
NIKHEF, AMSTERDAM, THE NETHERLANDS



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LOCAL SUPPORT

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KEES HUYSER	COMMUNICATION

APPLY FOR A SEAT

REGISTRATION DEADLINE NOVEMBER 1ST 2016

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TARGET AUDIENCE PHYSICS OR ENGINEERING MSC.,
PHD STUDENTS AND SCIENTISTS WITH INTEREST IN
TRIGGER AND DATA ACQUISITION

PLACES ARE LIMITED ACCEPTANCE TO THE SCHOOL
IS BY A SELECTION COMMITTEE, **APPLY EARLY.**

