



# ICE-DIP closing workshop

## Wednesday 14 September 2016

### Public session - 513/1-024 (14:00 - 17:30)

time	[id] title	presenter
14:00	[1] Introduction to ICE-DIP and this session	JONES, Bob
14:10	[2] ICE-DIP - key technical points	NOWAK, Andrzej
14:20	[5] Software Switching for Data Acquisition	Mr MALONE, David JERECZEK, Grzegorz
14:40	[3] Radiation-hard silicon photonics for high energy physics and beyond	ZEILER, Marcel
15:00	[4] FPGAs for next gen DAQ and Computing systems at CERN	SRIDHARAN, Srikanth
15:20	[13] Group Photo	
15:30	Break	
16:00	[6] Data transfer on manycore processors for high throughput applications	SANTOGIDIS, Aram
16:20	[7] The consequences of poor vectorization	KARPINSKI, Przemyslaw
16:40	[8] Closing statements - DCU	Prof. BARRY, Liam
16:50	[9] Closing statements - NUIM	Prof. WINSTANLEY , Adam
17:00	[10] Closing statements - Intel	BUTLER, Joe
17:10	[11] Closing statements CERN	NORDBERG, Markus
17:20	[12] Final word	JONES, Bob