Update on Investigator testbeam analysis

- Bias scan for mini-matrix with pitch of 28 μm -

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Wolfgang Klempt, Magdalena Munker, Andreas Nurnberg







Rheinische Friedrich-Wilhelms-Universität Bonn

Outline:

- Introduction
- Reconstruction
- Quality cuts in the analysis
- Results
- Summary & outlook

Introduction

ALICE investigator chip

ALICE investigator chip, TowerJazz 180 nm CMOS imaging process:





- Contains two times 134 matrices with 8x8 pixels
- Different pitch from 20 μm to 50 μm , various implant width and spacing between collection diode and p-ring
- Selected mini-matrix connected to 64 ADCs on readout board, to record full waveform of all pixels with a 65 MHz sampling
- Data from test-beam in June: Investigator placed in centre of the Timepix3-telescope setup

Reconstruction

- Hit definition and common mode filter
- Definition of signal and noise
- Definition of timing observables

Hit definition and common mode filter

Perform for each event:



Calculate for each pixel:

 $\Delta = Amplitude(t_{first}) - Amplitude(t_{last})$

- If there exists a $\Delta > 10$: Pixel is hit candidate
- If there exists no $\Delta > 10$: Pixel is not a hit candidate

General Pixel is used for common mode calculation if it is not a hit candidate

2.) Calculate & apply common mode filter:

- Average waveform of pixels which are no hit candidates = common mode
- Subtract common mode from each pixel

Definition of signal & noise



Calculate signal & noise for hit candidates:

(cut out a few time samples close to t(hit))

A hit candidate is only taken into account as a hit if Signal/Noise > 10

Definition of timing observables

• Fit Function to Waveform of pixels with hit (t = time sample):

 $\label{eq:Function(t)} \mathsf{Function(t)} = \left\{ \begin{array}{ll} \mathsf{Pedestal} & t \leq t(\mathsf{hit}) \\ \mathsf{Pedestal} + \mathsf{Signal}^* \left(e^{[t-t(\mathsf{hit})] \, / \, t(\mathsf{rise})} - 1 \right) & t > t(\mathsf{hit}) \end{array} \right.$

• Example of Function fitted to Waveform of pixels with hit:



Quality cuts in the analysis

Quality cuts in the analysis

Cuts on all observables:

- Masking of edge pixels
- Cut on track-cluster matching: Δ (hit,track position) < 100 µm

Example of mask of edge pixels:

X pixel number



Cut only on timing observables:

- Cut on χ^2 /ndof of waveform fit: 0.8 < χ^2 /ndof < 1.2
 - → Different values to be investigated (ongoing)







Results for different bias voltages - Mini-matrix with a pitch of 28 μ m -

- Cluster signal
- Cluster size & resolution
- Timing
- In-pixel studies

Cluster signal

Motivation:

- Prove expected behaviour:
 - Smaller depletion region
 - More recombination in non-depleted region
 - Smaller cluster signal for lower bias voltages



← Cluster signal shows behaviour expected from recombination

Cluster size

Motivation:

- Expect correlation with signal created in sensor
 - Higher signal created in sensor
 - Larger cluster size



Effect maybe not strong because S/N-cut and / or DAQ is not optimal (to be investigated)?

Resolution

Motivation:

- Expect correlation with cluster size dependancy on the bias voltage:
 - Higher signal created in sensor
 - Larger cluster size
 - Better resolution



 \hookrightarrow Not much better than expected binary resolution of ~ 0.008 mm \hookrightarrow η -correction performed on too less statistics (ongoing)?

Rise time

Motivation:

- Lower field through lower voltage
- Diffusion in larger un-depleted regions for lower bias voltage
- Expect "slower" signals with larger rise time for lower voltage



Agreement with expectation

Hit time

Motivation:

- Lower field through lower voltage
- Diffusion in larger un-depleted regions for lower bias voltage
- How does this effect the hit time of the first pixel in the cluster?

Results:





- → Later hits for lower bias voltage
- → Wider distribution of hit time for lower bias voltage

In-pixel cluster size

Motivation:

- Validate reconstruction on sub-pixel level
- Study behaviour of charge sharing within single pixel cell in dependance of the bias voltage

Results for different bias voltages:



In pixel structure of cluster size clearly visible and according to expectations
 Less charge sharing at the pixel edges and corners for lower bias voltage

In-pixel hit map for different cluster sizes

Motivation:

• More detailed understanding of charge sharing on sub-pixel level

Results $V_{\text{bias}} = 6 \text{ V}$:







In-pixel hit map

Motivation:

• Obtain measure of relative efficiency within the pixel in dependance of bias voltage



- \hookrightarrow Already at 6 V the count rate drops to the pixel edge
- \hookrightarrow Drop of count rate to the pixel edge larger for lower bias voltages

In-pixel timing

Motivation:

- Validate timing on sub-pixel level
- Study behaviour of timing within single pixel cell in dependance of bias voltage



- ← In pixel structure of timing clearly visible
 - → To compare different bias voltages timing on single pixel level is needed to disentangle effect of cluster size (ongoing work)

Summary & outlook

Summary & outlook

Dependancy of signal, cluster size and resolution on bias voltage:

• Results agree with expectations, slower timing & lower signal for lower voltages

In-pixel studies:

- Analysis performing on a great level of detail
- Relative hit map across pixel structure shows drop at pixel edges and corners already at 6 V \rightarrow inefficiencies already at 6 V

Missing bits / necessary next steps:

- In-pixel timing on single pixel level still missing
- Different S/N-cuts need to be investigated
- Different χ^2 /ndof-cuts need to be investigated
- Different DAQ-cuts need to be investigated
- More bias points (especially between 0 and 1 V) missing
- Need efficiency measurement in next test-beam

 Wish-list on the analysis side

 missing
 Wish-list for the next test-beam

Backup

Cluster size

Less dependancy of cluster size on bias voltage because of too high S/N-cut?



- Low bias voltage

To be confirmed / studied