

Integrating AHCAL with EUDET TLU

EUDET TLU

- <https://www.eudet.org/e26/e28/e42441/e57298/EUDET-MEMO-2009-04.pdf>
- LVDS on RJ45:
 - 1. TRIGGER CLOCK- (input to TLU)
 - 2. TRIGGER CLOCK+ (input to TLU)
 - 3. BUSY- (input to TLU)
 - 4. RESET- (output from TLU)
 - 5. RESET+ (output from TLU)
 - 6. BUSY+ (input to TLU)
 - 7. TRIGGER- (output from TLU)
 - 8. TRIGGER+ (output from TLU)

Possibilities

- With current firmware system is busy between particle arriving and complete frame being read out (or two frames??)
 - Frame time = 100us ??
- Use new AIDA mini-TLU
 - AHCAL interface clock , sync and busy signals
- Use EUDET TLU
 - Make special version of firmware that sends system BUSY to reset lines