

Developments for a digital TPC : the SiTPC project

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Results and simulations of a TPC with an endplate with a Micromegas or a GEM amplification followed by a readout by a VLSI CMOS chip Medipix2 will be presented. The steps in progress towards the realisation of a sizeable prototype, within the EUDET program, will be presented: improvement of the protection against breakdown, integration of the Micromegas grid onto the wafer (InGrid), and design of the Timepix chip provided with a timing and/or time-over-threshold capability.

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