LCWS06 (Linear Collider Workshop 2006)

Friday, 10 March 2006

Tracking and Vertexing: H-1 - LT (J. N. Tata Auditorium) (09:00 - 10:30)

time [[id] title	presenter
09:00 [151] Development of DEPFETs for the Vertex Detector	ANDRICEK, Laci
09:25 [[114] CMOS Monolithic Pixel R&D at LBNL	CONTARATO, Devis
09:45 [73] Monolithic CMOS Pixel Detectors for ILC Vertex	Prof. BRAU, James
-	[124] Development of CMOS sensors adapted to the vertex detector requirements	WINTER, Marc

Saturday, 11 March 2006

Tracking and Vertexing: H-2 - Hall B (11:00 - 12:30)

time	[id] title	presenter
11:00	[102] LCFI Status Report: Sensors for ILC Vertex Detector	Dr STEFANOV, Konstantin
	[17] Investigation in the Properties of Charge Traps Created in CCD by Neutron and Electron Irradiation	Dr SINEV, Nikolai
11:45	[150] Simulation Studies of VXD Performance	, Alexei Raspereza , Alexei Raspereza
12:10	[97] LCFI Vertex Detector Design Studies	Dr SOPCZAK, Andre

Sunday, 12 March 2006

Tracking and Vertexing: H-3 - SSCU (11:00 - 12:45)

time	[id] title	presenter
11:00	[26] SiD Vertex Detector Mechanical Design	Dr COOPER, William
11:25	[103] LCFI Status Report: Physics and Mechanics for ILC Vertex Detector	Dr WORM, Steve
	[70] Status on the development of FE and readout electronics for Large Silicon Trackers	Mr GENAT, Jean-Francois
12:00	[71] SiLC R&D present status and perspectives	Dr SAVOY-NAVARRO, Aurore
12:25	[75] Silicon Strip R&D Status in Korea	Prof. PARK, Hwanbae

Tracking and Vertexing: H-4 - SSCU (14:00 - 16:00)

time	[id] title	presenter
14:00	[131] Large-Area Micromegas TPC R&D	RONAN, Michael T.
14:25	[136] Comparison between data and simulation for MT3	SUGIYAMA, Akira
	[82] Studies on the Spatial Resolution and Drift Properties Using Micromegas Equipped TPC	Ms RESERVA, Rosario L. Reserva
15:10	[137] A beam test of prototype TPCs using micro-pattern gas detectors at KEK	KOBAYASHI, Makoto
15:35	[133] Micromegas- and GEM-TPC resolution studies with charge dispersion in a magnetic field in a test beam.	DIXIT, Madhu

Monday, 13 March 2006

Tracking and Vertexing: H-5 - SSCU (11:00 - 12:30)

time [id] title	presenter
11:00 [134] ILC TPC R&D studies at DESY/U. Hamburg	IKEMATSU, Katsumasa
11:25 [135] Developments for a digital TPC : the SiTPC project	COLAS, Paul
11:50 [138] TPC R&D Plans for the Large Prototype	SETTLES, Ron
12:15 [152] Discussion on the Large Prototype	