

Session Program

Sep 25 - 29, 2006

12th Workshop on Electronics for LHC and future Experiments

Parallel Session B7-DAQ session

Valencia, Spain

IFIC – Instituto de Física Corpuscular Edificio Institutos de Investigación Apartado de Correos 22085
E-46071 València SPAIN

Thu, September 28

10:55 AM

Parallel Session B7-DAQ session

Session |

Location: Valencia, Spain, IFIC – Instituto de Fisica Corpuscular Edificio Institutos de Investigación Apartado de Correos 22085 E-46071 València SPAIN

10:55 - 11:20

Production Test Rig for the ATLAS Level-1 Calorimeter Trigger Digital Processors

Speaker

Gilles Mahout

11:20 - 11:40 The Hardware of the ATLAS Pixel Detector Control System

Speaker

Tobias Henss

11:40 - 12:00

Algorithms in the ROD DSP of the ATLAS hadronic Tile Calorimeter

Speaker

Belen Salvachua

12:00 - 12:20

Low-power front-end for a Neutrino Underwater Telescope

Speaker

Domenico Lo Presti

12:20 - 12:40

New RPC front-end electronics for hades

Speaker

Alejandro Gil

12:40 - 13:00

Alibava : A portable readout system for silicon microstrip sensors

Speakers

Ricardo Marco-Hernández, Salvador Martí i García

1:00 PM