

Session Program

25-29 Sept 2006

12th Workshop on Electronics for LHC and future Experiments

Parallel Session B4-Trigger & DAQ session 1

Valencia, Spain

IFIC - Instituto de Fisica Corpuscular Edificio Institutos de Investigación Apartado de Correos 22085
E-46071 València SPAIN

Wednesday 27 September

11:20

Parallel Session B4-Trigger & DAQ session 1

Session |**Location:** Valencia, Spain, IFIC – Instituto de Física Corpuscular Edificio Institutos de Investigación Apartado de Correos 22085 E-46071 València SPAIN

11:20–11:40

Recent developments on th ALICE central Trigger processor

Speaker

Orlando Villalobos Baillie

11:40–12:00

Timing in the ALICE trigger system

Speaker

Roman Lietava

12:00–12:20

The ALICE silicon pixel detector read-out electronics

Speaker

Marian Krivda

12:20–12:40

The ALICE Silicon Pixel Detector Control system and online calibration tools

Speaker

Ivan Amos Cali

12:40–13:00

The Level 0 Pixel Trigger System for the ALICE Silicon Pixel Detector

Speaker

Gianluca Aglieri Rinella

13:00