12th Workshop on Electronics for LHC and future Experiments

Wednesday 27 September 2006

Poster sessions (16:20 - 18:00)

[id] title	presenter	board
[33] Development and test results of a readout chip for the GERDA experiment	SMALE, Nigel	
[106] Radiation Testing of electronic components and systems for the LHC experiments and machine : summary and future	WIJNANDS, Thijs	
[61] A high level modelling approach to design and manage 18 electronics configurations used for the ECAL's endcaps hardware design	VAUMORIN, Emmanuel ROMANTEAU, Thierry	
[44] Data Acquisition and Management in the Calibration Processes of the CMS Barrel Muon Alignment System	SZÉKELY, Géza	
[68] The Level-1 Global Trigger for the CMS Experiment at LHC	TAUROK, Anton JEITLER, Manfred	
[96] Signal Integrity Studies at Optical Multiplexer Board for TileCal System	TORRES PAIS, Jose	
[56] "CMAD", a Full Custom ASIC, for the Upgrade of COMPASS RICH-1	COBANOGLU, Ozgur	
[51] Background for High Energy Space Instrumentation at ISS	RUSSU, Andres	
[109] CMS ECAL Low Voltage system	SINGOVSKI, Alexander	
[83] CMS Optical Links - Lessons learned from Mass Production	TROSKA, Jan	
[46] Design and performance of a PASA for the FAST-TRD Detector of the CBM experiment at FAIR	SOLTVEIT, Hans Kristian	
[35] Radiation tests for Slow Control ALICE TOF systems	ANTONIOLI, Pietro	
[8] Evaluation of Data Transmission at 80MHz and 160MHz Over Backplane, Copper and Optical Links	MATVEEV, Mikhail	
[32] Electronic Devices for Controlling the Very High Voltage in the ALICE TPC Detector	BOCCIOLI, Marco	
[71] The LHC Beam Loss Monitoring System's Surface Building Instalation.	ZAMANTZAS, Christos	
[101] Ideas on DC-DC Converters for Delivery of Low Voltage and High Currents for the SLHC / ILC Detector Electronics in Magnetic field and Radiation environments.	DHAWAN, Satish	
[42] Wafer test of the LHCb Outer Tracker TDC-Chip	KNOPF, Jan	
[79] Implementation of the Control System for the LHCb Muon Detector	ANTUNES NOBREGA, Rafael	
[20] A compact plug-in module for LHC-like trigger emulation	SIDIROPOULOS, Georgios	
[22] Local Trigger Processor Interface Module	NUNES CARACINHA, Diogo	
[130] High-density backplanes – problems and solutions	SILVERSTEIN, Sam	
[86] Functional and linearity tester system for the LHC beam loss monitoring data acquisition card	EMERY, Jonathan	
[98] Commissioning and calibration of the CMS micro-strip tracker	BAINBRIDGE, Robert	
[76] Signal integrity analysis for the electronic design of printed circuit boards	OLTEAN KARLSSON, Alexandra Dana	

[12] The Gigabit Optical Transmitters for the LHCb Calorimeters	IGNAZIO, Lax	
[91] Total Dose and Single Event Effects in a 0.25 m Silicon-On-Sapphire CMOS Technology	GUI, Ping	
[57] Radiation Tolerance Qualification Tests of the Final Source Interface Unit for the ALICE Experiment	DENES, Ervin	
[31] Unified C/VHDL Model Generation of FPGA-based LHCb VELO algorithms	MUECKE, Manfred	
[19] Large scale production of the Multi-Chip Module of the ATLAS Level-1 Calorimeter Trigger	WEBER, Pavel	
[38] Development and Setup of a Prototype System of Distributed Analysis for ATLAS Tier-2	FASSI, Farida	
[25] Low-noise design issues for analog front-end electronics in 130 nm and 90 nm CMOS technologies	MANGHISONI, Massimo	
[85] Setup for testing LHCb Inner Tracker ladders	VAZQUEZ REGUEIRO, Pablo	
[67] Optical pattern generator board	MAGNE, Magali	
[81] System tests and debugging using Python	BROOKE, Jim	
[50] ATLAS Pixel Detector Timing Optimisation with the Back of Crate Card of the Optical Pixel Read out System	FLICK, Tobias	
[99] The CDF Run II Silicon Detector	MITRA, Ankush	
[110] CMS ECAL optical cables testing	SINGOVSKI, Alexander	
[94] The high voltage distribution system for the RICH photon detectors at LHCb	ARNABOLDI, Claudio	
[40] Time Resolution of a Few Nanoseconds in Silicon Strip Detectors Using the APV25 Chip	FRIEDL, Markus	
[80] Revised CMS Global Calorimeter Trigger Functionality & Algorithms	ILES, Gregory Michiel	
[53] A Test Stand System for High-Energy Physics Applications	CARDOSO, Guilherme	
[88] n-XYTER - A CMOS read-out ASIC for a new generation of high rate multichannel counting mode neutron detectors	TRUNK, Ulrich	