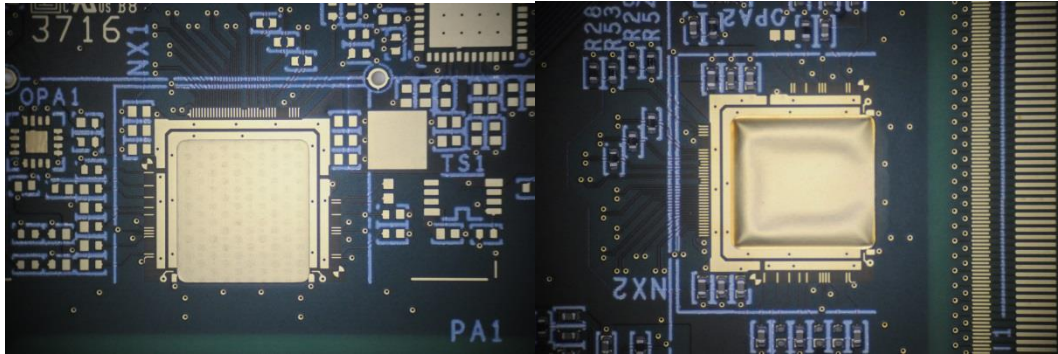


- (1) production of new ASIC-PCB (DNF)
 - PCB shows 'problems' during assembly



LEFT: PCB in good condition. RIGHT: copper layer for ASIC is "uplifted" after assembly

→ HH will ask HMP for an update

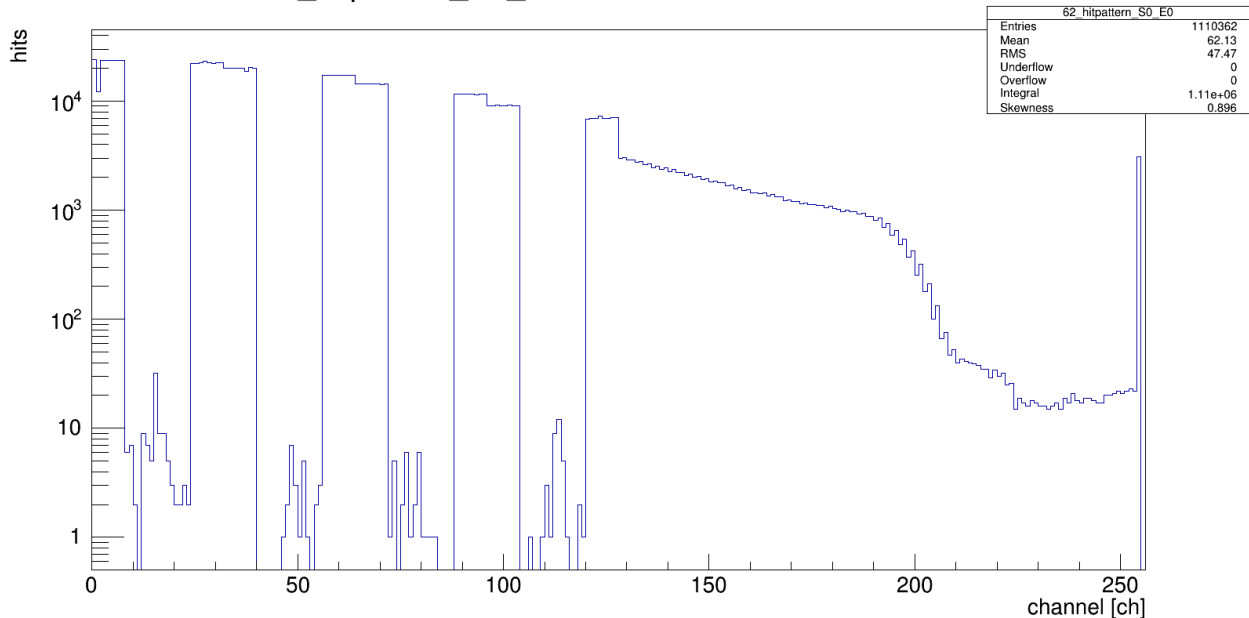
2 PCBs are now with Carmen for "bonding".
Possibility to get them 19.10.16.

→ Arrange the equipment of 300 Pin connector with Smyczek GmbH as soon as we know when we will have a larger quantity of boards available

- (2) investigate problems found in beam time:
 - (2.1) 'wrong' ch IDs on 2 ASICs

Example GMX005 from beam time june 2016:

62_hitpattern_S0_E0 16:09:13 2016-06-17



Detailed analysis with test trigger mode
(Mask channels) is needed

(2.2) 'problem' with chain of 4 cards, we used two PC etc.

- ➔ Test chain of 4 with GEMCON and SFPADA/fiber
Will be tested by HH

Is this connected to Galvanic decoupling of trigger (via fiber)?

- ➔ No additional EXPLODER available, currently. Thus we cannot test it.

(3) problem of coupling cards to PMT (via ATENIX)

- might also be connected to (2)
further investigations needed.

(4) "pulse generator" / multiplexer board

- ➔ Discuss about money source again (Haik)

CPLD or FPGA for 'pulse splitting'

HEX switch for changing fixed 'settings'

- ➔ "Detailed" plan including components-> cost (by HH & CC)

(5) cooling for DNF

Components on backside are in conflict with cooling block!

Will be covered with "thermal tape" in a first approach.