



Radiation effects on charge collection in HV-CMOS detectors

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on behalf of

ATLAS Strip CMOS Collaboration

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Motivation

- Depleted Monolithic Active Sensors with high voltage addons (HV-CMOS) have been investigated as an option for the ATLAS Phase II ITk upgrade (outermost pixel layer, strips)
- Large fill factor HV-CMOS produced on relatively low resistivity substrate (10 – few 1000 Ω·cm) → partial depletion
- Amount of collected charge depends on depletion depth
- Important to determine depletion depth after irradiation



M. Garcia-Sciveres and N. Wermes, arXiv:1705.10150

Radiation hardness requirements									
for ATLAS pixel layer 4 (similar for strips):									
1e15 n _{eq} cm ⁻²									
80 MRad									
•									

CHESS 2 chip

- CHESS 2 chip (large fill factor)
- Full reticle monolithic demonstrator chip of the ATLAS Strip CMOS project
- Designed at UCSC and SLAC, manufactured in the 350 nm technology by AMS
- 3 fully digital striplet arrays, 1 test field with analog test structures
- 4 substrate resistivities $(20 1000 \Omega \cdot cm)$, *n* in *p*
- Same manufacturer also involved in ATLAS Pixel CMOS project (ATLASPix)

	Size
Height	18.6 mm
Width	24.3 mm
Thickness	250 μm
Standard resistivity	20 Ω·cm
Resistivity 2	50-100 Ω·cm
Resistivity 3	200-300 Ω·cm
Resistivity 4	600-2000 Ω·cm



H. Grabas, FEE 2016: <u>https://indico.cern.ch/event/522485</u>/

Sample irradiations

- CHESS 2 irradiations:
 - reactor neutrons (Ljubljana) 4 resistivities, 5 fluences
 - 24 GeV protons (CERN PS) 3 resistivities, 2 fluences
 - 800 MeV protons (Los Alamos LANSCE) 4 resistivities, 3 fluences
- Highest fluences 2e15 n_{eq} cm⁻² (neutrons) and 3.6e15 n_{eq} cm⁻² (protons)
- Characterization of passive structures using Edge-TCT and ⁹⁰Sr
- Measurements before and after annealing (80 min at 60 °C)

φ	unirrad.	1	3	5	10	20	4.2	7.8	8.7	14	36
(10 ¹⁴ n _{eq} cm ⁻²)		(n)	(n)	(n)	(n)	(n)	(p)	(p)	(p)	(p)	(p)
20 Ω·cm		Ljubljana				CERN PS	LANSCE	CERN PS	n.a.	n.a.	
50 Ω·cm						n.a.		n.a.	LANSCE		
200 Ω·cm											
1000 Ω·cm	breakdown						CERIN PS		CERIN PS	n.a.	n.a.

to external amplifiers



Passive test structures:

- 3 x 3 pixel array for Edge-TCT (pixel size 630 x 40 μ m²)
- Large pad for ⁹⁰Sr measurements (1.2 x 1.2 mm²)

Bias voltage up to 120 V can be applied

Edge-TCT setup



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Charge collection profiles



Depleted depth = width of the charge collection profile



Depletion depth increases after irradiation (acceptor removal – later) Depletion depth is larger after irradiation with protons than with neutrons





Due to the beam position at LANSCE the received fluence in the Edge-TCT structure is relatively sensitive to placement precision.

Large error margins on fluence have to be assumed.

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Depletion depth at remaining resistivities



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$$N_{\rm eff} = N_{\rm eff0} - N_{\rm c} \cdot (1 - \exp(-c \cdot \Phi_{\rm eq})) + g_{\rm C} \cdot \Phi_{\rm eq}$$

acceptor removal acceptor introduction

- N_{eff} evolution with fluence described by the acceptor removal model
 - Removal of initial acceptors at lower fluences (beneficial for HV-CMOS)
 - Introduction with constant rate dominant at higher fluences
- Removal rate dependent on initial doping concentration – parameter *c*
- Complete removal observed with neutron irradiation $N_c / N_{eff,0} \approx 1$
- 1000 Ω·cm:

no measurement before irradiation, more measurements at low fluences are needed to evaluate acceptor removal

Acceptor removal constant



$$N_{\rm eff} = N_{\rm eff0} - N_{\rm c} \cdot (1 - \exp(-c \cdot \Phi_{\rm eq})) + g_{\rm C} \cdot \Phi_{\rm eq}$$

- A wide range of acceptor removal measurements available in literature
- General trend: Higher initial resistivity → faster removal The underlying process not yet understood
- With CHESS 2 chip measured removal rate is slower than in samples with similar doping

HV-CMOS: A. Affolder et al., JINST 11 P04007 2016 I. Mandić et al., JINST 12 P02021 2017 E. Cavallaro et al., JINST 12 C01074 2017 B. Hiti et al., JINST 12 P10020 2017



- In all measurements with proton irradiated (PS) samples N_{eff} is below the neutron irradiated samples
- Acceptor removal faster with protons than with neutrons

Effective space charge concentration – LANSCE protons



⁹⁰Sr setup



- HV-CMOS: small signals, large noise \rightarrow S/N low
- Signal strength determined from the shift between triggered and noise spectrum
- System calibrated with epi-diodes of known size











- More charge collected in proton irradiated than in neutron irradiated sample at any fluence/resistivity
- Acceptor removal:

at certain fluences collected charge is more than before irradiation

- Annealing (not shown)
 - ≈ 10 % more charge after 80 min at60 °C for all resistivities
- Very constant response of 200 Ω·cm sample with respect to fluence (both n and p)
- Comparison with Edge-TCT
 Example: 50 Ω·cm and 200 Ω·cm
 measured depletion depth > 100 μm
 - Deposited mean charge > 10000 e⁻
 - Collected mean charge < 5000 e⁻

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⁹⁰Sr collected charge

- In irradiated samples only ≈ 50 % of the deposited charge (Edge-TCT) is collected (⁹⁰Sr)
- The reason for low CCE is sensor biasing from the top
 - Drift of both types of charge carriers ends on the chip surface
 - Holes have to drift through a low *E* region, where trapping is high
 - Trapped charge carriers only pass a part of the weighting field
 - The effect is mitigated by thinning and back plane metalization
 - Observed in LFoundry CMOS chips with and without processed back plane
 - For pixels/strips the effect is not so crucial since most of the signal comes from drift close to the implants (weighting field)



- Extensive sensor radiation hardness study conducted with AMS CHESS 2 samples of different resistivities up to 2e15 (neutrons) and 3.6e15 n_{eq} cm⁻² (protons)
- Acceptor removal is an important beneficial effect in irradiated HV-CMOS
 - Larger depletion depth and greater charge than before irradiation can be observed at some fluences
 - Deeper depletion and more charge after proton irradiation than after neutrons at comparable fluences for all resistivities
 - Faster removal with protons
 - Smaller acceptor introduction rate with protons
 - Acceptor removal parameters determined for n irradiated samples
- Optimal case (200 Ω·cm):
 - > 60 μm depletion depth (Edge-TCT)

> 2000 electrons collected (⁹⁰Sr MIPs)

for any irradiation type and fluence in the relevant range

Back plane metalization would likely improve charge collection after irradiation

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BACKUP





- Collected charge before and after annealing (80 min at 60 °C)
- After annealing about 10 % increase in charge
- Later electrical breakdown in annealed samples