

11th International "Hiroshima" Symposium on the Development and Application of Semiconductor Tracking Detectors (HSTD11) in conjunction with 2nd Workshop on SOI Pixel Detectors (SOIPIX2017) at OIST, Okinawa, Japan

Tuesday, 12 December 2017

Session6 - Conference Center (11:00 - 12:30)

-Conveners: Craig Buttar

time	[id] title	presenter
11:00	[102] The Phase-2 ATLAS ITk Pixel Upgrade	ROSSI, Leonardo
11:20	[154] Development of the radiation tolerant fine size planar pixel detector by HPK/KEK	NAKAMURA, Koji
11:40	[17] Advances in pixel electronics for experiments with high rate and radiation	GARCIA-SCIVERES, Maurice
12:10	[190] A SOI Pixel Detector Using Pinned Depleted Diode Structure For High-Energy-Resolution X-ray Imaging and High-Sensitivity NIR Imaging	KAWAHITO, Shoji