1. PROM tests

- **DCFEB firmware downloading**
  - Failure rate ~10%

- **DCFEB PROM unused blocks test**
  - Probes ~70% of the PROM
  - Ran on all system
  - Found only 3 boards with issues
    - To be investigated in more detail

- **CHARM irradiation test**
  - Irradiated 2 boards with 32P + 128X to ~30kRad
  - First tests after the irradiation
    - Board #1 had no JTAG communication
    - Board #2 had bitflips, could not program 32P, could not erase 128X
  - Found dead LDO on board #1 and so-so regulator on board #2
  - After LDO replacement
    - Board #2: 32P PROM looks ok
      - Erase, blank check OK
      - Program, verify OK
    - Board #2: 128X PROM -- still cannot erase
    - Board #1: JTAG OK after Petrs "tricks", PROMs not tested yet