

Future CTP7 (for CSC FED upgrade)

- **Current CTP7 price per link count is optimal for CSC FED**
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- **Can we consider a plan for an ATCA design option that has nearly the same price per link?**
 - Need to find a cost-effective FPGA ~2021
 - We do not need faster links
 - We do not need more logic
- **If there is no plan for UW to build CTP7 hardware after 2020, can TAMU get CTP7 project design files so we can produce them ourselves around 2021?**
 - We will have no funds for this before 2021