

## Topics for the Discussion Session

### based on the first day closing discussions

1. CSC on-chamber
  - a. Summary of the previous PROM studies (including Evaldas), studies that have already been planned for the next 2 months, what else we should do soon
  - b. Evaluation boards that we want to design and test (types of FPGAs, PROMs, etc.)
  - c. Choice of ALCT (oscillator, compatibility with ODMB) and OTMB technology (keep backward compatibility, types of OTMBs)
  - d. GEM-CSC interface commissioning
2. CSC off-chamber
  - a. ODMB/FED design options (# links, speeds, back-pressure)
  - b. Next-generation CTP7-type with less logic (lower costs)
  - c. Common ATCA CERN solutions for FEDs
3. GE2/1 and ME0
  - a. GE2/1 PROM, back-end bandwidth and rates after LS3 (applies mostly to GE1/1)
  - b. ME0 PROM- and FPGA-less solutions
  - c. What exactly we need from simulations to determine the granularity
  - d. VFAT4 wishlist
  - e. Radiation tests
  - f. Dead-time/saturation for VFAT3 in high-eta ME0
  - g. Summary of expected rates