

AOB

LHC BCT 24 bits tests for SMP LHC

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Reminder

- New LHC BCT acquisition system based on 24 bits ADC
- Concerns the 4 links to the SMP LHC
- Tests performed during TS3 => Works well with excellent resolution
- Foreseen to replace the actual 16 bits acquisition system during these EYETS

Planning

- New system should be plugged from the beginning of EYETS
- Needs 1 full week of tests in order to
 - Validate the intensity functionality (precision after calibration)
 - Validate the behaviour during and after calibration (endurance test)
 - Ensure no timing frame garbage disturb the acquisition by the SMP
- Will be part of the SMP re-commissioning after a YETS