



# MICROCHANNEL COOLING FOR SILICON DETECTORS

Status and perspectives at AIDA-2020 mid-term

Alessandro Mapelli  
Paolo Petagna

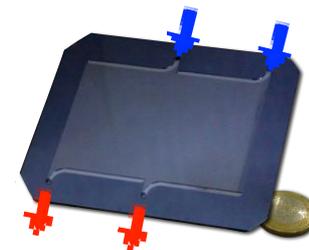
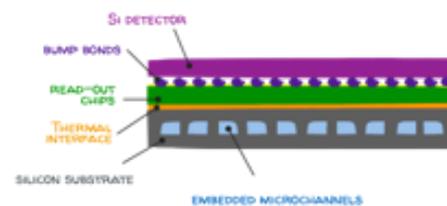
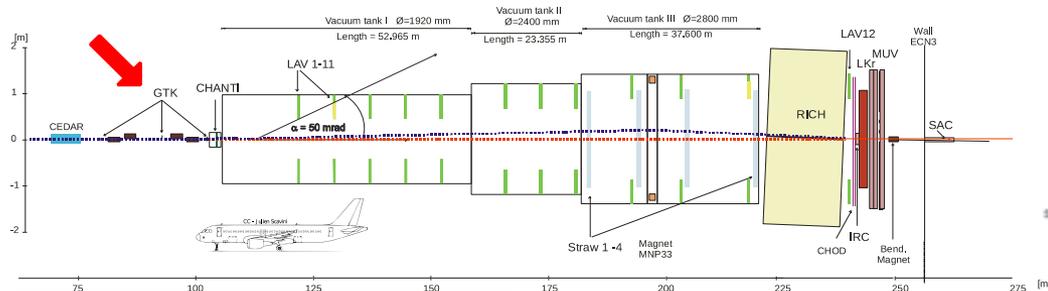
22 February 2017



TREDI 2017

12th Trento Workshop on Advanced Silicon Radiation Detectors  
Monday 20 February 2017 to Wednesday 22 February 2017 - TRENTO

NA62 is the first experiment to use silicon microcooling plates for the thermal management of their GTK pixel detectors (since 2014).

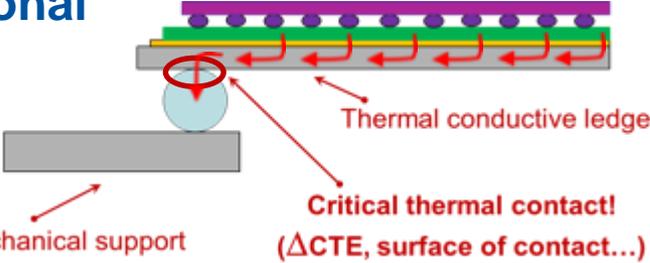
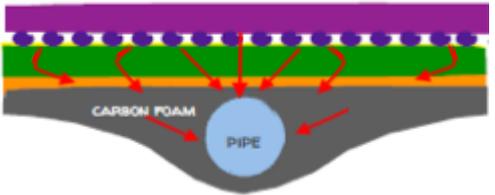
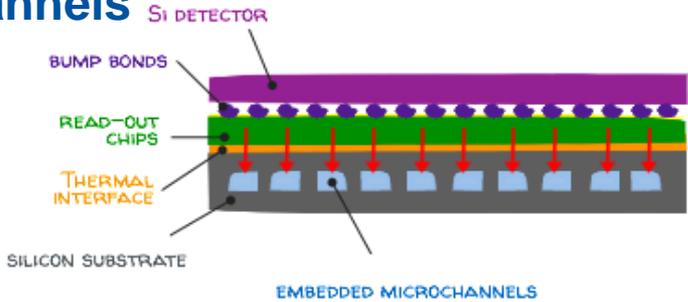


- 3 detector modules
- Liquid  $C_6F_{14}$
- $T_{op}$  below  $-10^{\circ}C$
- Power dissipation 25W-48W over  $6 \times 4 \text{cm}^2$

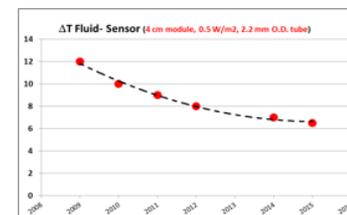
A. Mapelli *et al.* 2012 JINST 7 C01111

P. Petagna *et al.*, Microelec. Journal 44 (2013) 612–618

G. Romagnoli *et al.*, Microelec. Eng. 145 (2015) 133-137

approach	TFM
<p><b>conventional</b></p>  <p>(CFRP) Mechanical support</p> <p>Thermal conductive ledge</p> <p>Critical thermal contact! (<math>\Delta CTE</math>, surface of contact...)</p>	20
<p><b>integrated</b></p>  <p>CARBON FOAM</p> <p>PIPE</p>	12
<p><b>microchannels</b></p>  <p>Si DETECTOR</p> <p>BUMP BONDS</p> <p>READ-OUT CHIPS</p> <p>THERMAL INTERFACE</p> <p>SILICON SUBSTRATE</p> <p>EMBEDDED MICROCHANNELS</p>	<p>5-8 <i>liquid</i></p> <hr/> <p>3 <i>bi-phase</i></p>

$$TFM = \frac{(\Delta T \text{ fluid-sensor})}{(\text{power density})}$$



NA62-GTK,  $C_6F_{14}$

LHCb VeLo,  $CO_2$



8" Si wafer



DRIE channels



wafer bonding



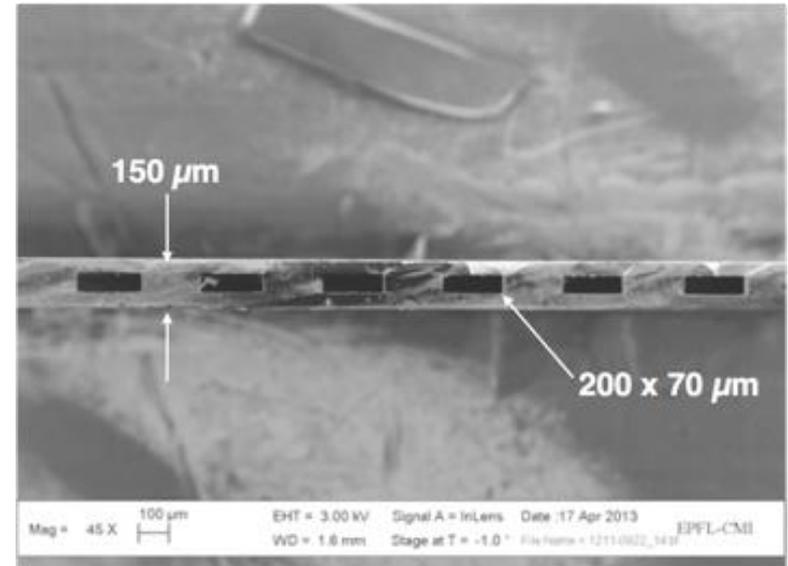
DRIE inlets



DRIE acceptance



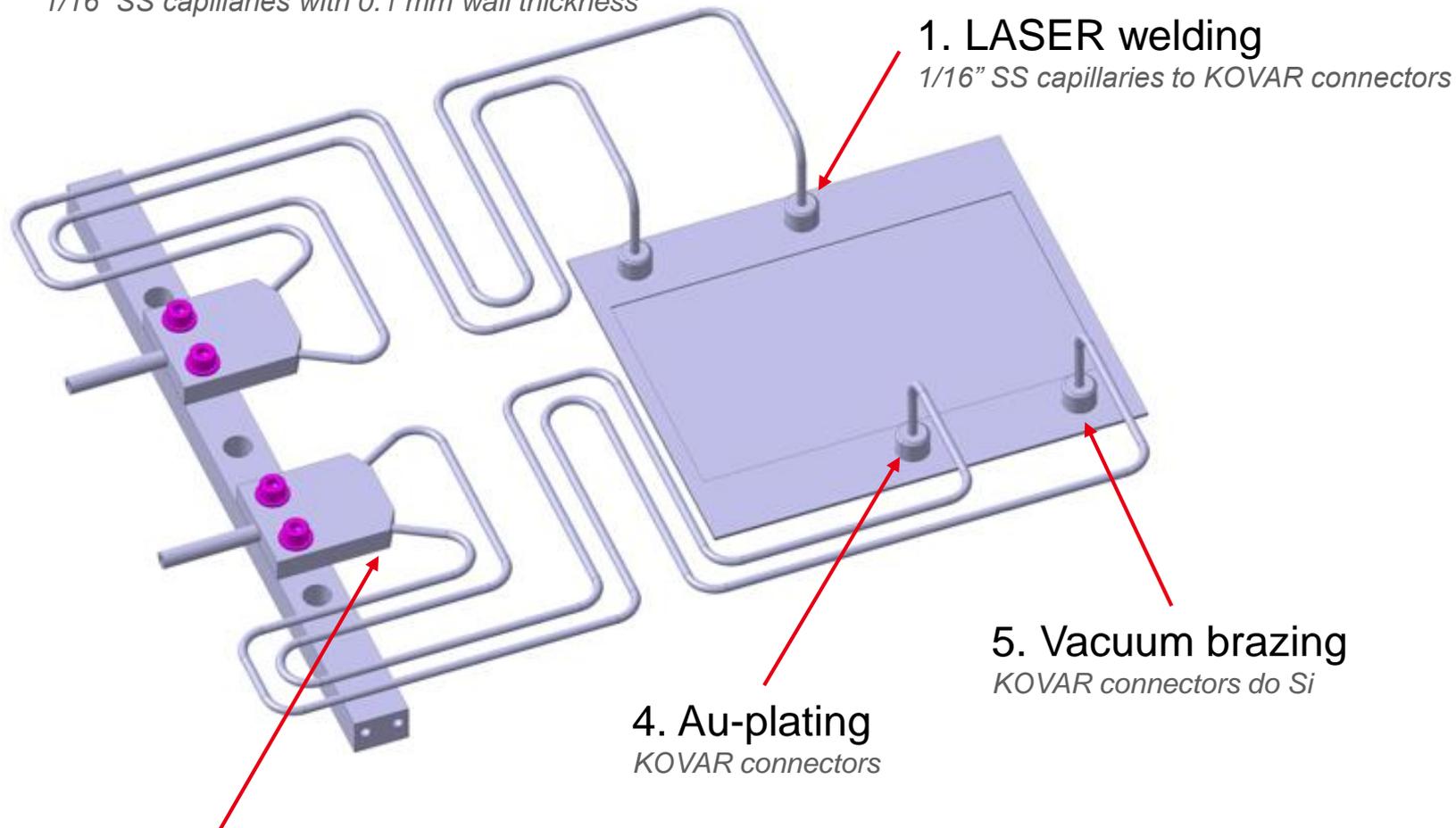
Metallization



- Design by CERN EP-DT
- Prototypes fabricated by EP-DT at EPFL-CMi on 4" wafers
- Pre-production series by IceMOS on 6" wafers
- Two batches fabricated at CEA-Leti on 8" wafers

## 2. Bending of the capillaries

*1/16" SS capillaries with 0.1 mm wall thickness*



## 1. LASER welding

*1/16" SS capillaries to KOVAR connectors*

## 5. Vacuum brazing

*KOVAR connectors do Si*

## 4. Au-plating

*KOVAR connectors*

## 3. MICROBRAZ brazing

*1/16" SS capillaries to SS manifolds*

## A NETWORK TO COORDINATE THE EFFORTS:



- **microfab**

silicon direct bonding... critical step for pressurized channels!

- buried channels: complex channels in silicon without bonding?
- additive manufacturing: working devices in ceramic (~matching CTE)?

- **connectors**

machined metallic connector brazed on silicon: OK. Better alternatives?

- 3D printed connectors: Polymeric / Metallic / Ceramics

- **thermo-fluid dynamics data for models**

- fundamental studies on CO<sub>2</sub> evaporation in microchannels
- performance studies on prototype silicon devices in single phase flow
- performance studies on prototype silicon devices in two-phases flow



## WAFER BONDING APPROACH

Low material budget microfabricated cooling devices for particle detectors and front-end electronics

A. Mapelli<sup>a,b</sup>, A. Catinaccio<sup>a</sup>, J. Daguin<sup>a</sup>, H. van Lintel<sup>b</sup>, G. Nuesle<sup>c</sup>, P. Petagna<sup>a</sup>, P. Renaud<sup>b</sup>,

<sup>a</sup>Physics Department, CERN, Geneva, Switzerland

<sup>b</sup>Laboratoire de Microsystèmes, Ecole Polytechnique Fédérale de Lausanne, Switzerland

<sup>c</sup>CP3, Université catholique de Louvain, Louvain-la-Neuve, Belgique

Nuclear Physics B (Proc. Suppl.) 215 (2011) 349–352



## BURIED CHANNELS APPROACH

Silicon buried channels for pixel detector cooling

M. Boscardin<sup>a,\*</sup>, P. Conci<sup>a</sup>, M. Crivellari<sup>a</sup>, S. Ronchin<sup>a</sup>, S. Bettarini<sup>b,c</sup>, F. Bosi<sup>c</sup>

<sup>a</sup>Fondazione Bruno Kessler Trento, Via Sommarive 18, I-38123 Trento, Italy

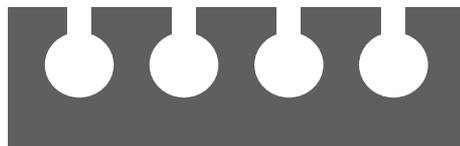
<sup>b</sup>Università di Pisa, Lgo B. Pontecorvo 3, I-56127 Pisa, Italy

<sup>c</sup>Istituto Nazionale di Fisica Nucleare, Sez. di Pisa, Lgo B. Pontecorvo 3, I-56127 Pisa, Italy

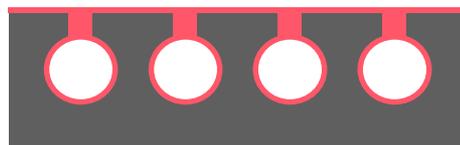
Nuclear Instruments and Methods in Physics Research A 718 (2013) 297–298



trenches - anisotropic etch



channels - isotropic etch



trench filling

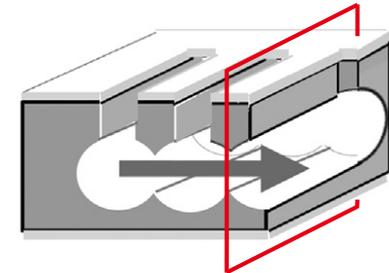
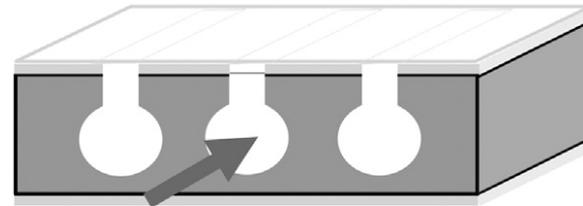
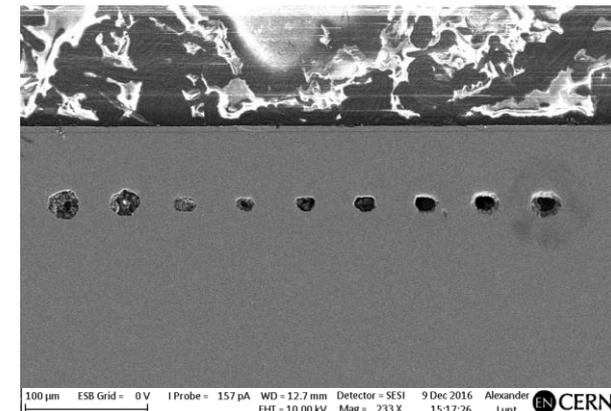
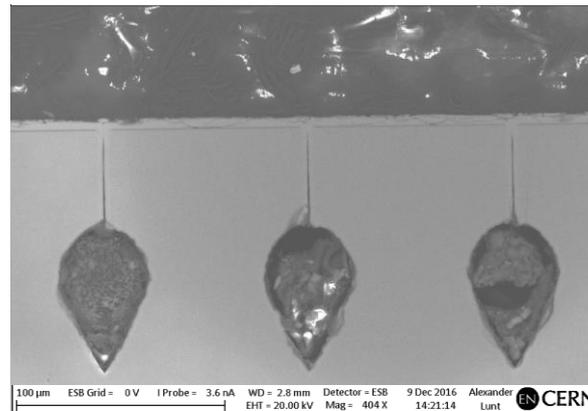
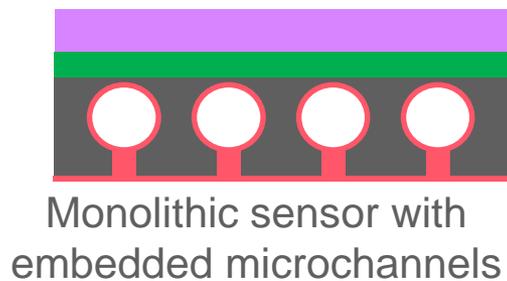
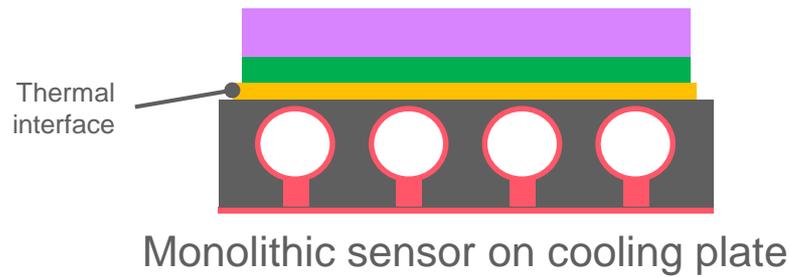
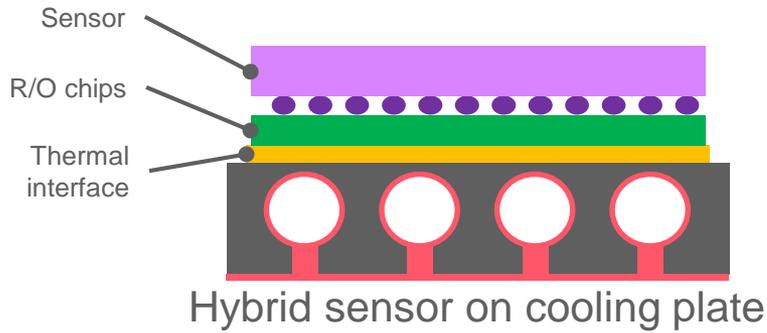


Fig. 1. Process sections for longitudinal and transverse channels.



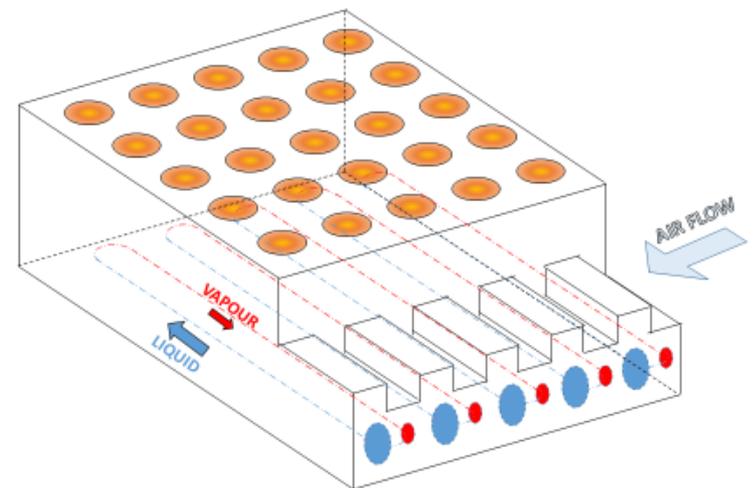


## Integrated, autonomous solutions for thermal management in high energy physics and space applications

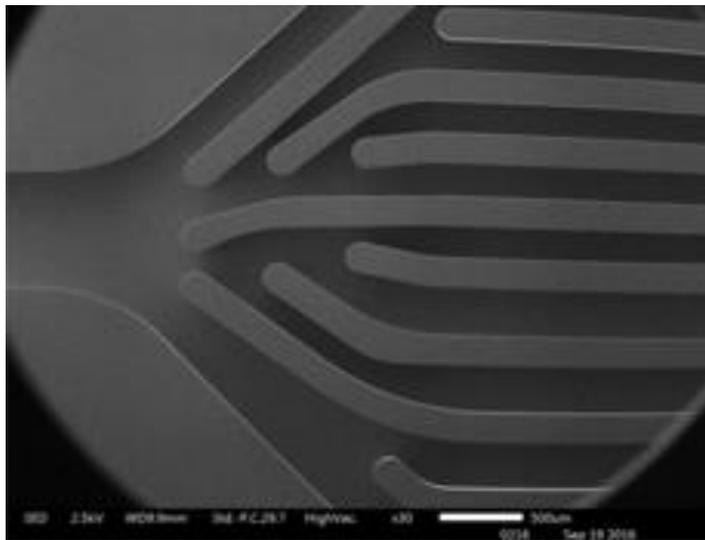
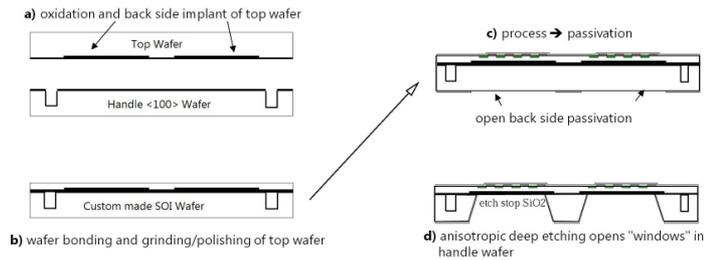
D. Alvarez Feito<sup>1</sup>, M. Boscardin<sup>2</sup>, A. Mapelli<sup>1</sup>, P. Petagna<sup>1</sup>

<sup>1</sup>CERN EP-DT, Experimental Physics Department, Detector Technologies Group, Geneva, Switzerland

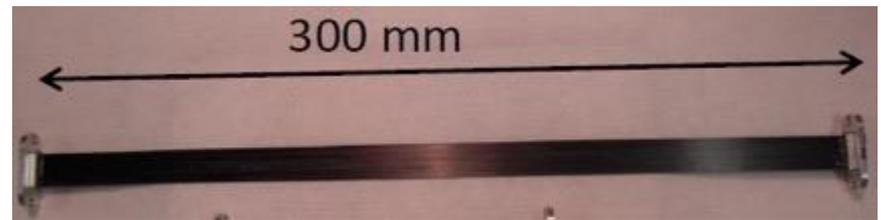
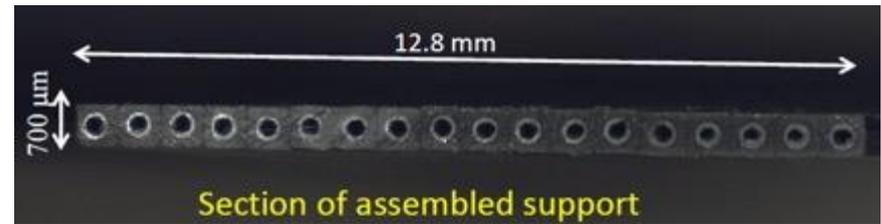
<sup>2</sup>FBK, Fondazione Bruno Kessler, Trento, Italy



**Valencia-Bonn-Munich:**  
SOI substrate with embedded DRIE channels  
*see also: 2016 JINST 11 P06018*



**Pisa:**  
PEEK+CFRP pultruded pipes array

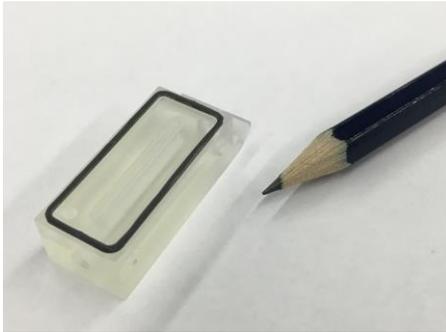


Images courtesy of A. Messineo and F. Bosi

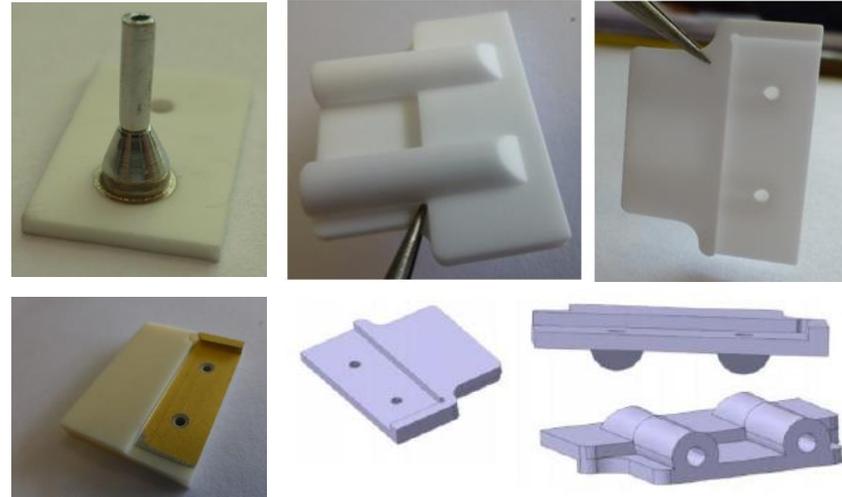
**CERN + Manchester (+ industry):**  
3D printed ceramic channels demonstrators



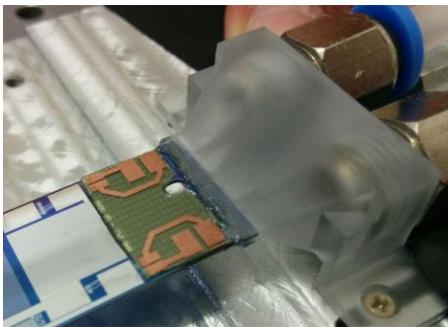
## CERN (Polymer)



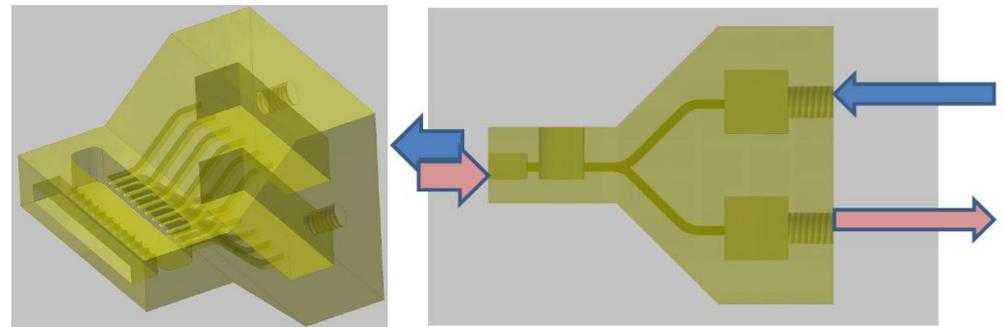
## CERN + Manchester (Ceramics)



## Valencia (Polymer)

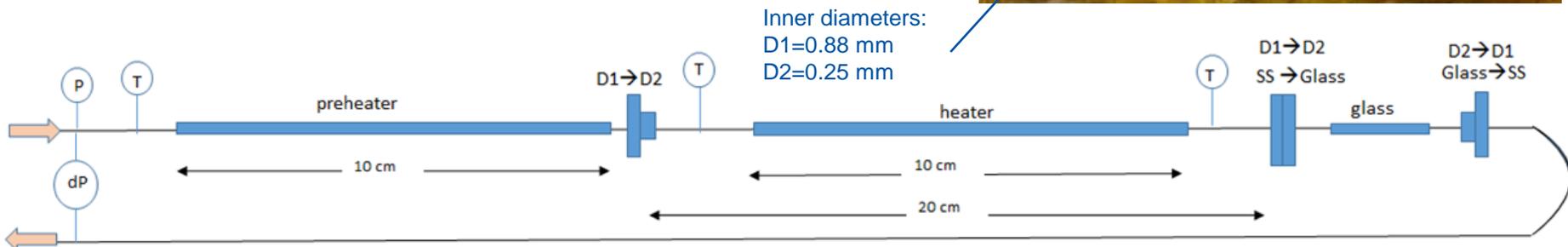
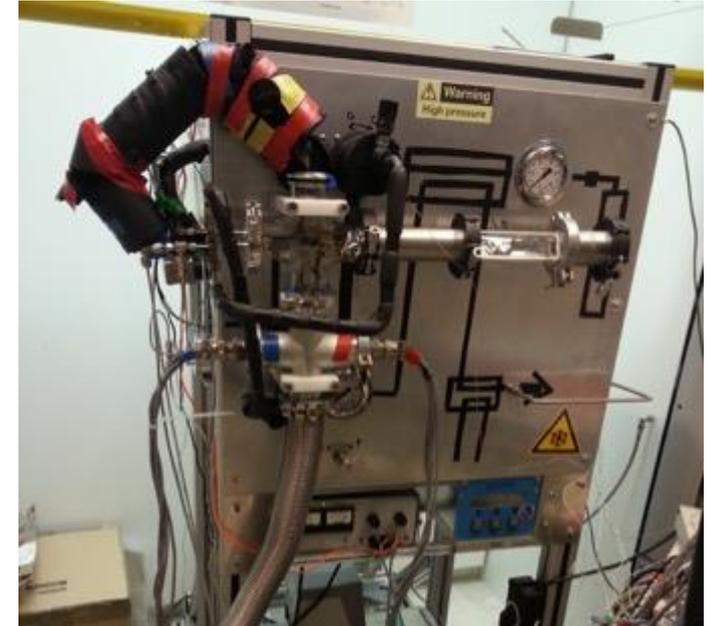


## Pisa (design ready for metal or polymer)

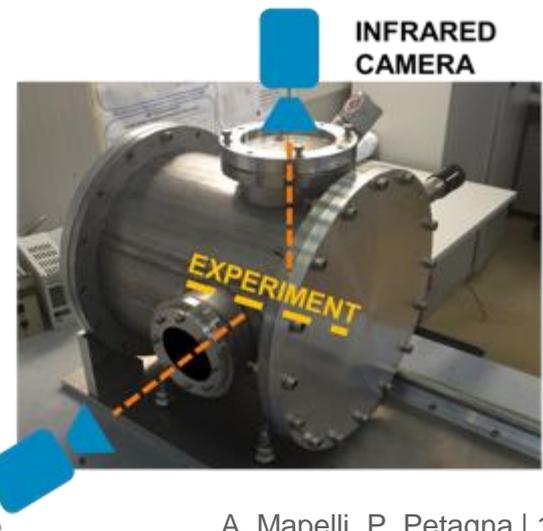


## A. Koutoulaki (UT, Nikhef), Marcel Ter Brake (UT), Harry Holland (UT)

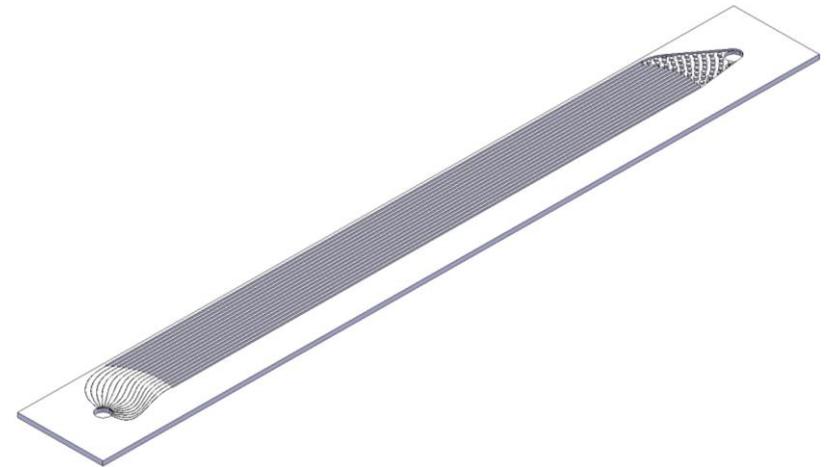
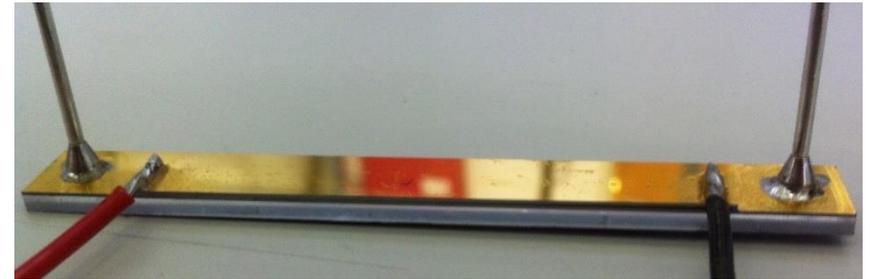
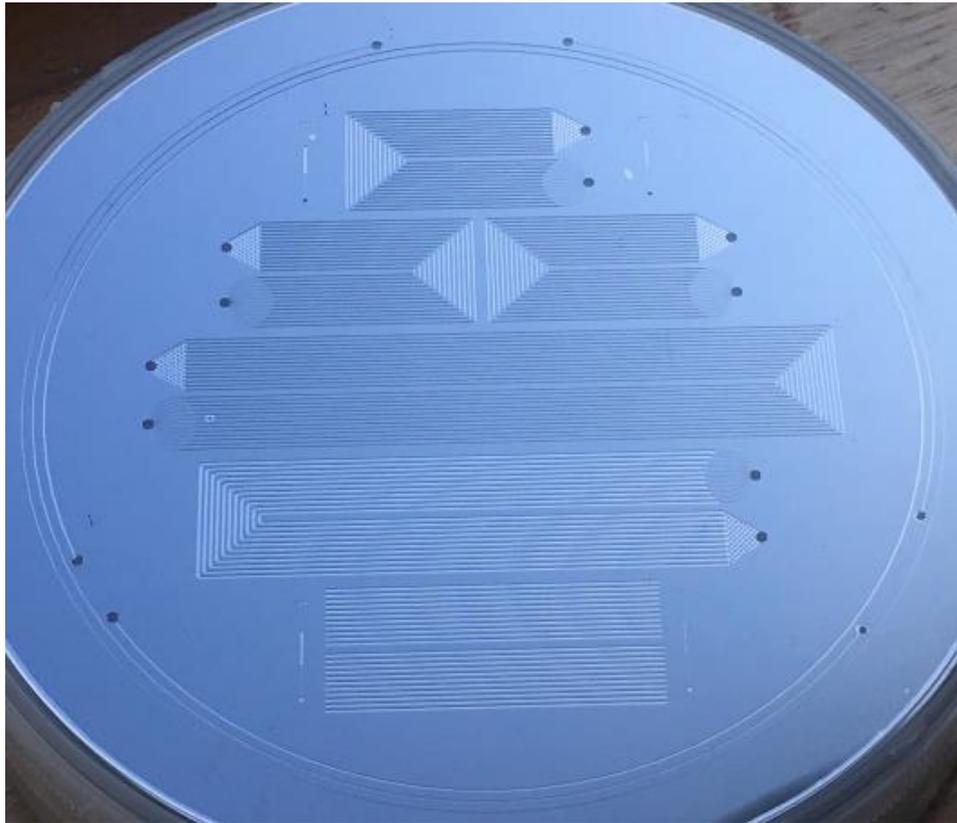
- CO<sub>2</sub> Blow system with by-pass extension
- Temperature and pressure measurements
- High speed camera for flow visualization
- Fixed circular pipe diameter: 250  $\mu\text{m}$
- Fixed evaporation temperature: -20°C
- Measurement matrix:
  - heating power variation
  - mass flow variation
- Next step: silicon multichannel device (provided by CERN)



- Controlled CO<sub>2</sub> recirculation cooling unit
- Test setup fully under vacuum
- Temperature and pressure measurements
- High speed camera for flow visualization
- Infrared camera for thermal visualization
- Deliverable of Task 9.2 due in October 2017 - on schedule.
  
- Study boiling at the microscale level in simple single channels and tubes.
  - Glass, Stainless Steel, Titanium, Silicon
  - Round ID: 0.1 – 1.0 mm
  - Square Dh: 0.1 - 0.8 mm
- Test complex microchannel layouts in silicon substrates designed for HEP experiments.

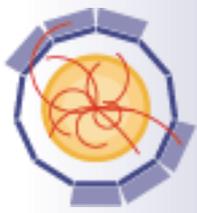


# MICROCHANNELS FOR BI-PHASE CO<sub>2</sub> STUDIES



## SCOPE OF AIDA-2020 TO GATHER THIS KNOWLEDGE AND BRING BUILDING BLOCKS TOGETHER

- **Thermal management** represents a major challenge in HEP.
- Continuous **advances in micro-engineering** have opened the door to the development of smaller and more efficient cooling devices capable of handling increasing power densities with a minimum mass penalty.
- AIDA-2020 is bringing together a **community** to develop and study silicon microchannel cooling devices and low mass mechanical structures .
- By 2019, a catalog of **building blocks, design guidelines and standards for characterization and qualification** of devices and systems for microchannel cooling (single phase and evaporative) will be available.



# AIDA<sup>2020</sup>

## Advanced European Infrastructures for Detectors at Accelerators

**AIDA<sup>2020</sup>**  
Advanced European Infrastructures  
for Detectors at Accelerators  
**ACADEMIA MEETS INDUSTRY**  
*Medical imaging and image processing*  
3-4 April 2017  
CNRS-LPNHE, Jussieu Campus, Paris

The AIDA-2020 project brings together the leading European research infrastructures in the field of detector development and testing and a number of institutes, universities and technological centers, thus assembling the necessary expertise for the ambitious programme of work.

The first Academia meets Industry hosted by CNRS-LPNHE at Université Pierre et Marie Curie in Paris will focus on **Medical Imaging and Image Processing**. This event aims at fostering synergies between detector R&D programmes of AIDA-2020 members and the industry, by creating awareness of strategic R&D topics in academia and of strategic industry needs for which industry-academia collaborations could be envisaged.

**Organising committee:**  
Eberhard Aulry-Hillemanns (CERN)  
David Stasse (CERN)  
Aurilia Pozou (CERN)  
Giovanni Porcellana (CERN)

 <http://cern.ch/aida2020> 

### AIDA-2020 Academia Meets Industry event

- During the AIDA-2020 Annual Meeting
- 3<sup>rd</sup>-4<sup>th</sup> April 2017 (Mon-Tue)
- At CNRS-LPNHE, Jussieu Campus in Paris
- Topic: Medical imaging and image processing

Speaker  
applications  
welcomed

Send applications to:  
[AIDA-2020-info@cern.ch](mailto:AIDA-2020-info@cern.ch)

