

An overview of recent HV-CMOS results

Tuesday 21 February 2017 09:45 (20 minutes)

In accordance with the High-Luminosity upgrade of the LHC (HL-LHC), the current Inner Tracker (ID) of the ATLAS detector will be replaced with an all-silicon sub-detector (ITk upgrade) comprising of pixel and micro-strip silicon sensors. A candidate technology for the outer pixel layers of the ITk is a new radiation hard monolithic pixel silicon sensor, based on High Voltage CMOS technology, allowing for the pixel electronics to be embedded in the silicon sensor itself. Results of the characterisation of a full demonstrator sensor produced in the 350nm process are presented, as part of an overview of recent results of HV-CMOS sensor technologies.

TRACK

CMOS Sensors

Primary author: WESTON, Thomas (Universitaet Bern (CH))

Presenter: WESTON, Thomas (Universitaet Bern (CH))

Session Classification: Session 5: CMOS