

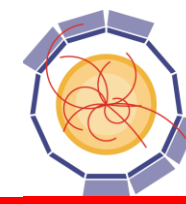
AIDA 2020

# Development and production of silicon micro-channels prototypes: Paris contribution

Marco Bomben, LPNHE & UPD, Paris

AIDA 2020, 2<sup>nd</sup> annual meeting

4-7/4/2017, LPNHE-Paris

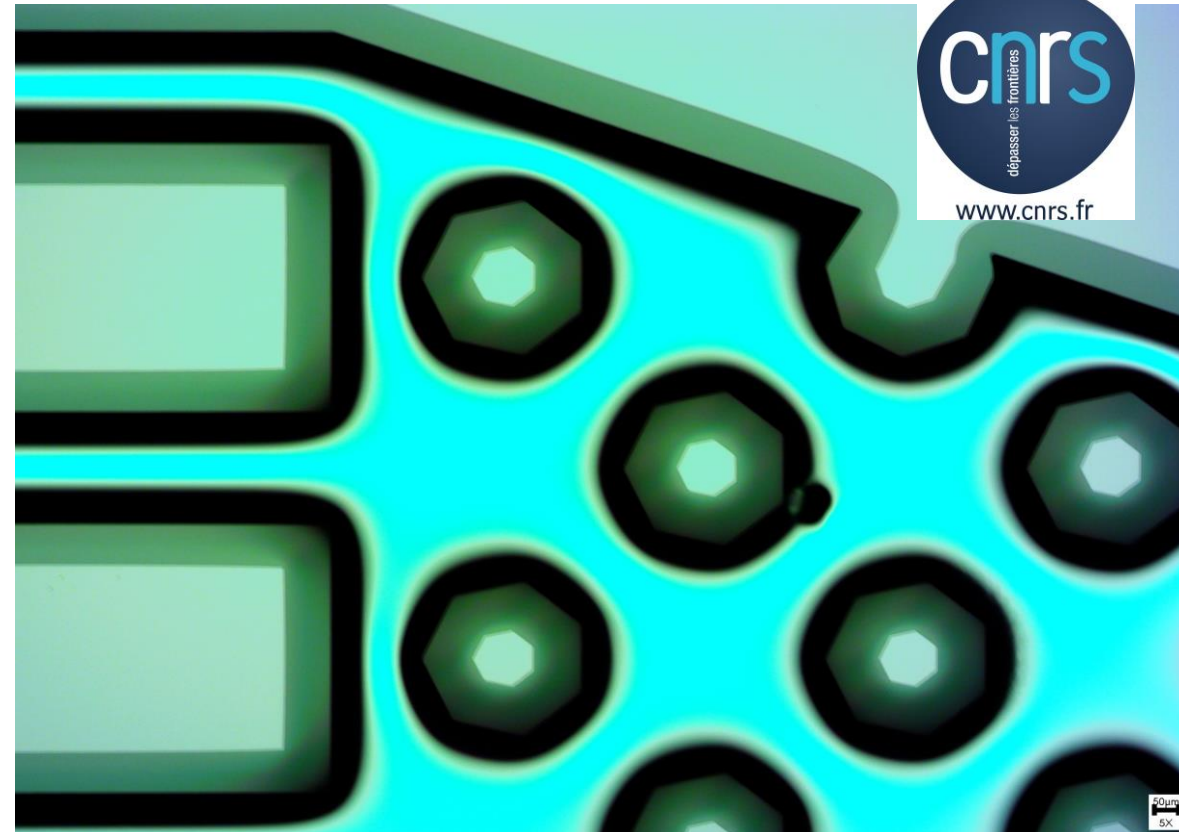


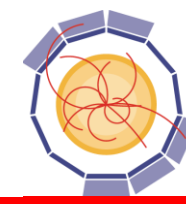
## REFLECS projects

# REFroidissement LÉger au CO<sub>2</sub> en Silicium

Financed by CNRS through the call "L'instrumentation aux limites"

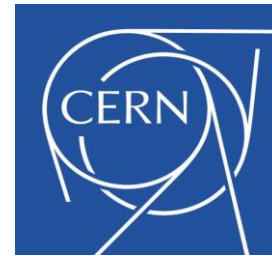
- Reflecs & Reflecs2: partners
- The project
- Actions
- Status
- Conclusion & Outlook





## REFLECS

# REFroidissement LÉger au CO<sub>2</sub> en Silicium

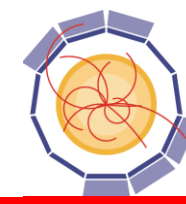


M. Bomben<sup>1</sup>, S. Bortolin<sup>2</sup>, M. Boscardin<sup>3</sup>, G. Calderini<sup>1</sup>, M. Crivellari<sup>3</sup>, J. R. Coudeville<sup>4</sup>, D. Del Col<sup>2</sup>, A. Mapelli<sup>5</sup>, P. Petagna<sup>6</sup>, G. Romagnoli<sup>6</sup>, S. Ronchin<sup>3</sup>

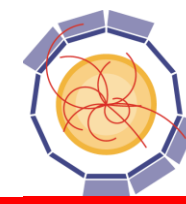
1 LPNHE, Paris – France ; 2 DII, Padova – Italy ; 3 FBK, Trento - Italy; 4 IEF, Orsay – France ; 5 PH-DT-EO (CERN), Meyrin – Switzerland ; 6 PH-DT-DI (CERN), Meyrin - Switzerland



[www.cnrs.fr](http://www.cnrs.fr)



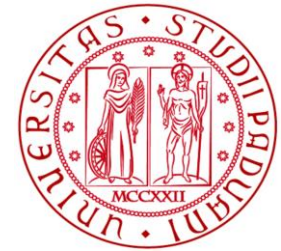
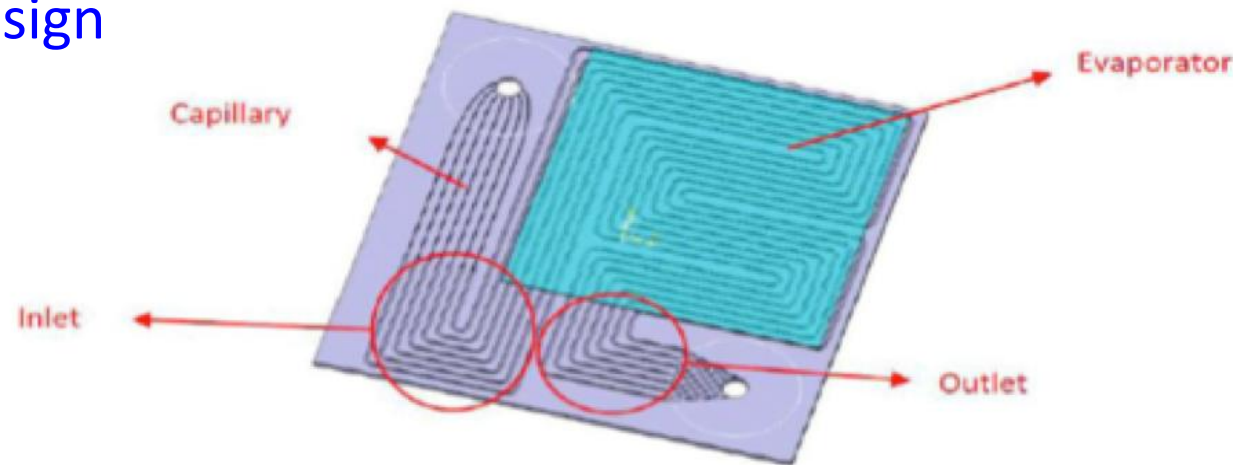
- Two main research axes
    1. Develop cooling building blocks for HEP and more
      - Not only using CO<sub>2</sub> (*e.g.* R1234ze(E))
    2. Study fundamental properties of 2-phase fluids in micro-channels
      - In particular in high pressure\* regime
- \* 30-100 bars: lack of data and models in literature



- Two main research axes

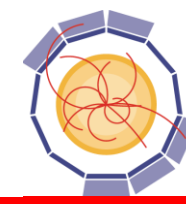
## 1. Develop cooling building blocks for HEP and more

- Not only using CO<sub>2</sub> (e.g. R1234ze(E))
- SNAKE design



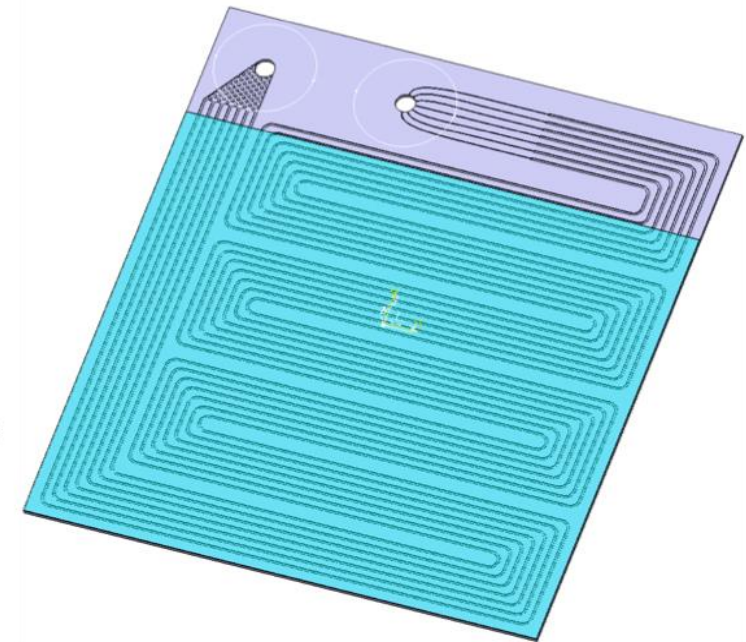
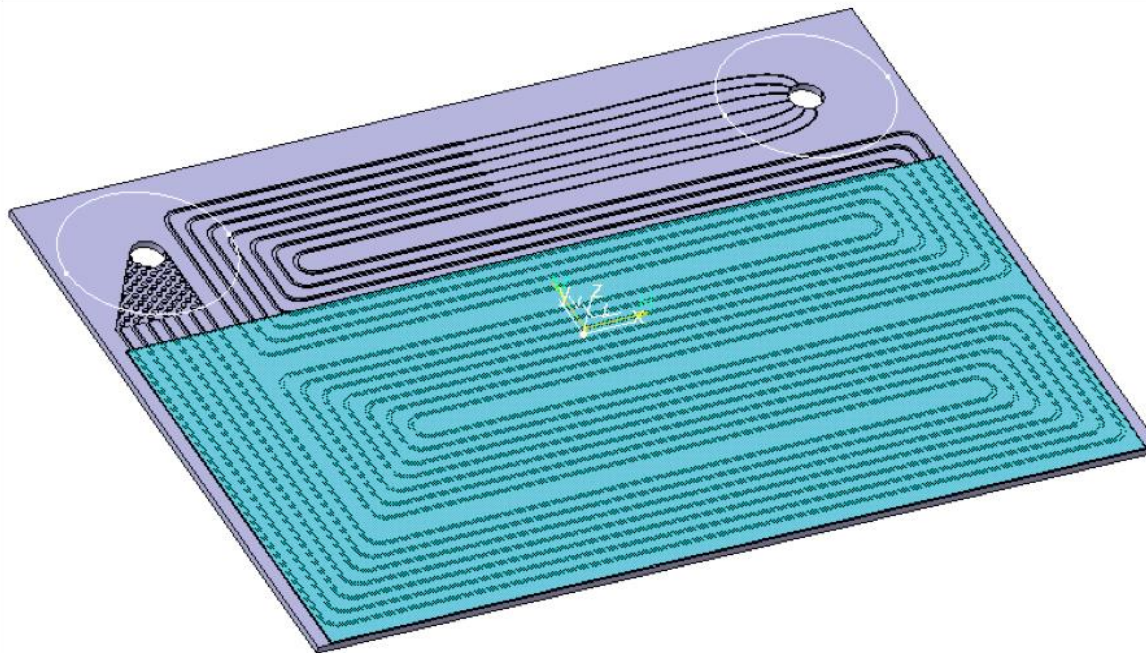
Dimensions optimized  
thanks to DII-Padua  
colleagues

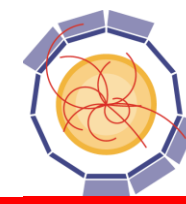
7 parallel 120  $\mu\text{m}$  deep, 200  $\mu\text{m}$  wide channels,  
with a 700  $\mu\text{m}$  pitch.



## “Snake” design microchannel cooling unit size

Design	Width [mm]	Height [mm]
Single	30.0	30.0
Double	30.0	41.3
Quad	42.4	48.0





- Two main research axes

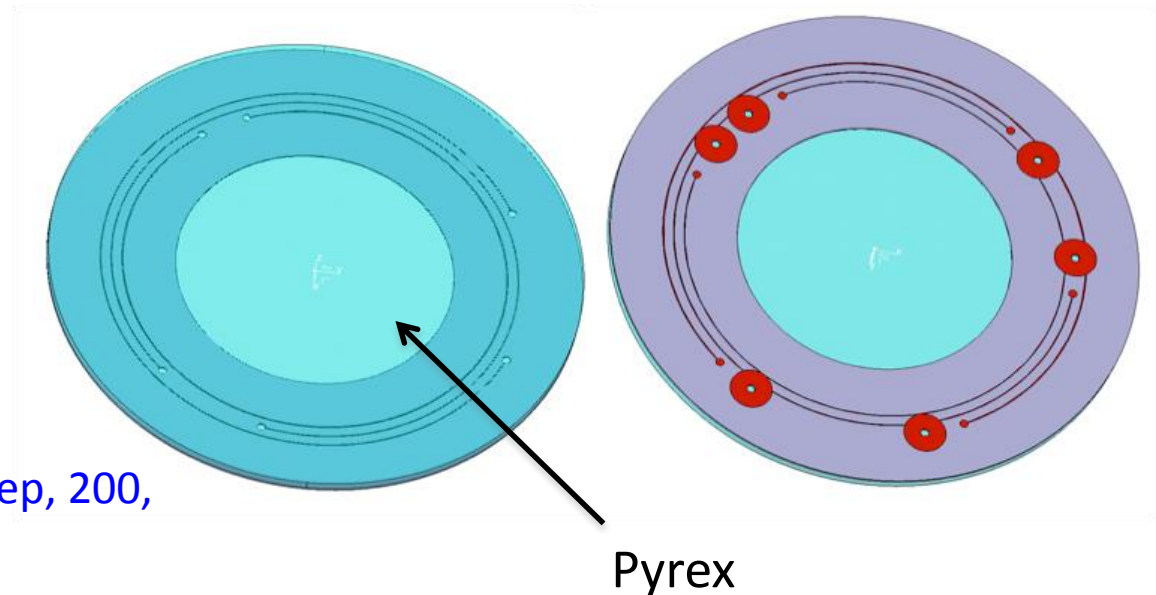
## 2. Study fundamental properties of 2-phase fluids in micro-channels

2. In particular in high pressure\* regime
3. 30-100 bars: lack of data and models in literature

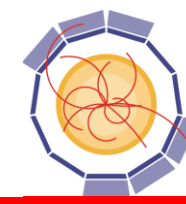
- Doughnut design

front view

back view



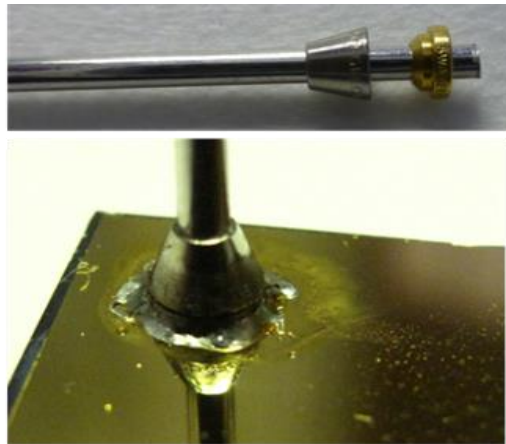
Three concentric circular channels, 200  $\mu\text{m}$  deep, 200, 150 and 100  $\mu\text{m}$  wide



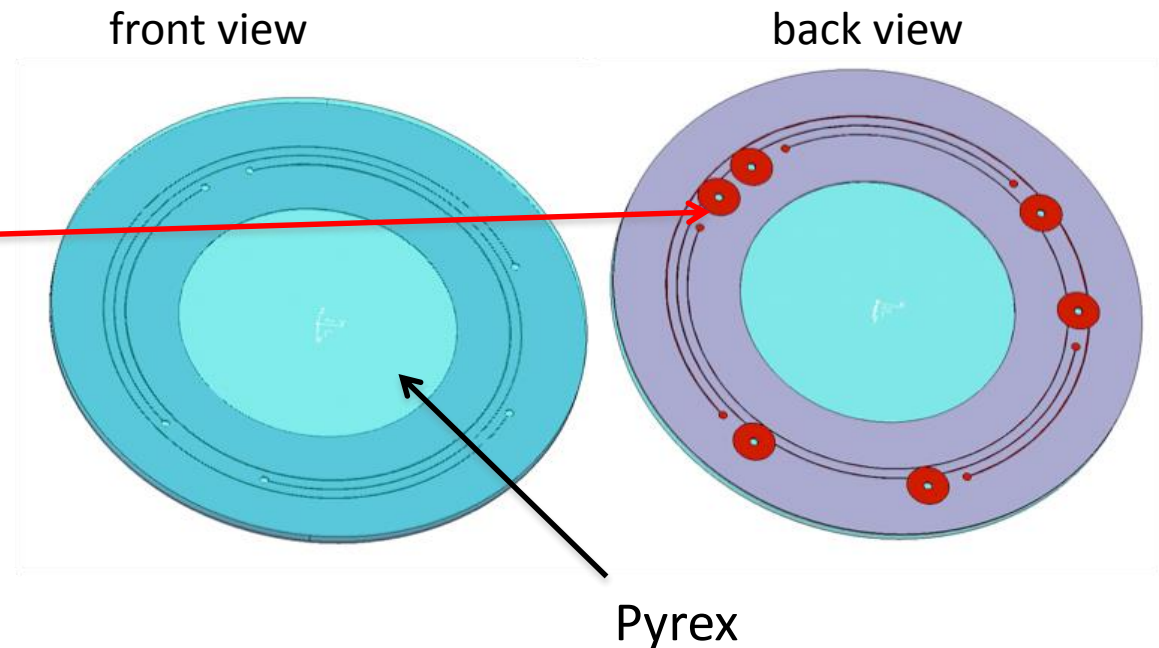
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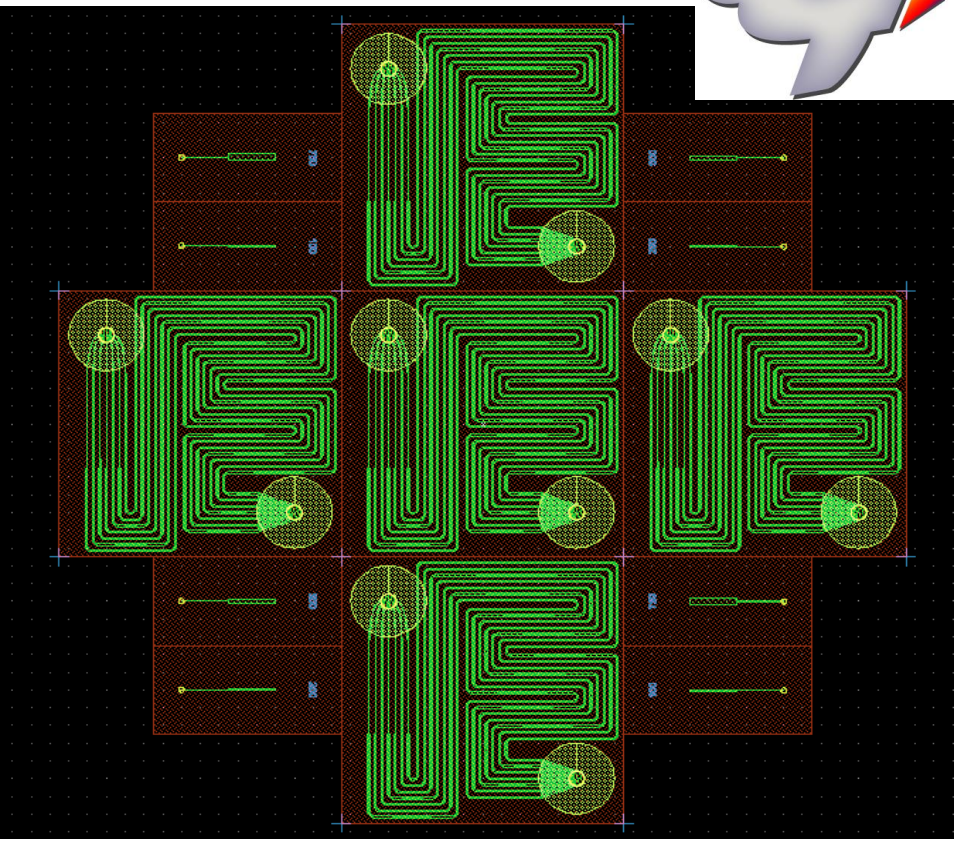
External connection for tests





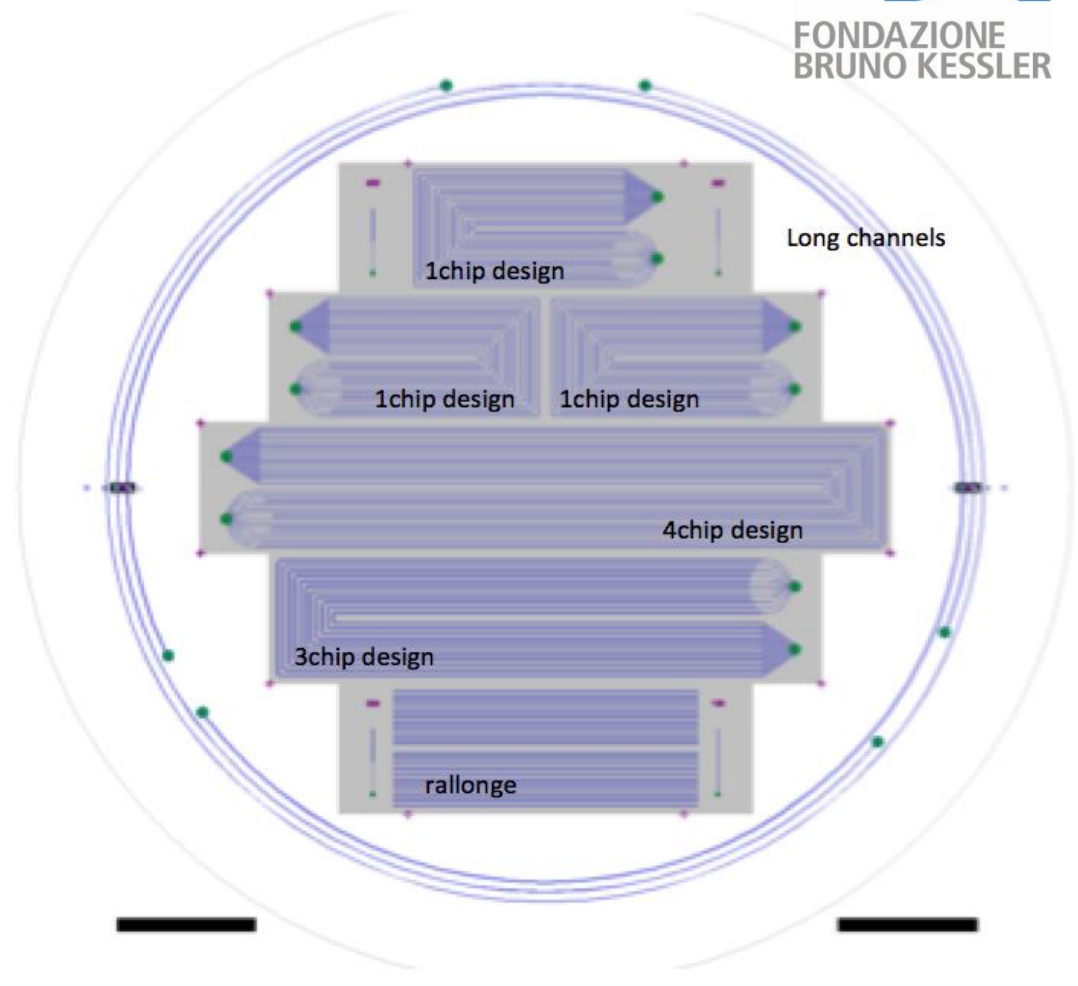
## Two productions of silicon $\mu$ -channels

- IEF, Orsay



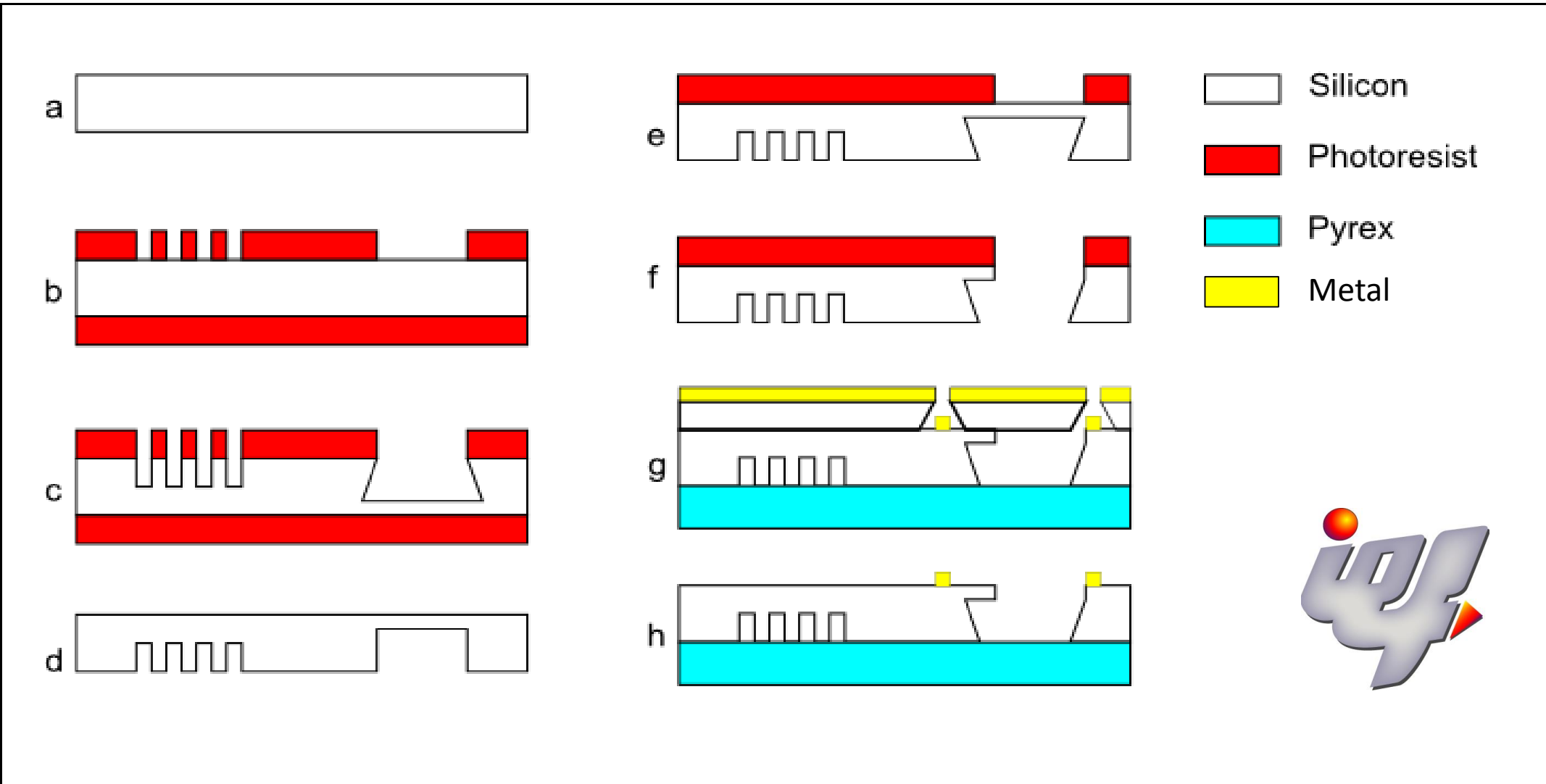
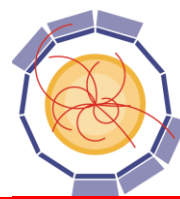
- On 4" wafers
  - Single channel wafers too

- FBK, Trento – Italy

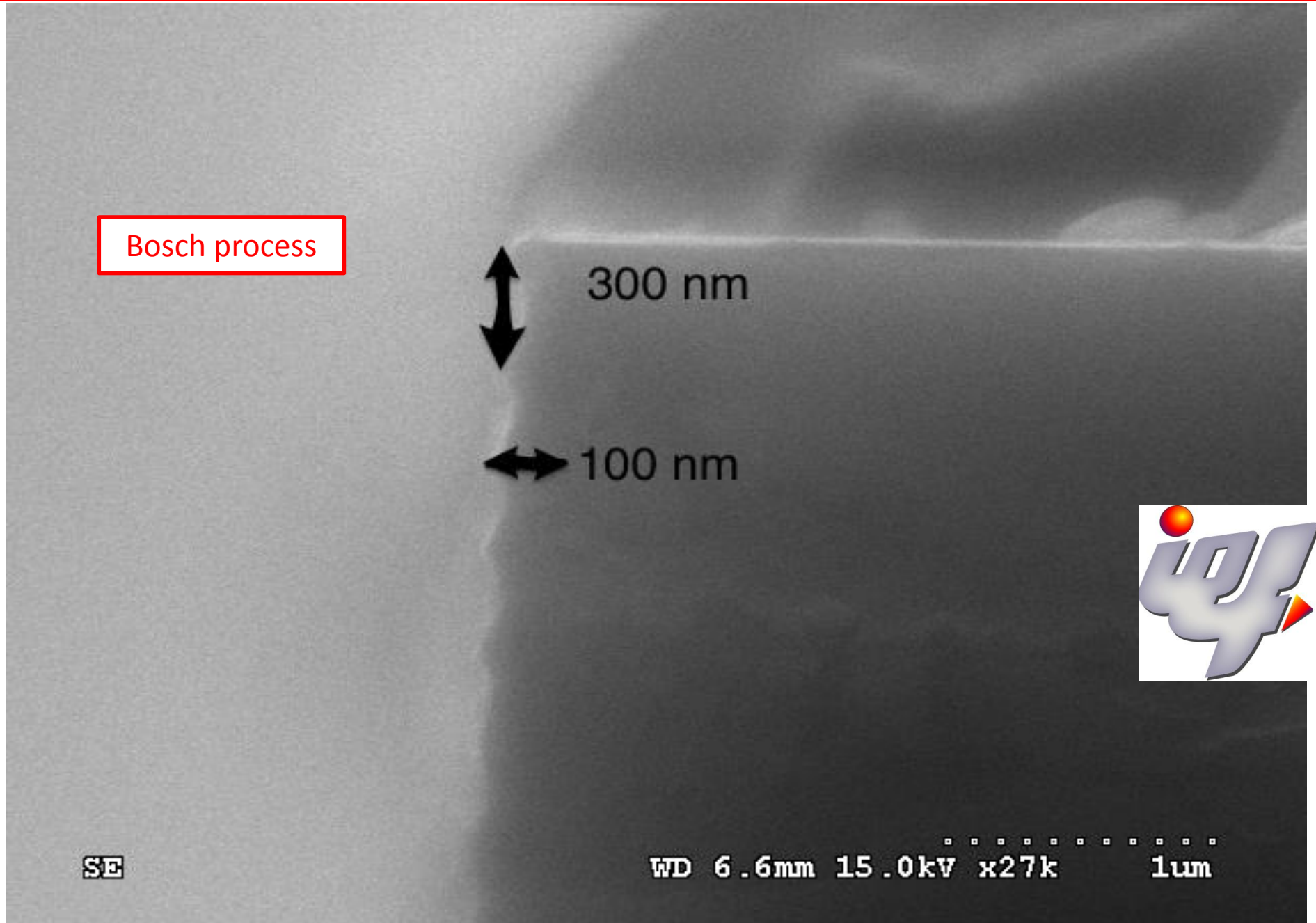
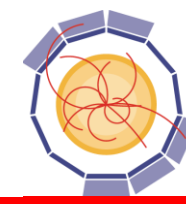


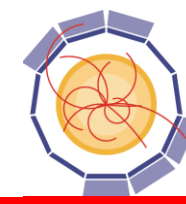
- On 6" wafers

# Fabrication process



Similar at FBK



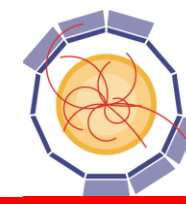


Boiling  
influenced by  
surface  
roughness



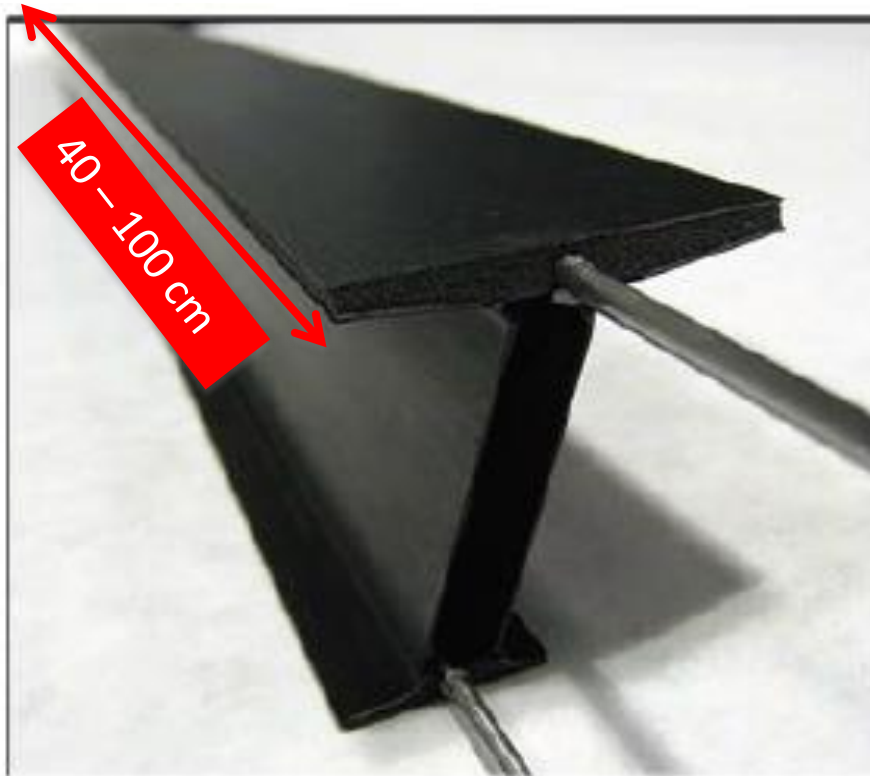
SE

WD 8.7mm 15.0kV x32k 1um

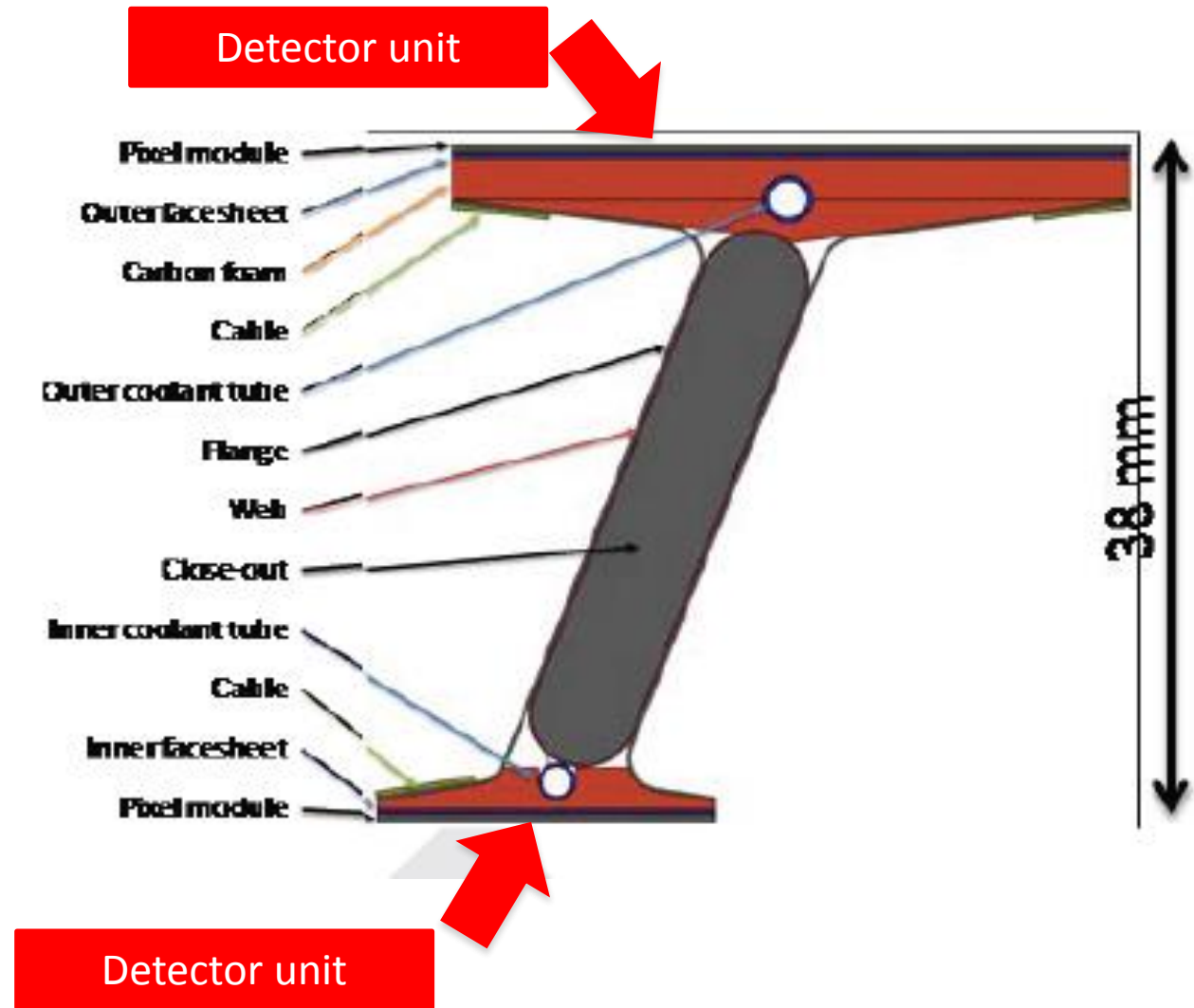


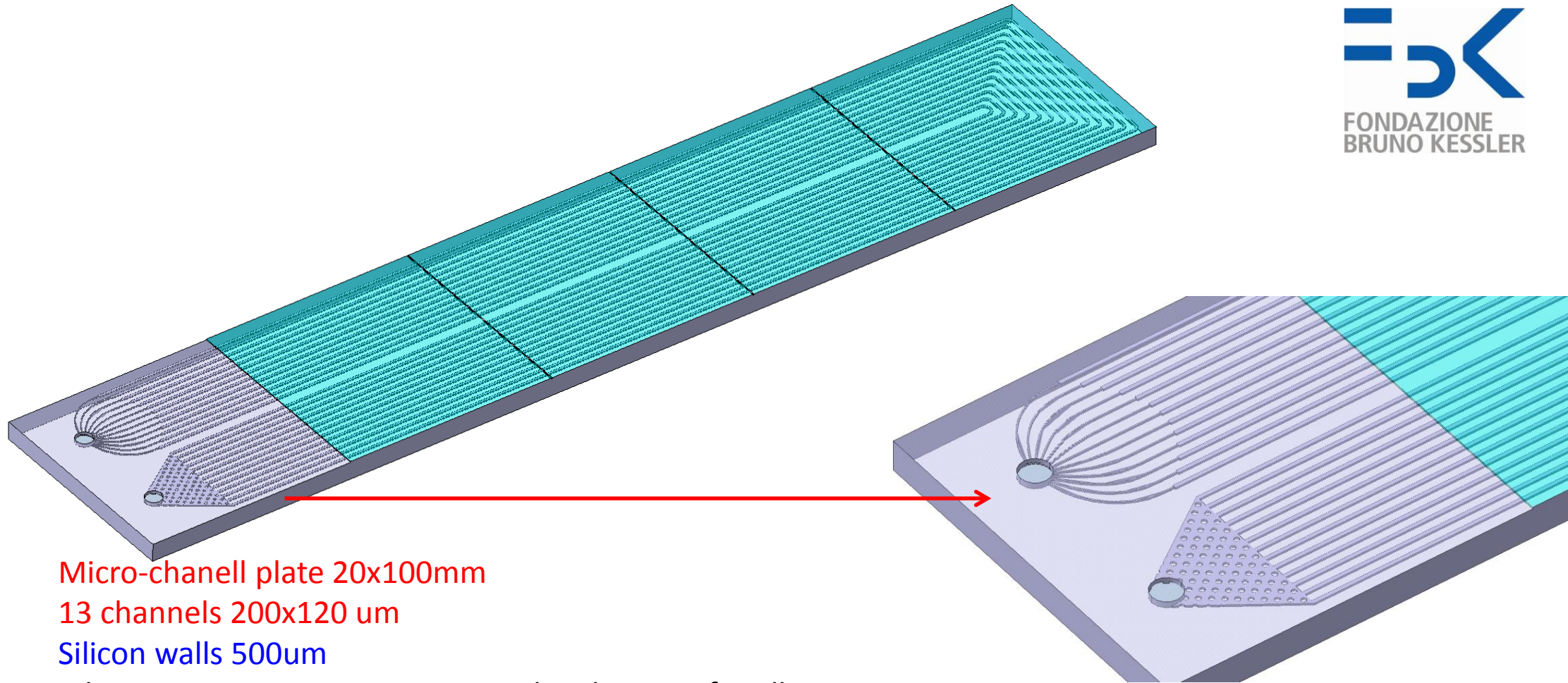
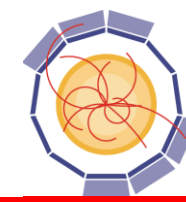
# ATLAS new stave

- Stave example



- 40-100 cm long





Micro-channel plate 20x100mm

13 channels 200x120  $\mu\text{m}$

Silicon walls 500 $\mu\text{m}$

Inlet restrictions 60x120 $\mu\text{m}$ : same length 6mm for all

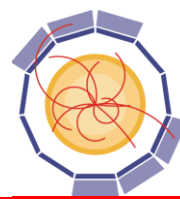
Inlet outlet holes 1.6 mm diameter

Pillars in the outlet: 350 $\mu\text{m}$  diameter

Shortest channel: 165 mm

Longest channel: 199 mm

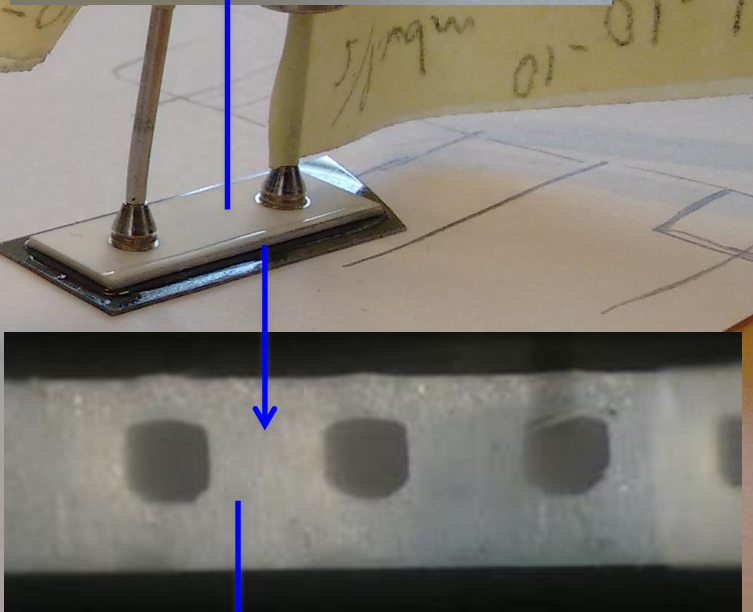
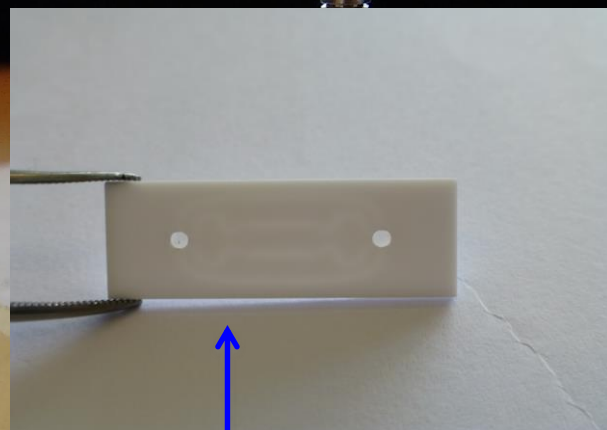
# Ceramic connectors (and microchannels)



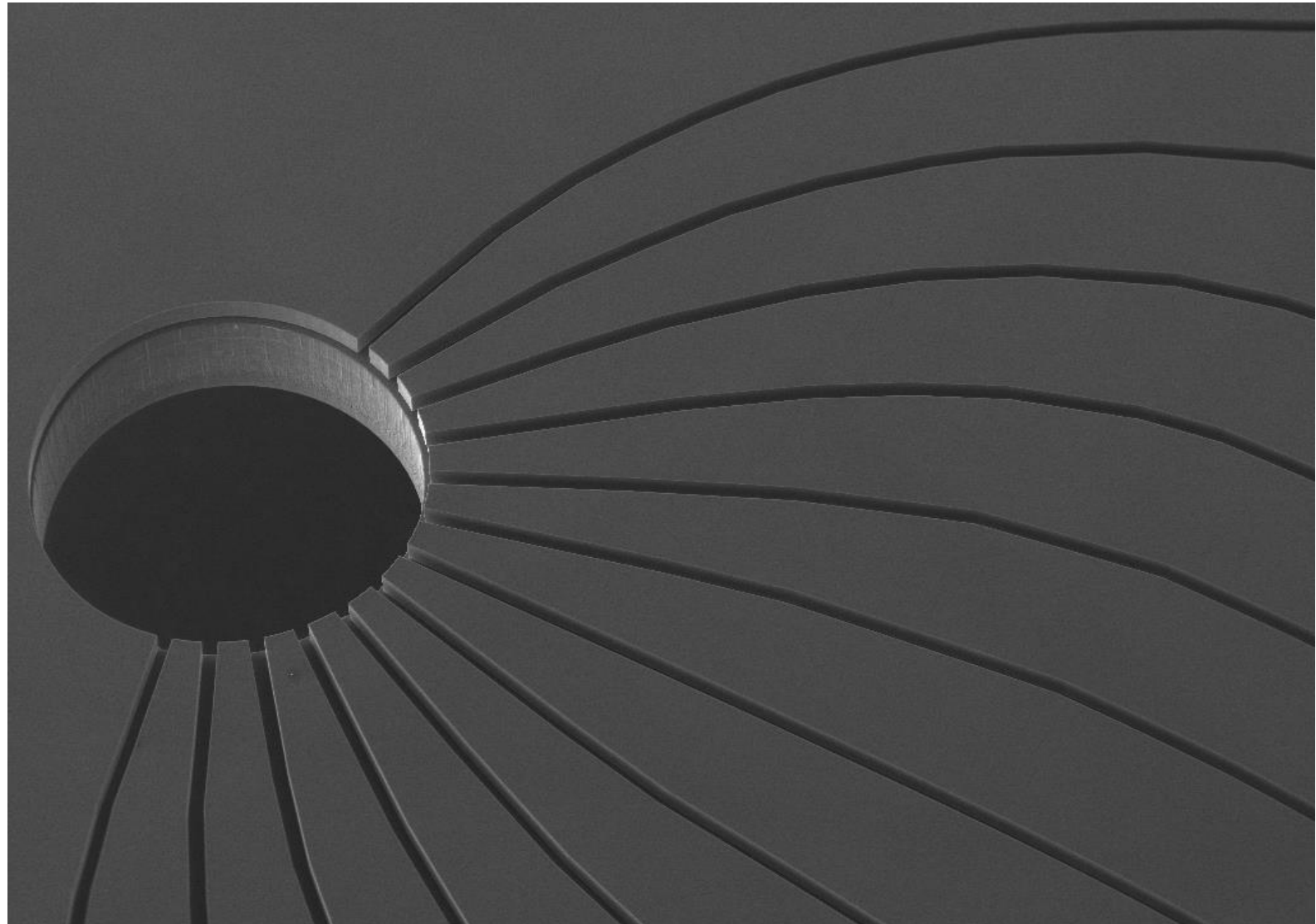
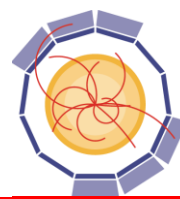
Ceramic connector


Now at LPNHE too!

Silicon micro-channels

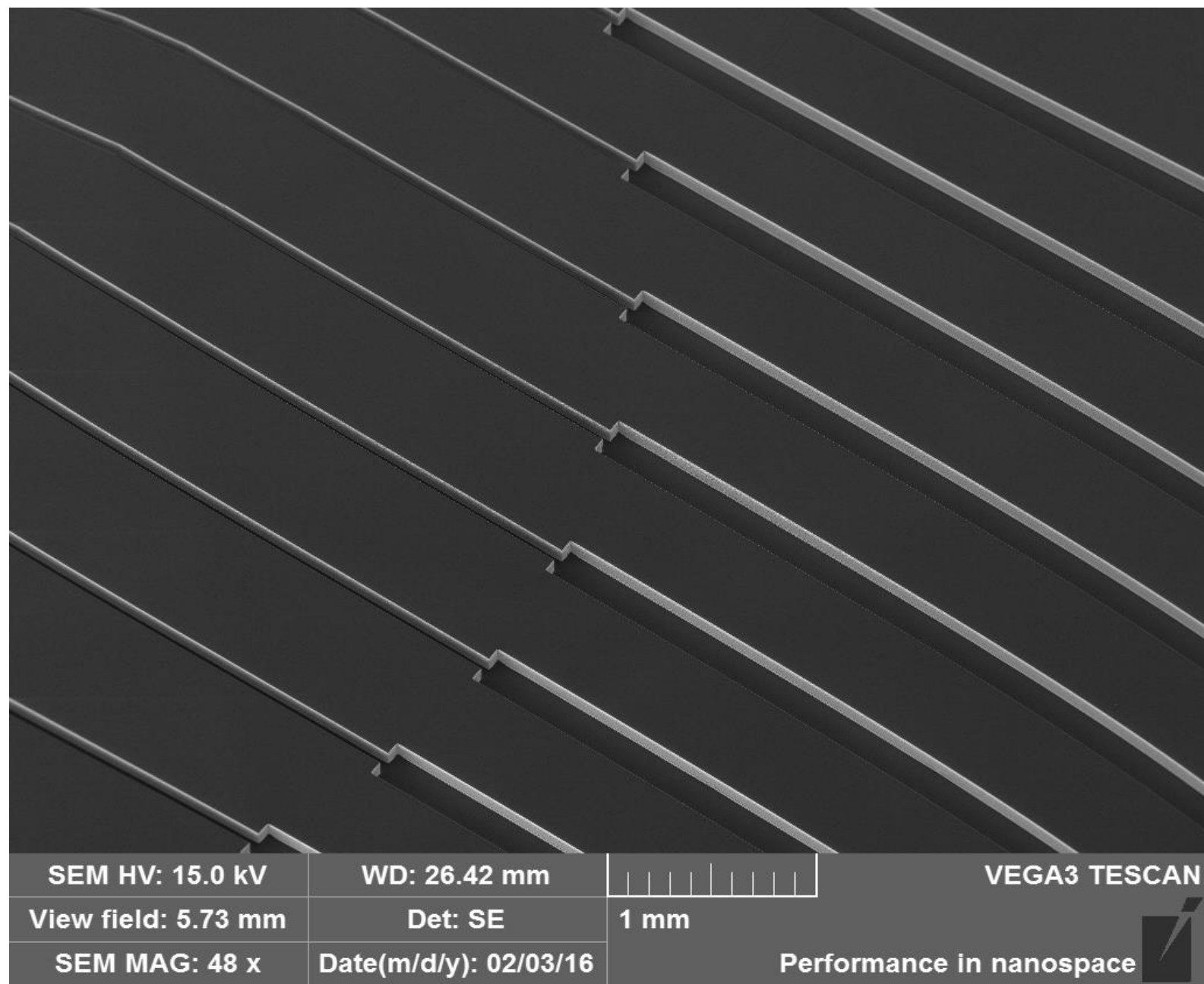
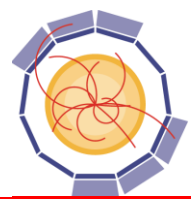


# Pictures from FBK - inlet

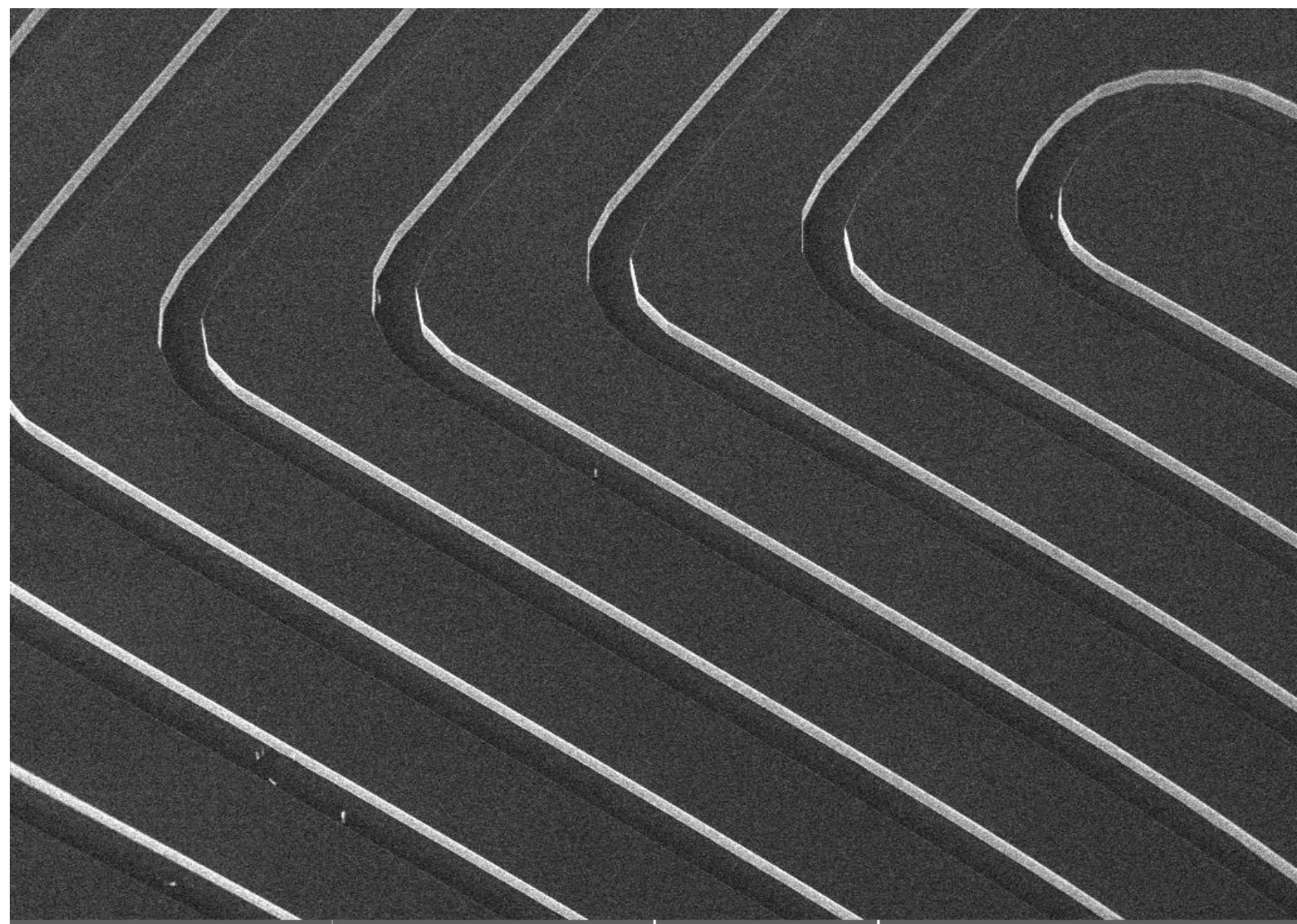
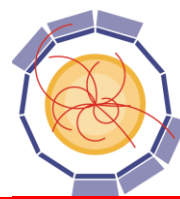




SEM HV: 30.0 kV	WD: 24.21 mm		VEGA3 TESCAN
View field: 5.33 mm	Det: SE		
SEM MAG: 52 x	Date(m/d/y): 03/10/16	Performance in nanospace 	



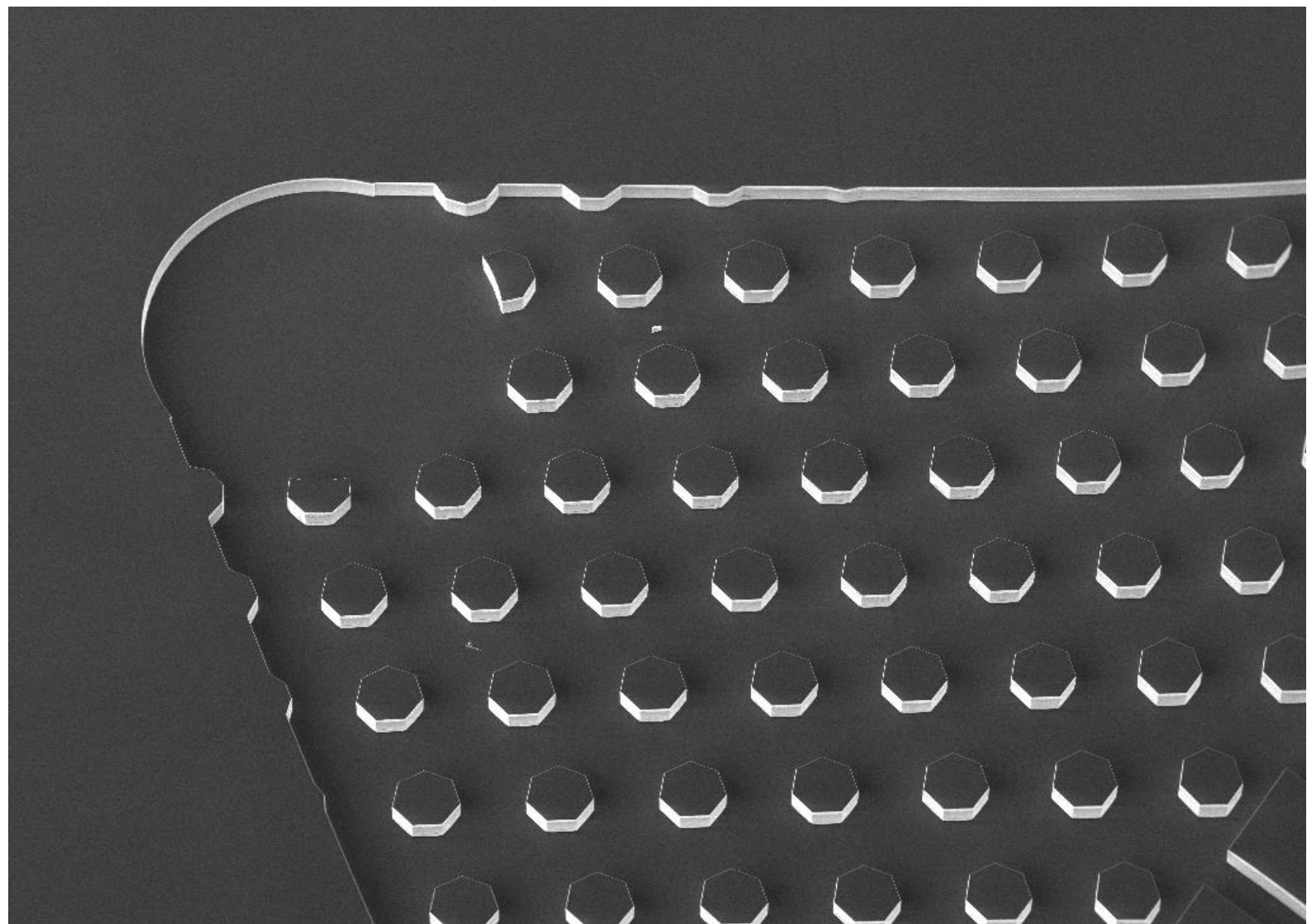
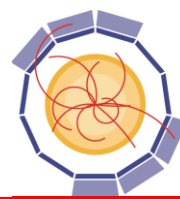


# Pictures from FBK – curved channels

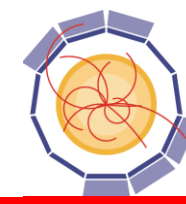


SEM HV: 15.0 kV	WD: 26.51 mm		VEGA3 TESCAN
View field: 5.74 mm	Det: SE		
SEM MAG: 48 x	Date(m/d/y): 02/03/16	Performance in nanospace 	

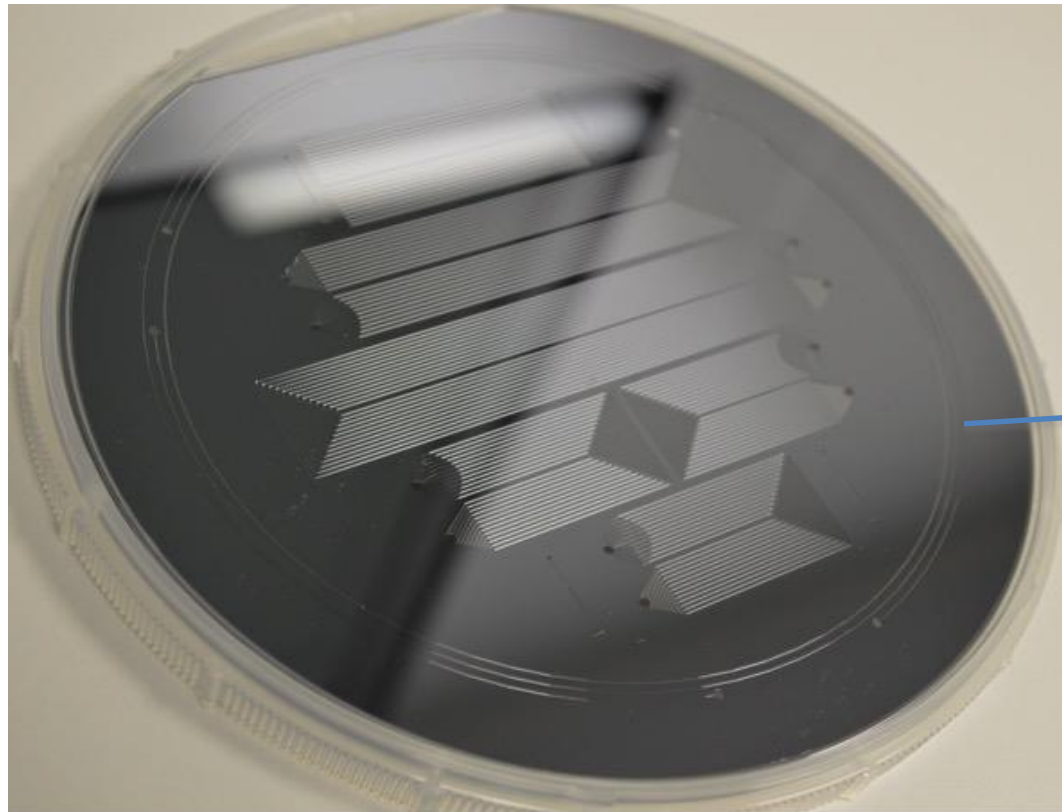
# Pictures from FBK - outlet

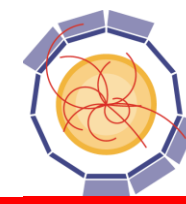


SEM HV: 15.0 kV	WD: 26.52 mm	VEGA3 TESCAN
View field: 5.75 mm	Det: SE	1 mm
SEM MAG: 48 x	Date(m/d/y): 02/03/16	Performance in nanospace

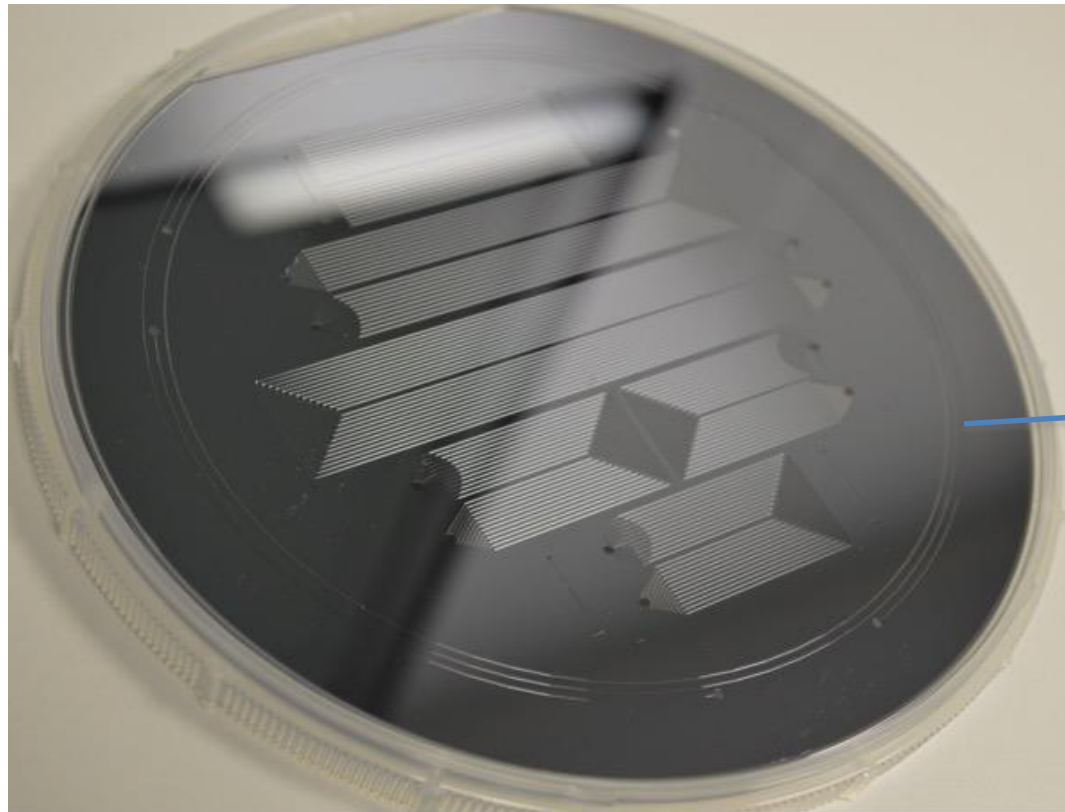


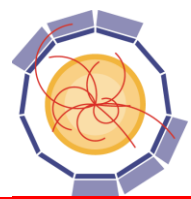
- First three wafers received
- Metal deposition (TiNiAu) on squared devices and inlets: for heating purposes and soldering the connectors
- with hard mask
- Cutting of the donut



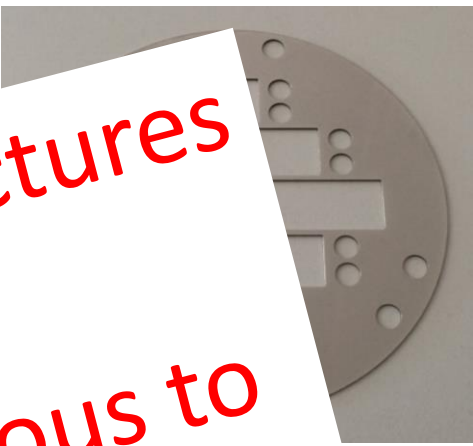


- First three wafers received
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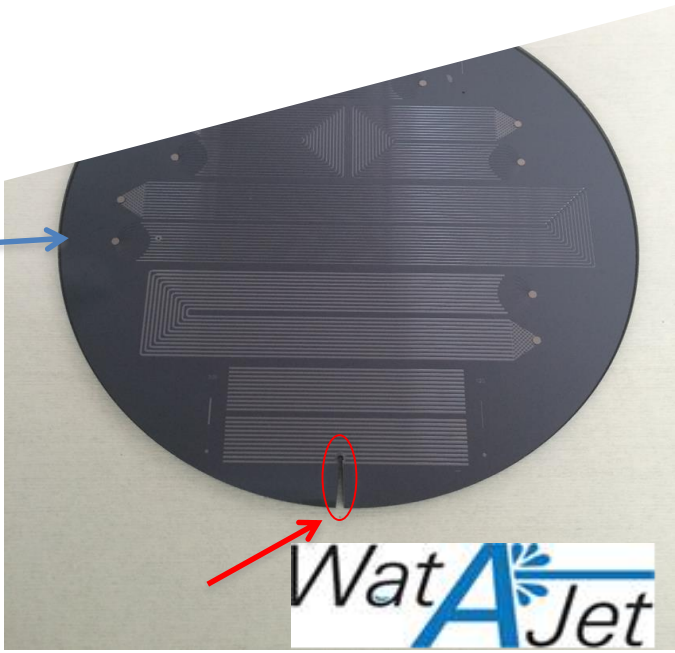




- First three wafers received
- Metal deposition (TiNiAu) on squared devices and inlets: for heating purposes and soldering the connectors
- with hard mask
- Cutting of the donut



**Lesson learned: always send clear pictures of all features, never rely on understanding of what seems obvious to you.**



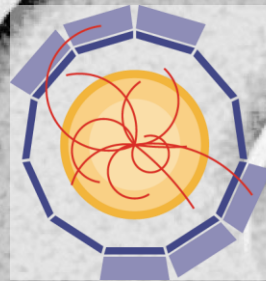
- First  $\mu$ -channel production being completed at IEF
- New  $\mu$ -channel production started at FBK
- Ceramic microfluidic connectors received
- Miscellanea: metallic connectors, thermic and pressure sensors for pressure tests received
- Now: assembly first modules to test basic properties
- Then: acquire data on flowing and boiling in microchannels to build new numeric models

# Conclusions

---

- The REFLECS & REFLECS2 projects study a microchannel based cooling solution
- It assures:
  1. high thermal performance
  2. high uniformity
  3. low material budget
  4. virtually no CTE mismatch.
- Within the project microchannel cooling units have been designed and produced.
- The next steps will involve the thermal tests on these units, the development of a numerical method for the high-pressure two-phase fluid flow in microchannels and the study of high-pressure rated connections among many units





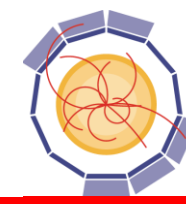
**AIDA** 2020

**THANK YOU FOR  
YOUR ATTENTION**

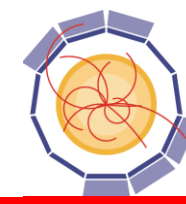
Reflecs & Reflecs2

Marco Bomben, LPNHE & UPD, Paris

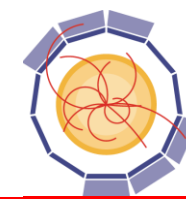




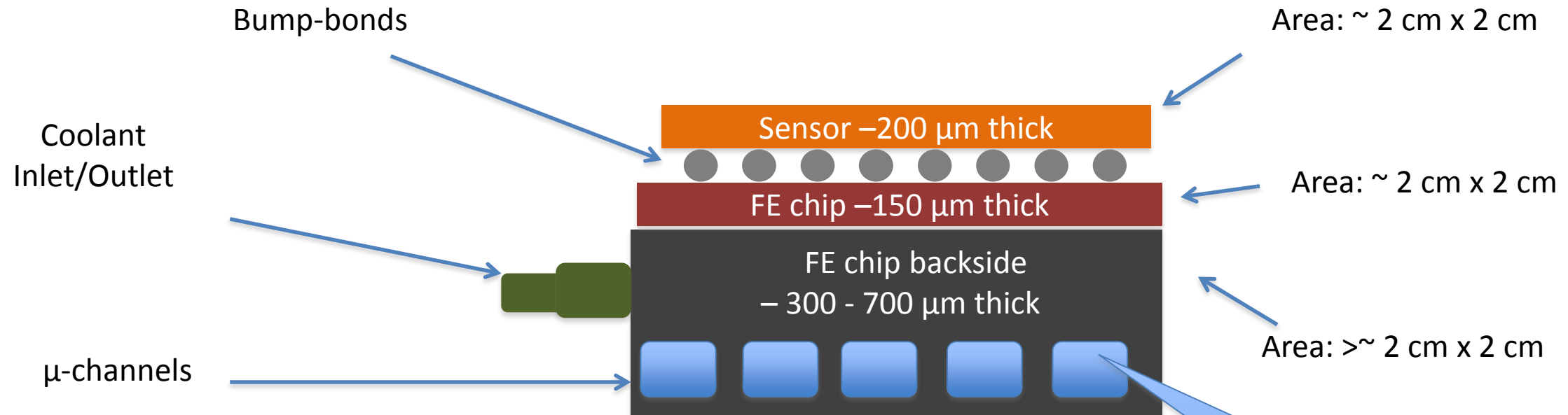
- **@Future Hadronic Colliders**
- To avoid negative annealing **future sensors must be operated** well below  $0^{\circ} \text{ C} \rightarrow$  **ideally:  $-20^{\circ} \text{ C}$**
- **Sensor+chip will dissipate  $\sim \text{W/cm}^2$**
- A very efficient cooling system is needed
- Important **constraint: very low material budget** envisaged ( $< 1\% X_0$ )
- **Space-Born experiments**
- Silicon detectors require:
  - a high degree level of **temperature homogeneity** across the apparatus
  - a **cooling system** capable of **working for several years without** possibility of **intervention** for the **many years** of the space mission



- **@Future Hadronic Colliders**
  - To avoid negative annealing **future sensors must be operated** well below  $0^{\circ}\text{C}$   $\rightarrow$  **ideally:  $-20^{\circ}\text{C}$**
  - Sensor+chip will dissipate  $\sim$   $\text{W}/\text{cm}^2$
  - A very efficient cooling system is needed
  - Important **low**
  - **Space-Born experiments**
  - Silicon detectors require
  - a high degree of **temper**
- Promising solution: micro-channel based cooling using  $\text{CO}_2$  system** capable of **operating for several years without possibility of intervention for the many years of the space mission**



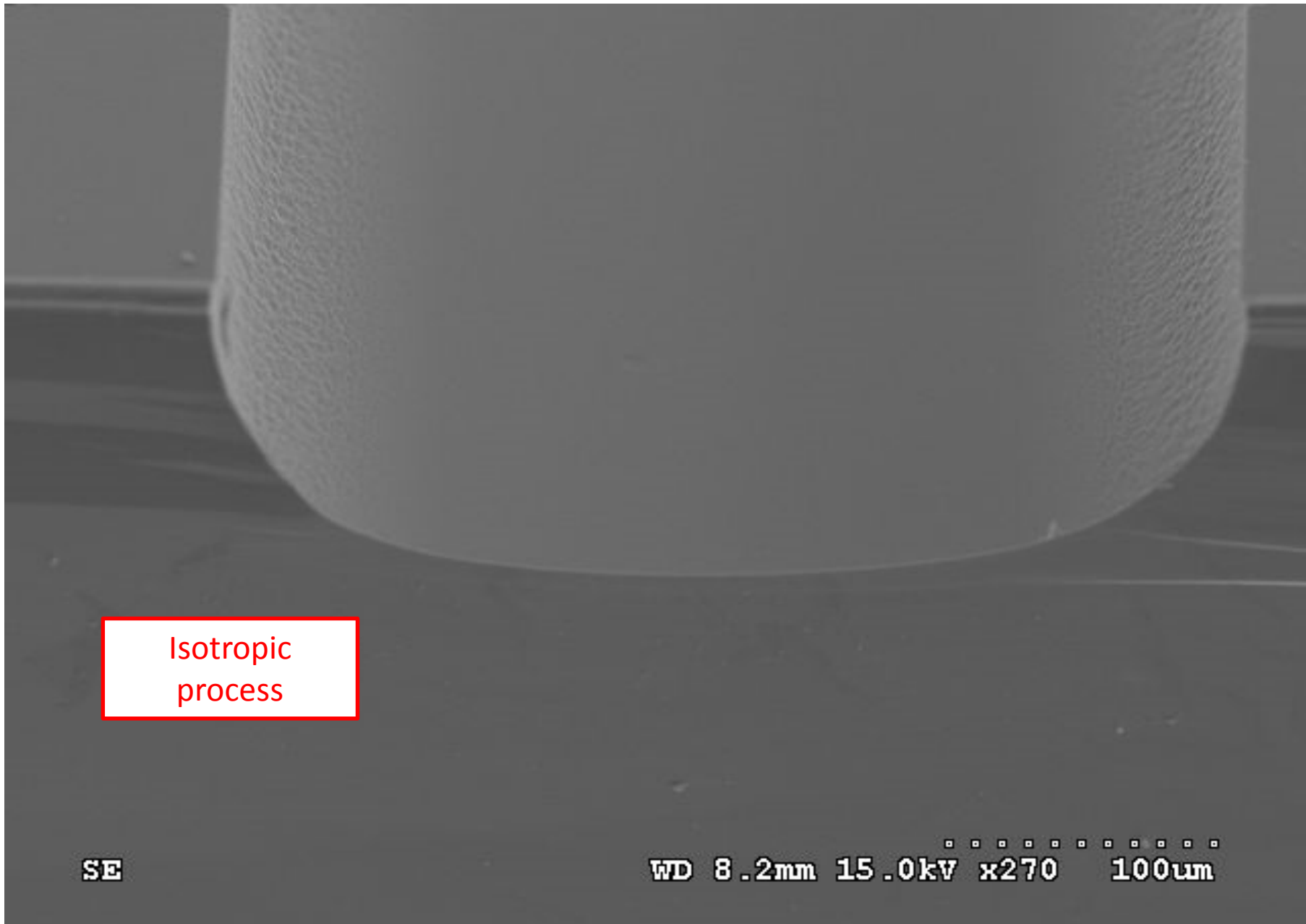
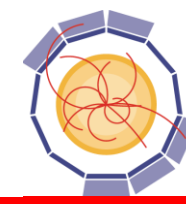
# The $\mu$ -channels solution



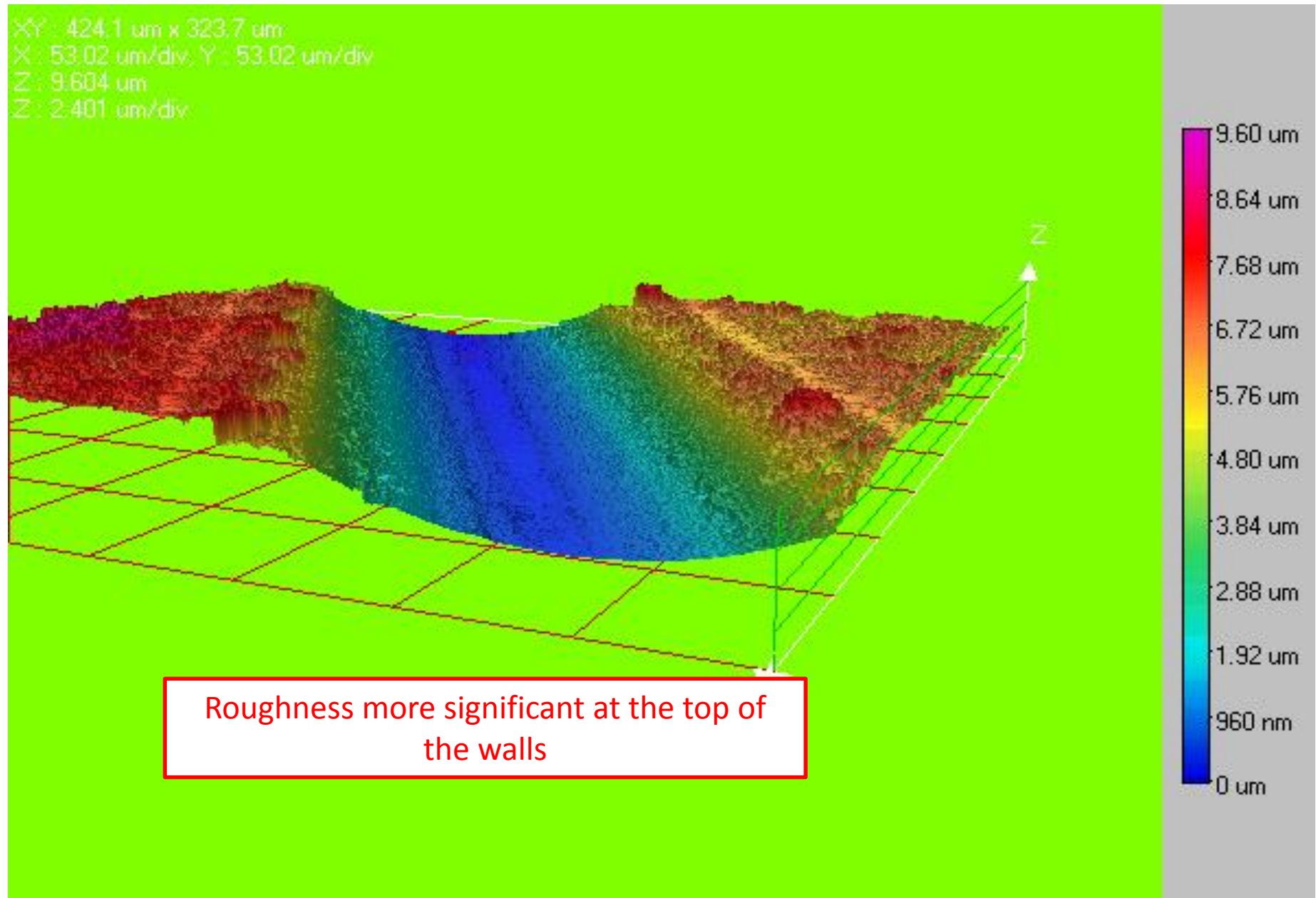
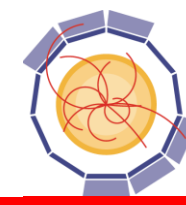
## Silicon-only system:

- ✓ No mechanical stress due to CTE mismatch
- ✓ Low mass
- ✓ Customizable layout of the channels → more efficient and uniform cooling

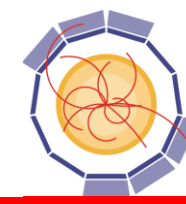
▪ Very important to avoid thermal runaway after irradiation



# One channel – roughness measurement



# Three circular channels: section



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