Session Program

4–7 Apr 2017

AIDA-2020 Second Annual Meeting

WP4 Micro-electronics and interconnections

LPNHE
LPNHE - Barre 12-22, 1er étage - 4 place Jussieu 75252 PARIS CEDEX 05

Thursday 6 April

09:00

WP4 Micro-electronics and interconnections: Micro-electronics and interconnections
Session | **Location:** LPNHE, Amphi Charpak | **Conveners:** Christophe De La Taille, Valerio Re, Valerio Re

09:00–09:10 **Introduction**

Speaker
Valerio Re

09:10–09:30 **Design and submission of the RD53A chip**

Speaker
Luigi Gaioni

09:30–09:45 **AIDA/RD53 activities in Perugia**

Speakers
Sara Marconi, sara marconi

09:45–10:00 **The CHIPIX65 demonstrator chip**

Speaker
Lino Demaria

10:00–10:15 **Update on CPPM activities towards development in 65nm**

Speakers
Alexandre Rozanov, Mohsine Menouni

10:15–10:30 **Update on 65nm developments in Bonn**

Speaker
Hans Krueger

10:30

11:00

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11:00–11:15 **A high precision TDC for CMS MG-RPC in TSMC130n**

Speaker
Herve Mathez

11:15–11:30 **developments at OMEGA and DESY/Heidelberg**

Speaker
Nathalie Seguin-Moreau

11:30–11:45 **developments at AGH Krakow**

Speaker
Marek Idzik

11:45–12:00 **discussion on 130 nm WP4.3**

12:00–12:15 **TSV developments at UBonn**

Speakers
Fabian Huegging, Fabian Huegging

12:15–12:30 **TSV developments in Glasgow**

Speakers
Richard Bates, Richard Bates

12:30