# TWEPP 2017 Topical Workshop on Electronics for Particle Physics

## Monday, 11 September 2017

### <u>ASIC</u> - Earth and Marine Sciences (E&MS) Building (16:30 - 18:10)

-Conveners: Christophe De La Taille

| time | [id] title | presenter |
|------|-----------|-----------|
| 16:30 | [21] CBC3: a CMS Micro-Strip Readout ASIC with Logic for Track-Trigger Modules at HL-LHC | PRYDDERCH, Mark |
| 16:55 | [3] A 2.56 GHz Radiation Hard Phase Locked Loop ASIC for High Speed Serial Communication Links | PRINZIE, Jeffrey |
| 17:20 | [16] Characterization of a 9-Decade Femtoampere ASIC Front-End for Radiation Monitoring | VOULGARI, Evgenia |
| 17:45 | [58] An 8-Channel ASD in 130 nm CMOS with Superior Performance of Rise Time, Noise and Threshold Uniformity for ATLAS Drift Tube Readout at the HL-LHC | FRAS, Markus |

# Tuesday, 12 September 2017

### ASIC - Earth & Marine Sciences (E&MS) Building (11:05 - 12:45)

-**Conveners: Angelo Rivetti**

| time | [id] title | presenter |
|------|-----------|-----------|
| 11:05 | [77] Development of a Large Pixel Chip Demonstrator in RD53 for ATLAS and CMS Pixel Upgrades | CONTI, Elia |
| 11:30 | [62] ALTIROC0, a 20 Pico-Second Time Resolution ASIC for the ATLAS High Granularity Timing Detector (HGTD) | DE LA TAILLE, Christophe |
| 11:55 | [85] A 2 Gsps Waveform Digitizer ASIC in CMOS 180 nm Technology | Dr QIN, Jiajun |
| 12:20 | [70] MuTRiG: A Silicon Photomultiplier Readout ASIC with High Timing Precision and High Event Rate Capability | SCHIMANSKY, David |

### ASIC - Earth & Marine Sciences (E&MS) Building (14:50 - 16:05)

-**Conveners: Marcus Julian French**

| time | [id] title | presenter |
|------|-----------|-----------|
| 14:50 | [143] The SALT, a 128-Channel Readout ASIC for Upstream Tracker in the Upgraded LHCb Experiment | SWIENTEK, Krzysztof Piotr |
| 15:15 | [91] Test Vehicles for CMS HGCAL Readout ASIC | THIENPONT, Damien |
| 15:40 | [93] Results from CHIPIX-FE0, a Small Scale Prototype of a New Generation Pixel Readout ASIC in 65nm CMOS for HL-LHC | PACHER, Luca |

# Wednesday, 13 September 2017

<u>ASIC</u> **- Earth & Marine Sciences (E&MS) Building (11:05 - 12:45)**

  **-Conveners: Ping Gui**

| time | [id] title | presenter |
|------|-----------|-----------|
| 11:05 | [161] Laboratory and Beam Test Results of TOFFEE ASIC and Ultra Fast Silicon Detectors | ARCIDIACONO, Roberta |
| 11:30 | [152] Design and Characterisation of the Monolithic Matrices of the H35DEMO Chip | CASANOVA MOHR, Raimon |
| 11:55 | [151] KlauS4: A Multi-Channel Silicon-Photomultiplier Charge Readout ASIC in 0.18 µm UMC CMOS | STANKOVA, Vera |
| 12:20 | [153] A high-Precision Timing ASIC for TOF-PET Applications | VALERIO, Pierpaolo |

# Thursday, 14 September 2017

**ASIC - Earth & Marine Sciences (E&MS) Building (11:05 - 12:45)**

-**Conveners: Christophe De La Taille**

| time | [id] title | presenter |
|------|------------|-----------|
| 11:05 | [174] Design and Characterization of the Readout ASIC for the BESIII CGEM Detector | COSSIO, Fabio |
| 11:30 | [136] Development of Depleted Monolithic Pixel Sensors in 150 nm CMOS technology for the ATLAS Inner Tracker Upgrade | RYMASZEWSKI, Piotr |
| 11:55 | [172] FE65-P2: a Pixel Prototype Readout Chip in 65nm Technology for HL-LHC Upgrades | HEIM, Timon |
| 12:20 | [177] Monolithic Pixel Development in 180 nm CMOS for the Outer Pixel Layers in the ATLAS Experiment | KUGATHASAN, Thanushan |