TWEPP 2017 Topical Workshop on Electronics for Particle Physics

Monday, 11 September 2017

Trigger - **Thimann I Lecture Hall (14:50 - 16:05)**

-Conveners: Philippe Farthouat

time	[id] title	presenter
14:50	[61] An FPGA-Based Track Finder for the L1 Trigger of the CMS Experiment at HL-LHC	CIERI, Davide
15:15	[23] The ATLAS Muon-to-Central Trigger Processor Interface for the Phase-I Muon Trigger Upgrade	VICHOUDIS, Paschalis
15:40	[27] Development of the jet Feature EXtractor (jFEX) for the ATLAS Level 1 Calorimeter Trigger upgrade at the LHC	WEIRICH, Marcel

Tuesday, 12 September 2017

Trigger - Thimann I Lecture Hall (14:50 - 16:05)

  -Conveners: Jean-Pierre Cachemiche

time	[id] title	presenter
14:50	[98] FELIX: the New Detector Readout System for the ATLAS Experiment	SCHREUDER, Frans Philip
15:15	[109] A High Luminosity LHC Track Trigger for the CMS Detector	Prof. WINER, Brian
15:40	[144] Development of a High-Throughput Tracking Processor on FPGA Boards	CENCI, Riccardo

Thursday, 14 September 2017

Trigger - Thimann I Lecture Hall (14:50 - 16:05)

  -Conveners: Julie Whitmore

time	[id] title	presenter
14:50	[185] Design and Performance of the Upgrade of the CMS L1 Trigger	FOUNTAS, Costas
15:15	[158] A Real-Time Demonstrator for Track Reconstruction in the CMS L1 Track-Trigger System Based on Custom Associative Memories and High-Performance FPGAs	MAGAZZU, Guido
15:40	[166] The ATLAS Fast Tracker System	IIZAWA, Tomoya