

TWEPP 2017 Topical Workshop on Electronics for Particle Physics

Friday, 15 September 2017

TUTORIAL: ASIC Design below 65 nm: Benefits and Challenges with Designing in Advanced CMOS Nodes - Earth & Marine Sciences (E&MS) Building (09:30 - 12:45)

-Conveners: Alessandro Marchioro

time	[id] title	presenter
09:30	[192] Advance Node Impact on Physical Design and Resulting Tool Support - Electrically Aware Design Flow	NAIR, Sravasti
11:15	[194] Designing CMOS Chips Beyond 65 nm	CHEN, Ping