The Development of the Large Pixel Detector at RAL



Marcus French

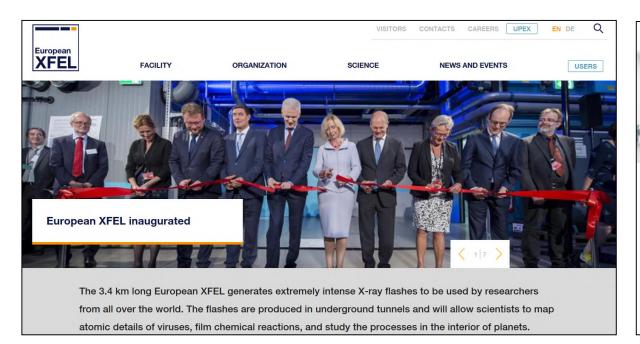
Science and Technology Facilities Council Rutherford Appleton Laboratory





XFEL Inauguration 1st Sep 2017

- Now open for first users
- LPD is now delivered and installed on FXE Beamline
- LPD commissioning is currently ongoing
- It is confirmed that UK now becoming full partner!! ③





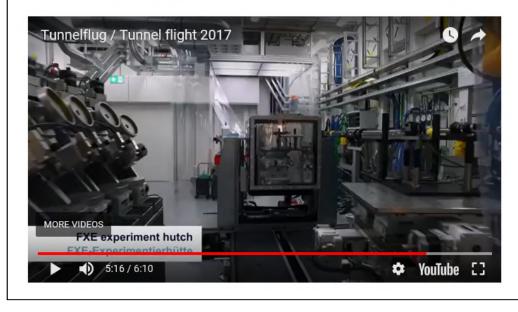
DATE 17/08/21

European XFEL installs its most advanced "eye" yet

XFEL Inauguration 1st Sep 2017

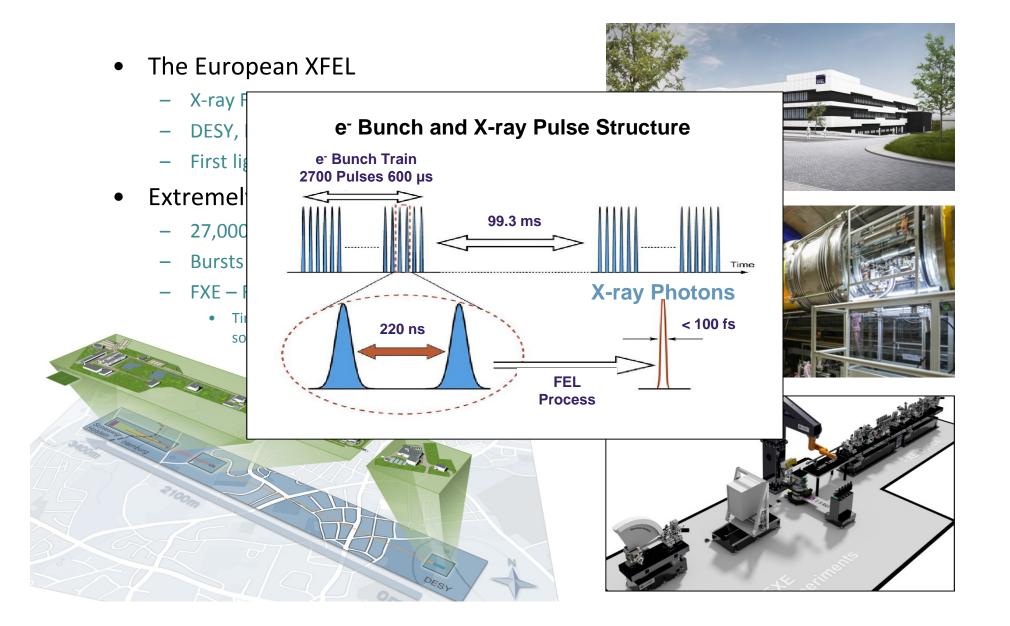
BBC News article
 <u>http://www.bbc.co.uk/news/scie
 nce-environment-41117442</u>

Fly through the tunnel

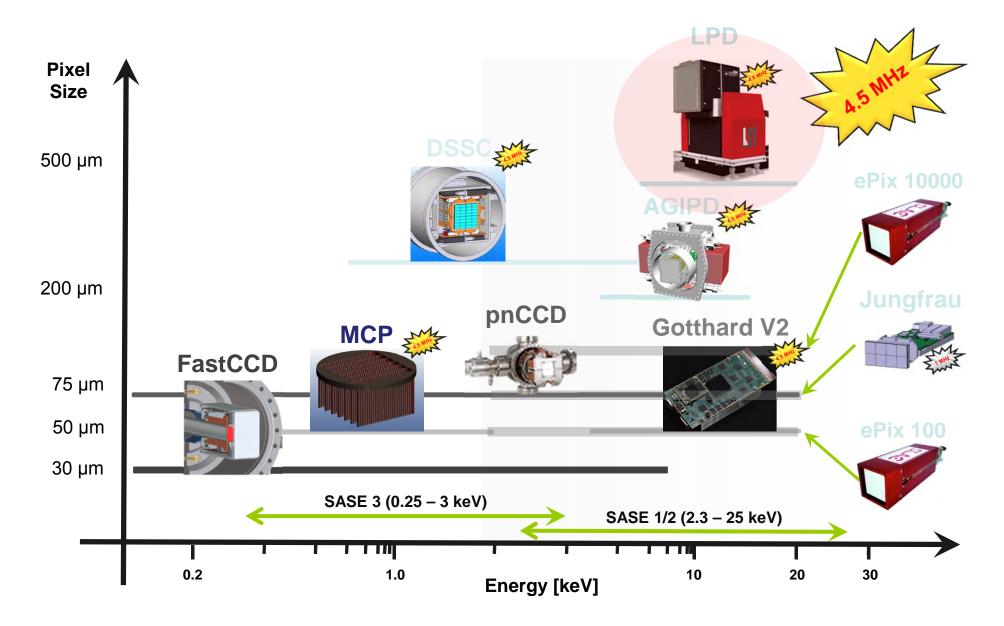




XFEL Introduction



LPD is one of several projects...



LPD: From First Ideas to User Operation: 2006 - 2017



The initial Call



- The European XFEL pulse structure poses strict constraints on detectors (e.g. intensity and time structure)
- No commercial imaging detectors available
- Call for expression of interest launched in 2006
- 3 project proposals were selected with the goal to finally have at least one fast 2D imaging detector

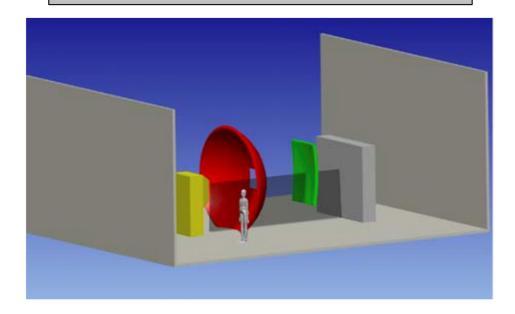
Selected proposals:

- i. Adaptive Gain Integrating Pixel Detector
- ii. The Large Pixel Detector
- iii. DEPFET Sensor with Signal Compression



Initial Thoughts

- Key Features
 - Pixel size 1 mm x 1 mm
 - 16 tiles (16 mm x 64 mm) organized in super modules (128 mm x 128 mm)
 - Two detection planes at 2 m and 4 m sample distance
 - Total sensitive area 12 m2
 - 3000 image storage capacity
 - All in vacuum!



The Really Large Pixel Detector



LPD: Towards a full proposal

April 24th, 2007 Invitation to present the LPD proposal at the 2nd XDAC, A. Schwarz to M. French, H. Graafsma, R. Feidenhans'I, M. Altarelli, I. Gembalies,, N. van Bakel, M. Campbell, N. Allison, P. Delpierre, P. O'Connor; P. Fajardo, K.-T. Knöpfle et al.
 Dear Marcus,

you are cordially invited to present your proposal to develop the

XFEL Large Pixel Detector (LPD)

for the XFEL at the 2nd meeting of the XFEL Detector Advisory Committee, which will take place

May 8th to May 9th, 2007 at DESY, Hamburg The presentation for your consortium has been scheduled for Tuesday, May 8th, 10:15-11:45 o'clock

Technical Team: STFC Rutherford and Daresbury Laboratory Glasgow University Surrey University

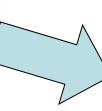
Science Team: UCL Daresbury others

XFEL Large Pixel Detector

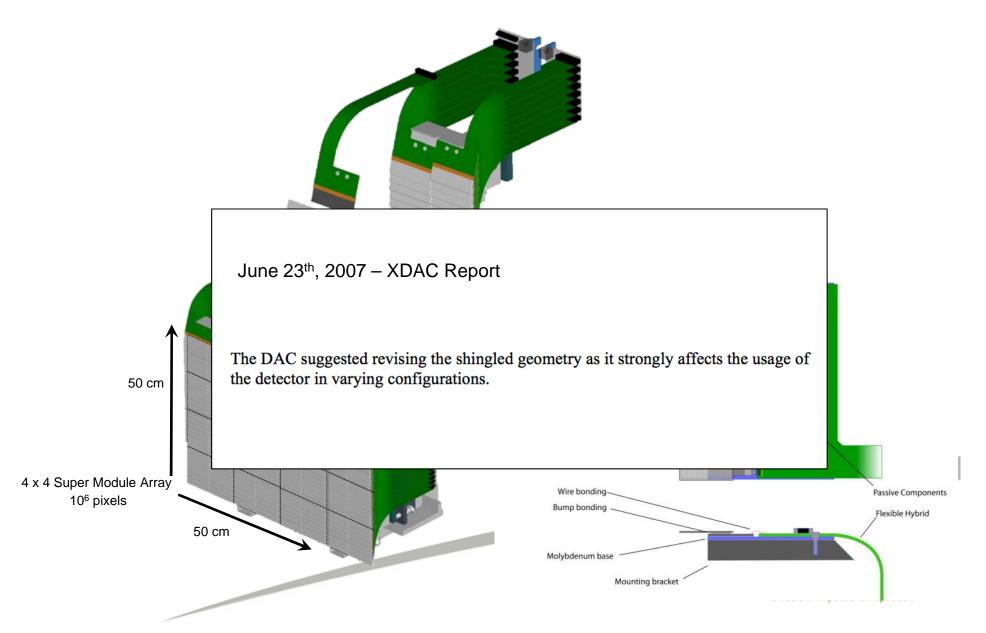
Detector Advisory Committee Meeting 8th May 2007



Science & Technology







The LPD Today



LPD Key Parameters

Large Pixel Detector

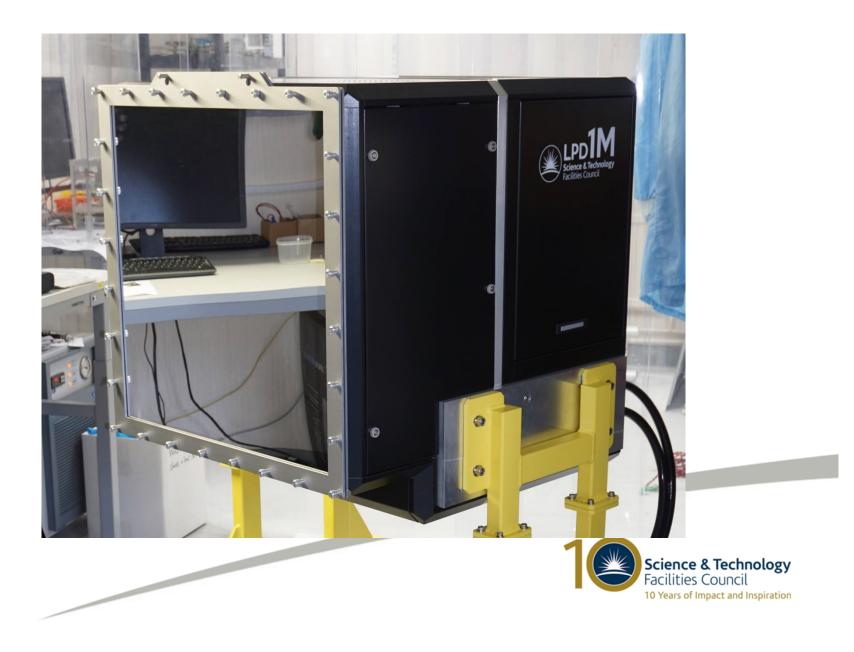
- Built by STFC for the European XFEL
- **1 Megapixel** 500um pixels
- 4.5MHz frame rate
- **Dynamic range**, 1 to 1x10⁵ photons per pixel per pulse. Using parallel gain stages.
- **512 frame memory depth** continuously stores all three gains, overwriting whenever a veto is received.
- Output data rate ~10GByte/s per megapixel



The LPD megapixel detector.



LPD Tour – Front View

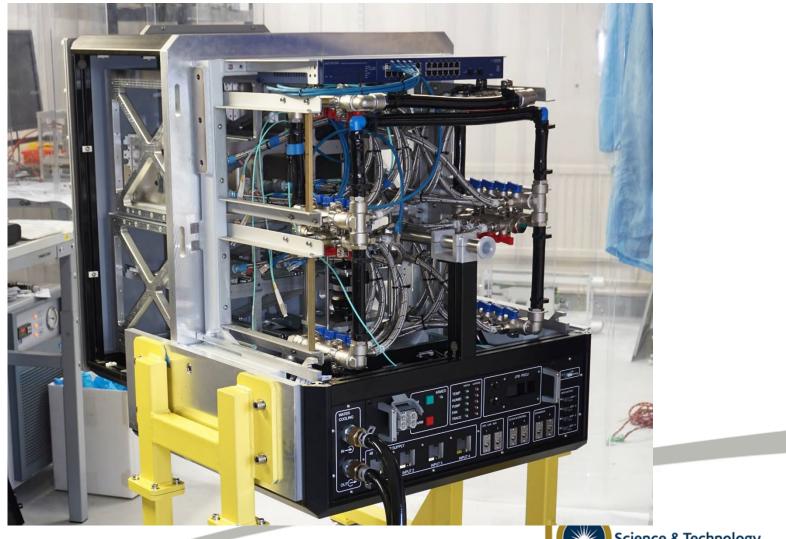


LPD Tour – Front View





LPD Tour – Rear View





LPD Tour – Rear View



LPD Tour – Detector Control



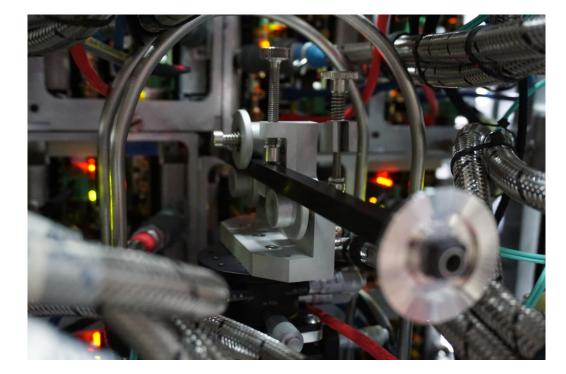


System monitoring and interlocks only allow power up of the front end systems when safe

- Housekeeping power supplies
- On-board PC for remote access
- Hard wired interlock logic
 - Information screen: Status and alerts

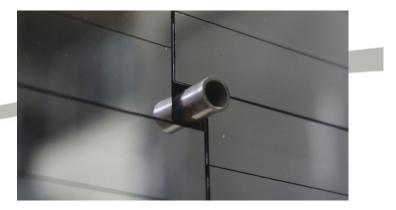
If conditions deteriorate then power is shut down and the failure state latched for inspection prior to rearming the system

Added Beam Flight Tube

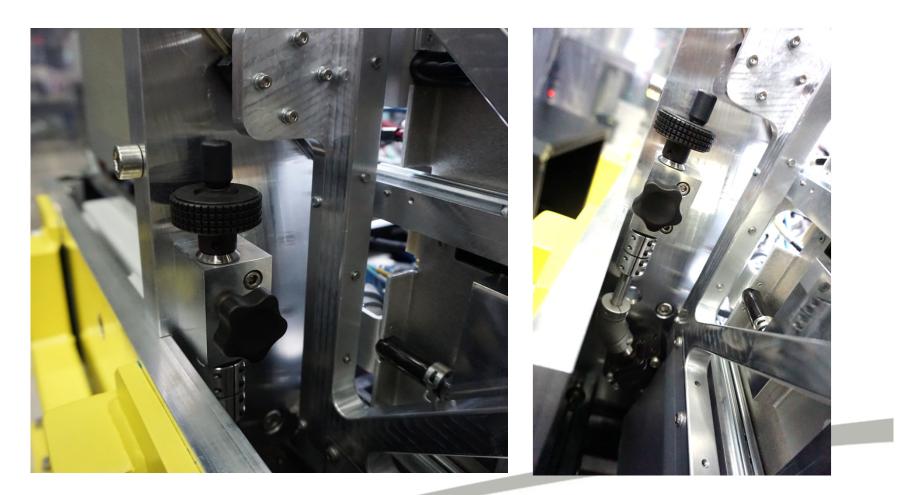








Central Hole Adjustment





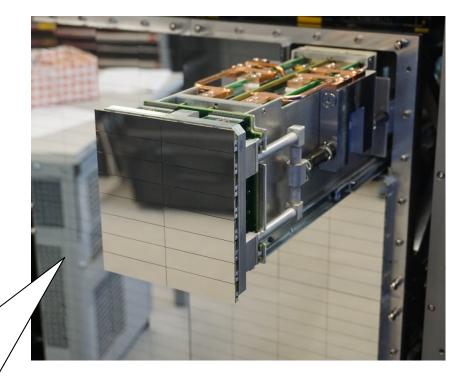
LPD Detector Family

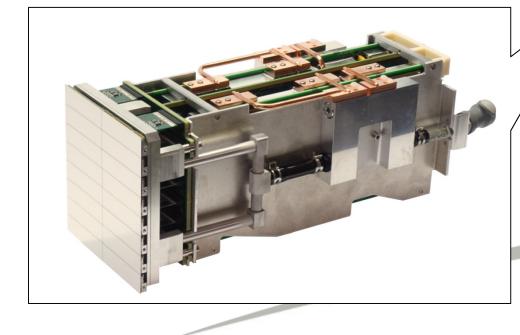


Two-Tile System	Super-Module	¹ /4 Megapixel Detector	1 Megapixel Detector
Small flexible test system (32 x 256 pixels)	Small flexible test system (256 x 256 pixels)	Test system at XFEL.EU (512 x 512 pixels)	Final system (1024 x 1024 Pixels)
Firmware tests Early beamline tests and calibration Veto system tests First 4.5 MHz images at FXE	1/16 th of the 1 Mpixel detector Early beamline tests and calibration measurements	Mechanical concept and integration concept for 1 Mpixel detector	Installation at photon beamline Scientific experiments at FXE

LPD Super-Module

- Front end system segmented into 16 Supermodules
- Each easily swappable for servicing and maintenance

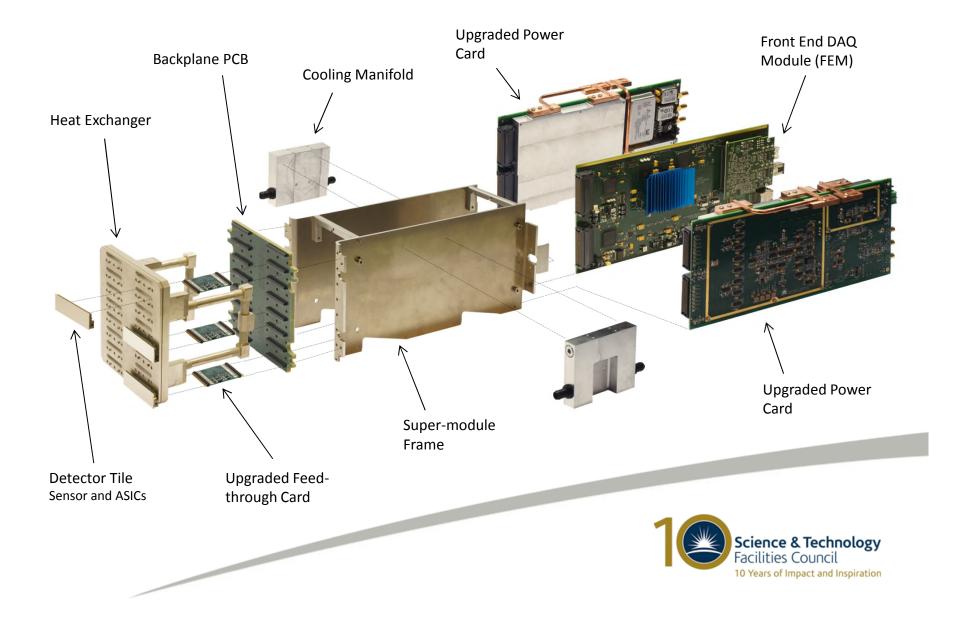




link

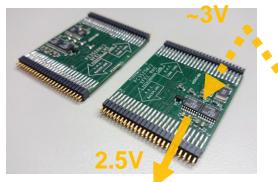


Super Module Components

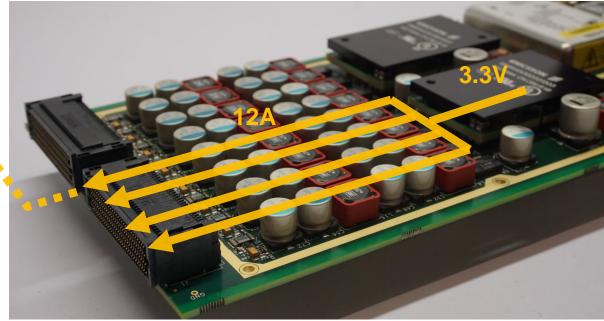


Delivering the Power

Front end low drop out regulators



Power to ASICs



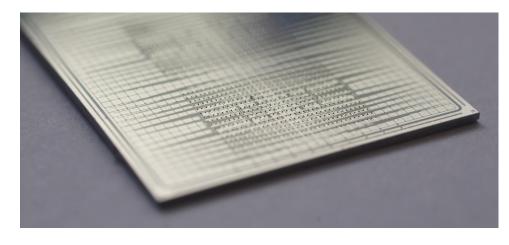
Separate filtration chain for each LPD detector tile.

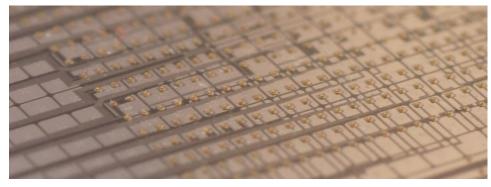
- 3.3V DC-DC converters each feed 4 filtration chains
- Each chain handles up to 12A (RLCCLCC) required for each detector tile
- Closer to the tile there is further voltage regulation to deliver smooth 2.5V



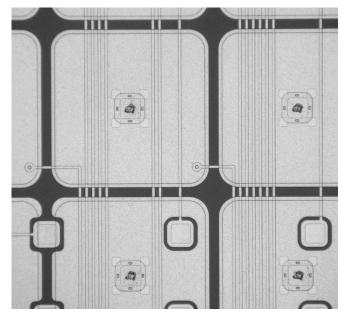
LPD Sensors

- 4096 Pixels (32 x 128)
- 500um pixels tracked to a finer pitch bump bond array
- 2 layer metal design

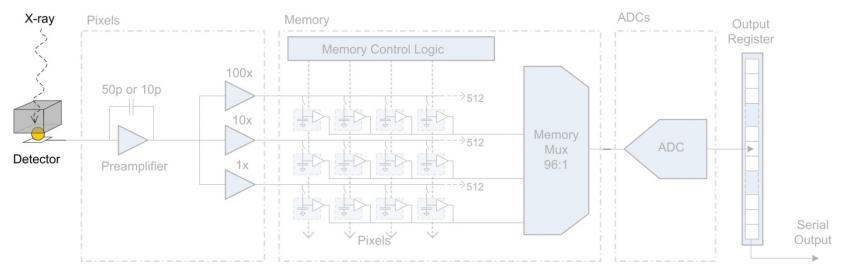




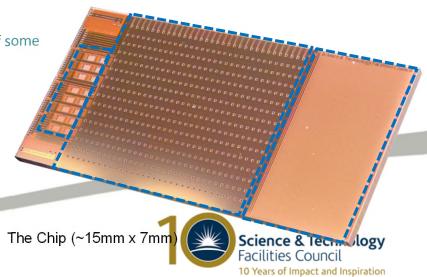




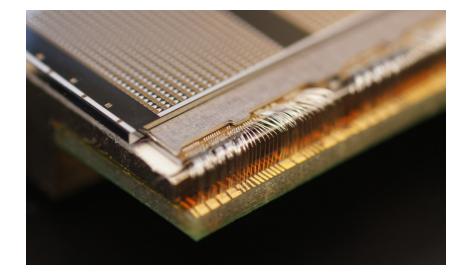
LPD ASIC 2012

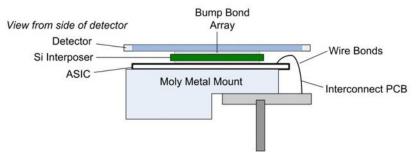


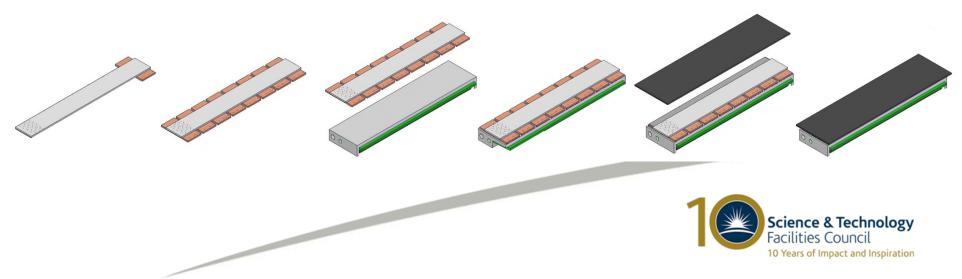
- 512 Channels
- Preamplifier with 10/50pF feedback
 - An additional high mode gives lower noise at the expense of some dynamic range.
- 100x, 10x and 1x parallel gain stages
- 512 frames of memory for each channel and gain
 - Veto System
- 16 SAR ADCs 12 Bit
- 100MHz digital output
- IBM 130 nm



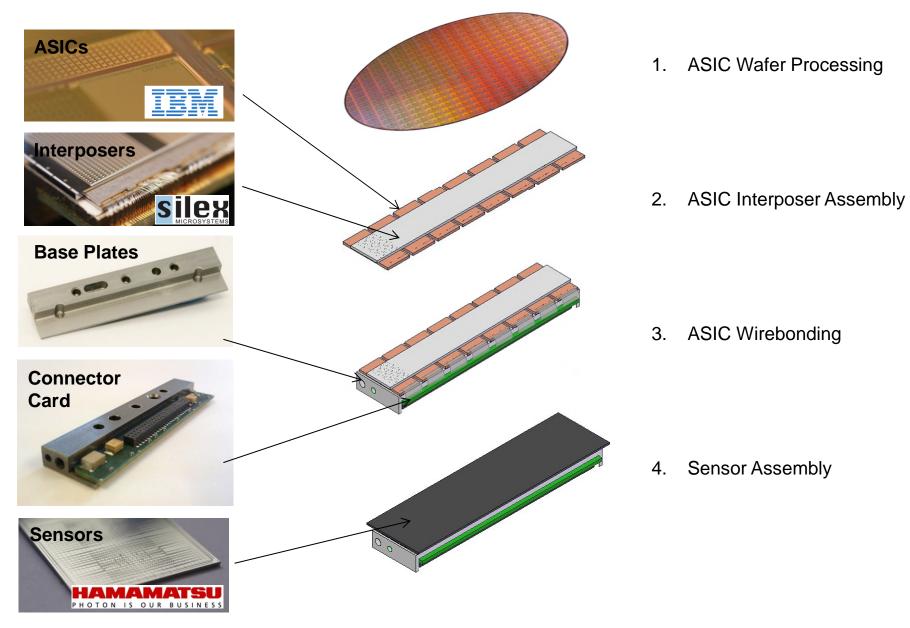
LPD Overview - Interconnect







Manufacture of Modules



LPD Current Data Rates

- Output data rate ~10GByte/s per detector
- During a 12 hour experiment this could generate ~0.4PB of raw data
- LPD is an integrating detector, each image could be interesting in it's own right, so all of this may be saved.
- In reality experiments are unlikely to run with such high efficiency XFEL estimate lower values for their annual disk space requirements
- 10PB per year for long term storage (<u>http://www.xfel.eu/research/data_handling/</u>)



- The LPD system has 4 MTP fibre optic outputs
- Using a total of 16 pairs for data output.





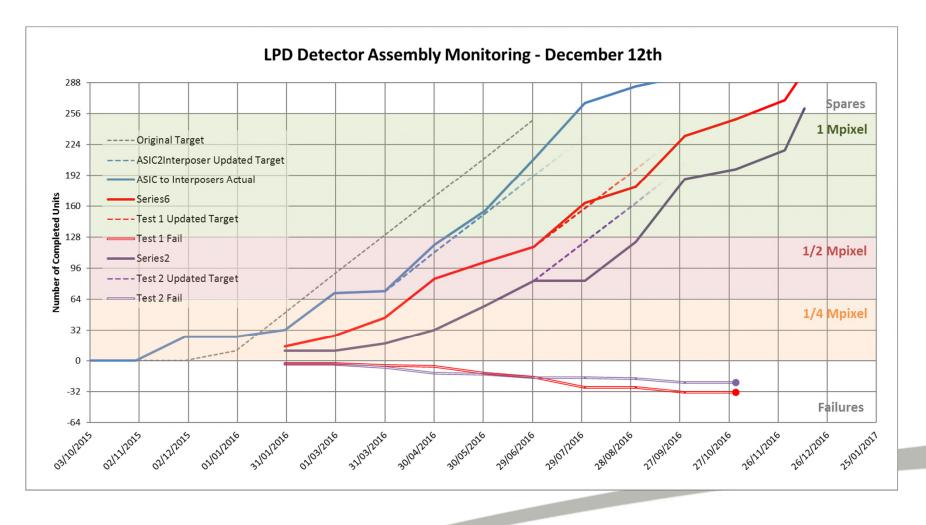
LPD Production



Traceability & configuration records

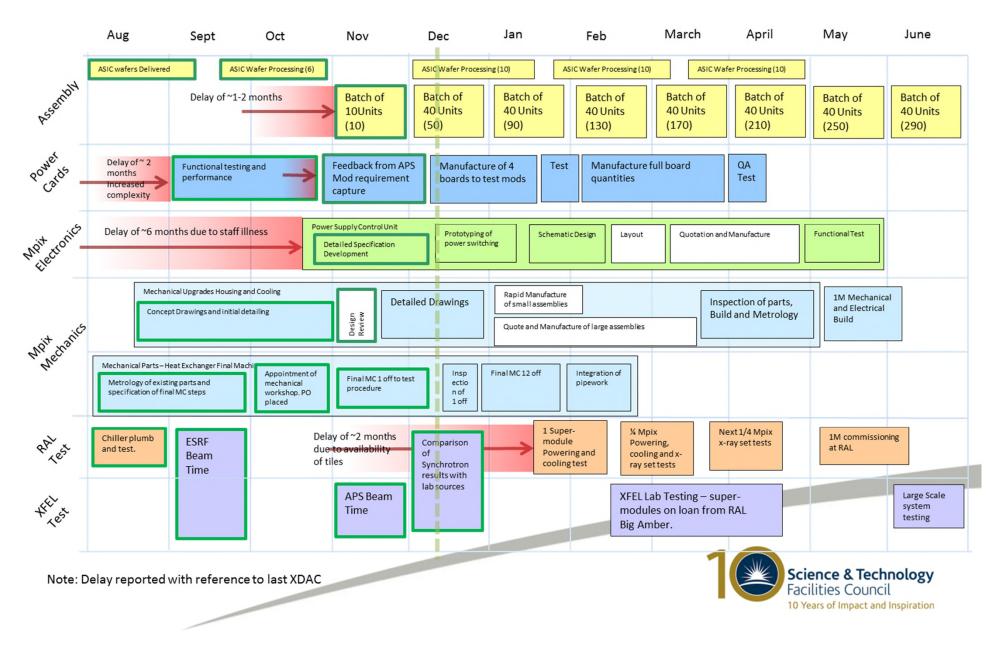
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											Scienc Facilitie	ce & Technology es Council	

Tile manufacture tracked

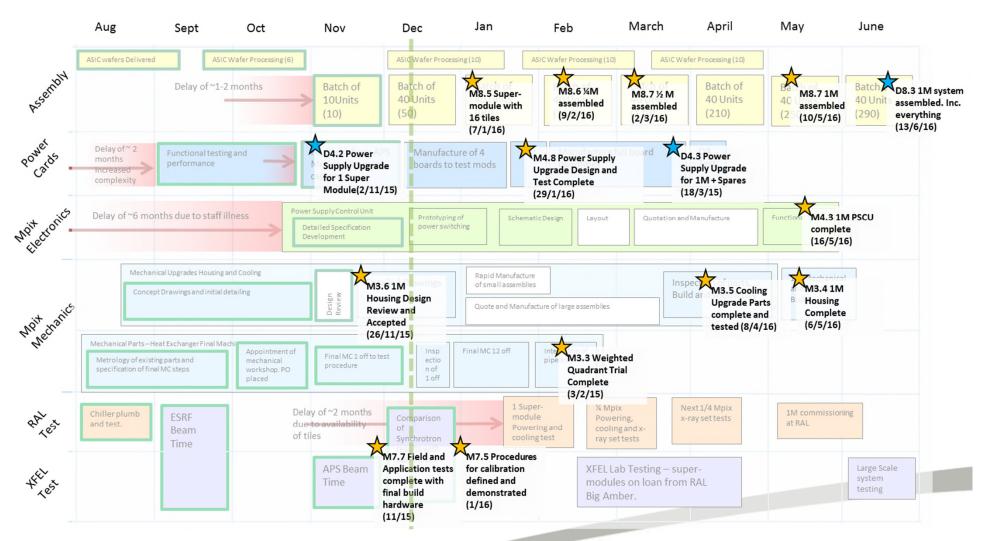




Detailed plans for all components



With Milestones tracked

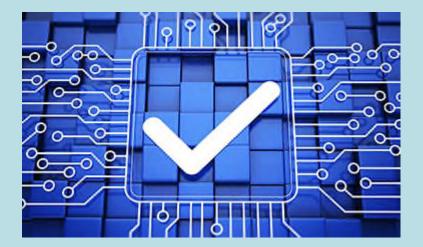


Note: Delay reported with reference to last XDAC

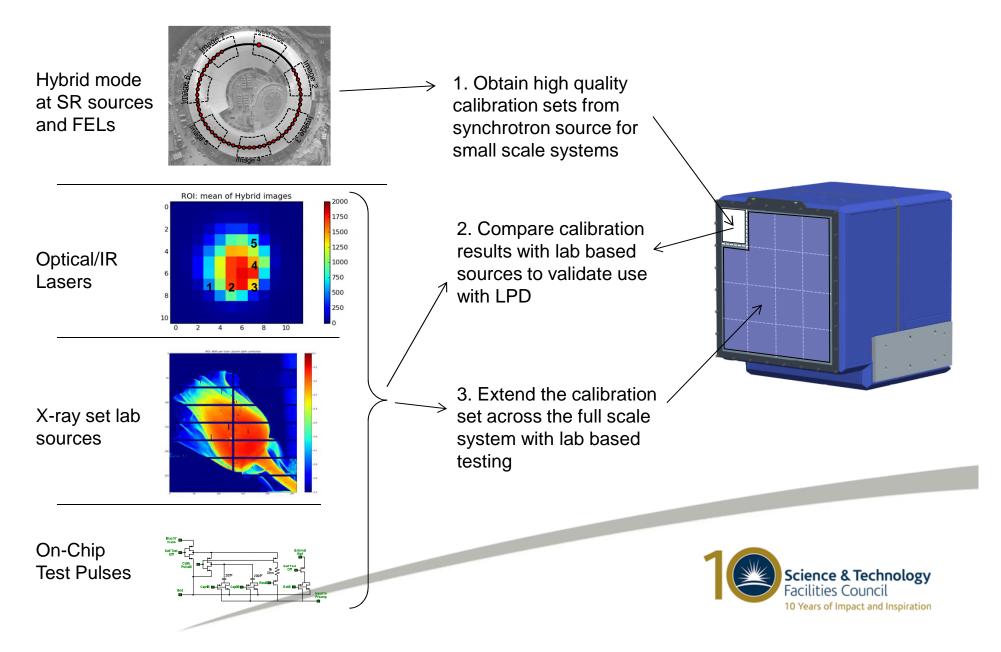




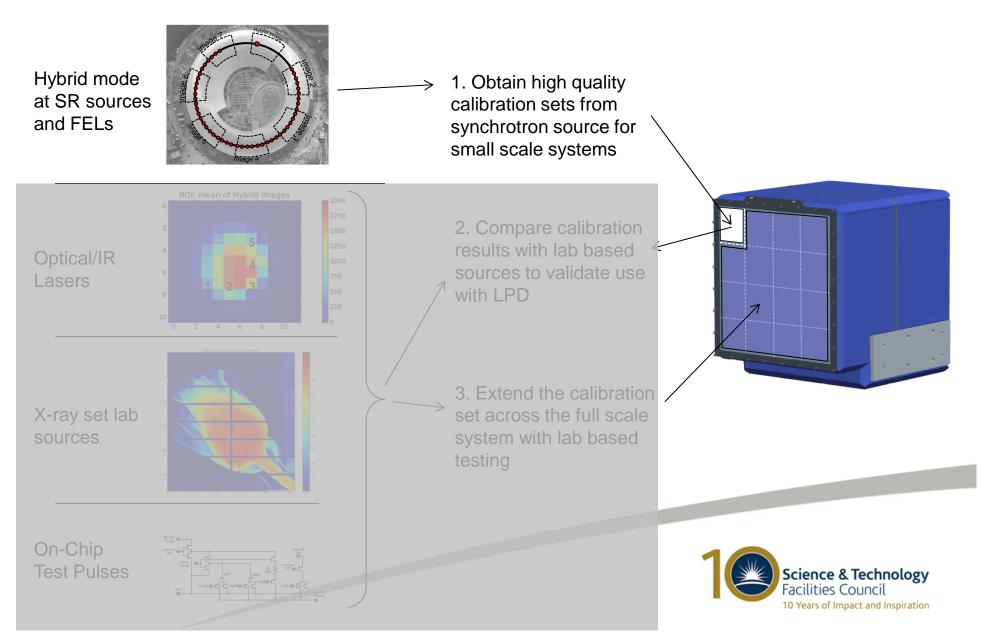
LPD Test and Callibration

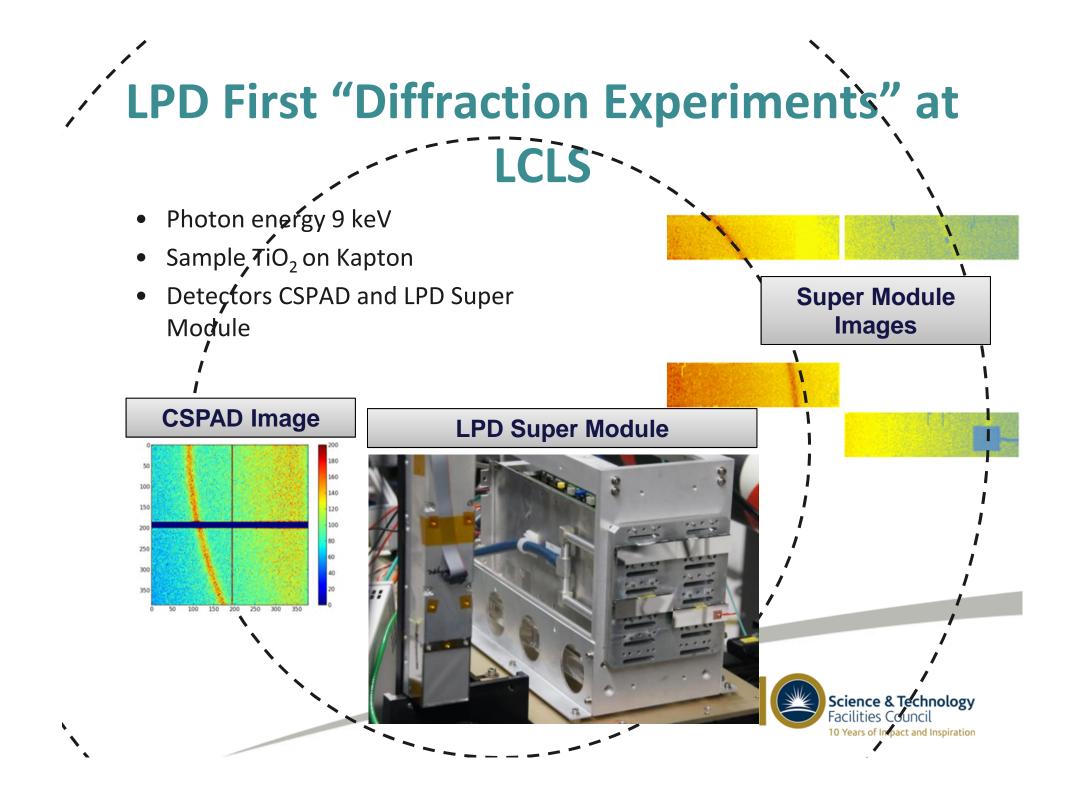


LPD Test and Calibration Programme



Xray Photon Sources

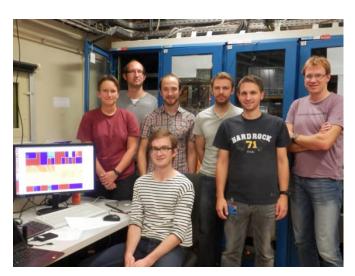




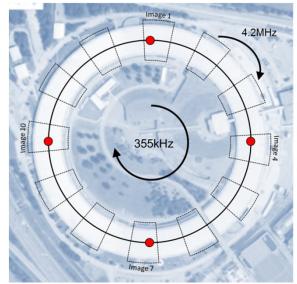
ESRF – 4 bunch mode

ESRF – Using 4 Bunch mode to mimic XFEL and FXE style experiments with liquid jets and laser pumping.

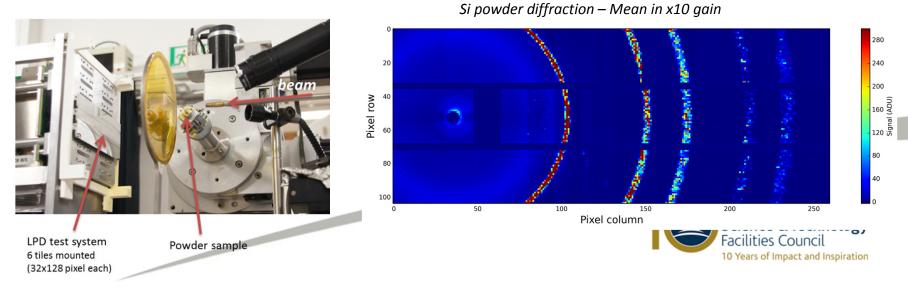
Single shot diffraction patterns



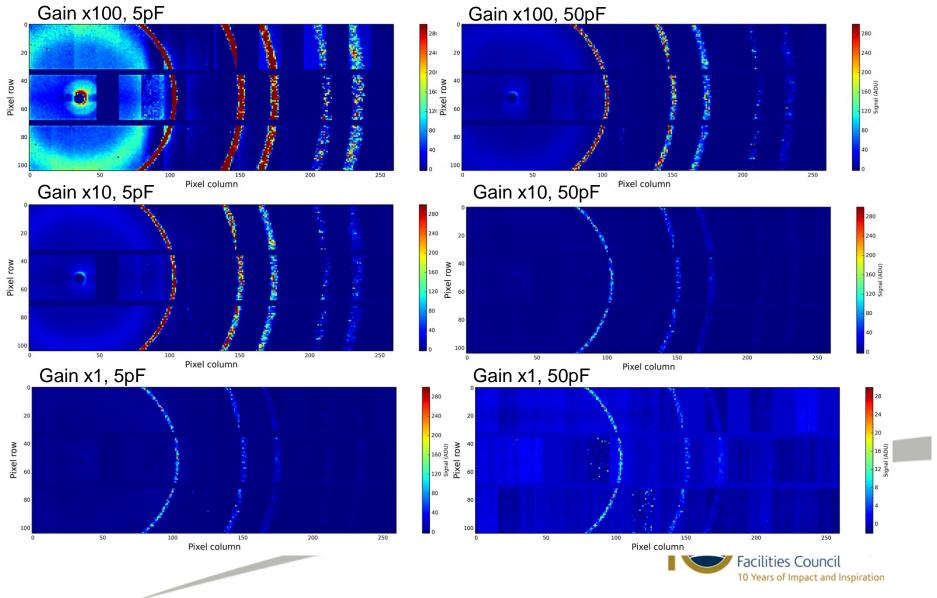
ESRF - XFEL and RAL team



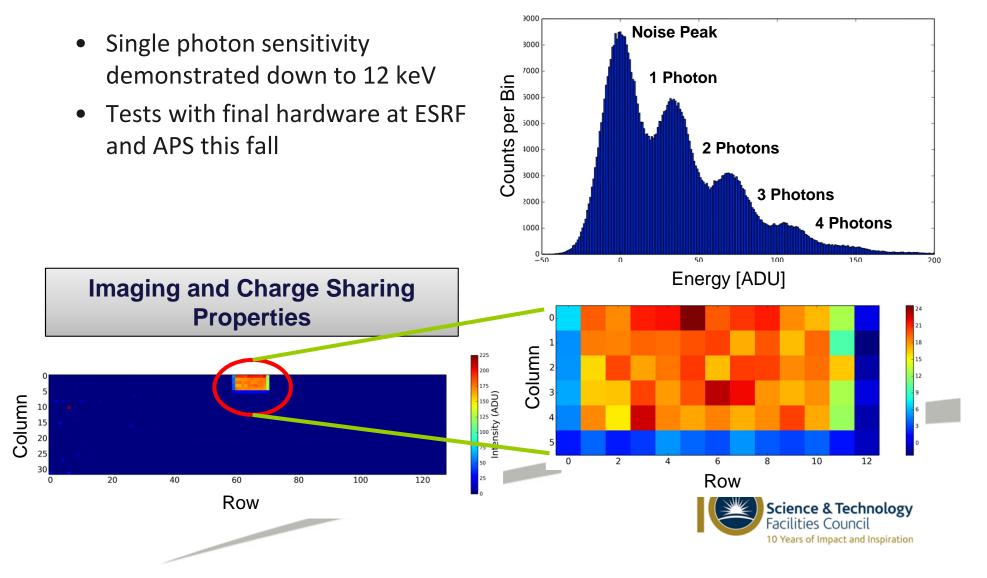
ESRF Hybrid fill 4 bunch mode. Synchronized with LPD running at 4.2MHz



LPD – Same Diffraction Pattern at Different Gains at ESRF

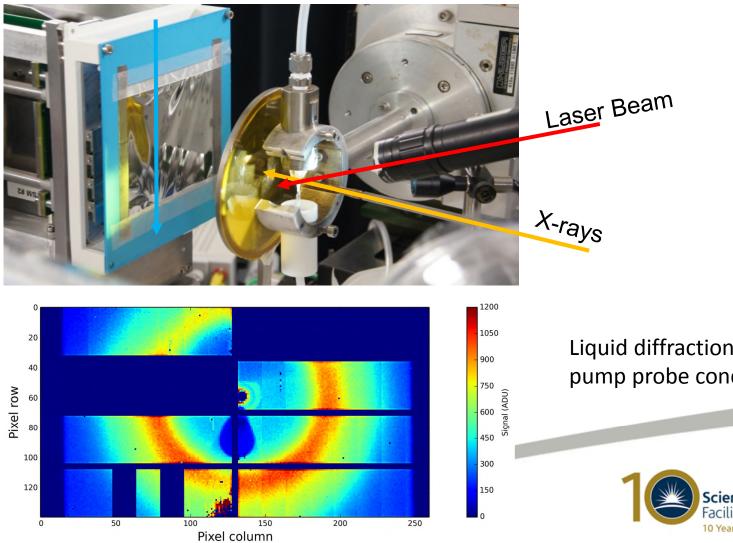


Low Intensity Performance at LCLS, PETRA III and APS



ESRF – 4 Bunch Mode

Water Jet (sample)



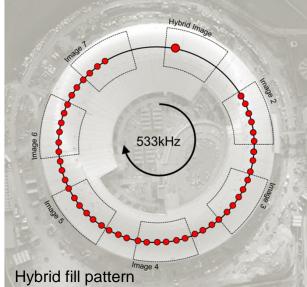
Liquid diffraction with laser pump probe conditions

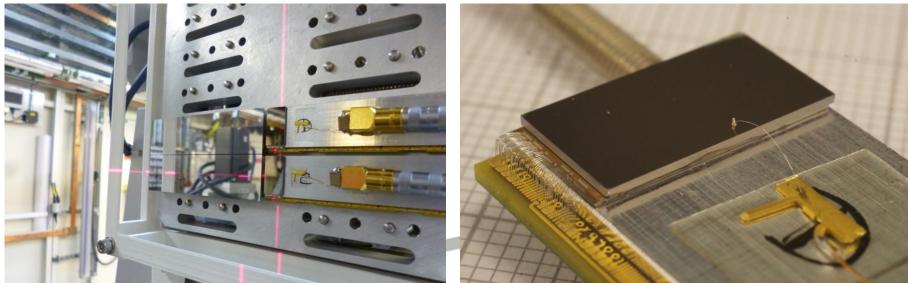


Diamond – Hybrid Mode

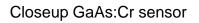
Diamond – Hybrid mode

- Well timed monochromatic photons for 4.5MHz imaging.
- Test of a full LPD super-module with new power cards
- LPD Silicon and GaAs side by side trials





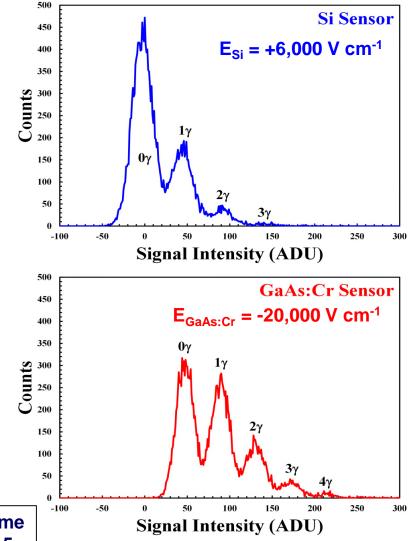
LPD Silicon and GaAs:Cr tiles operating side by side



10 Years of Impact and Inspiration

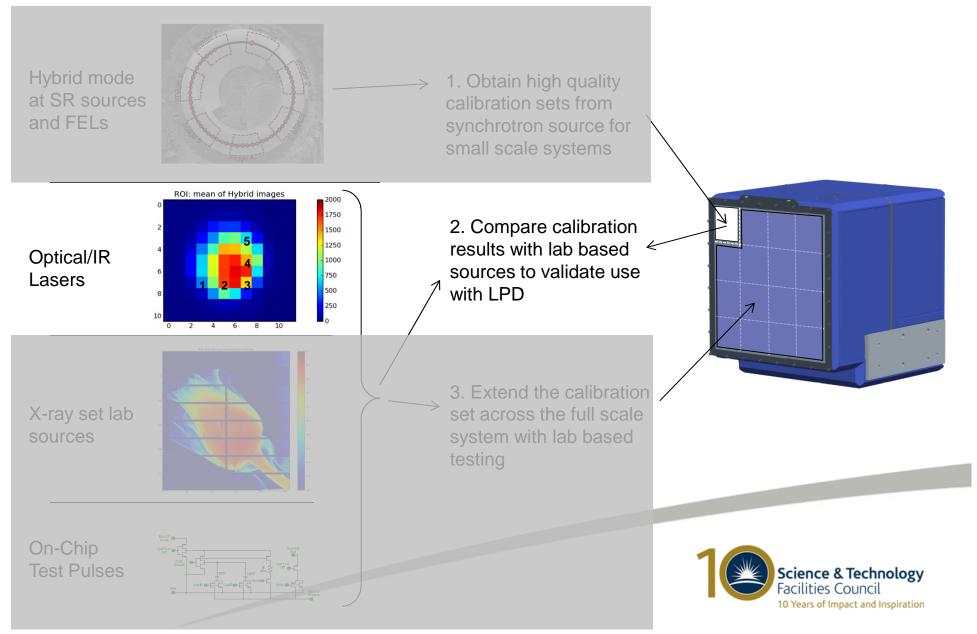
Diamond – LPD GaAs:Cr

- Al absorber = 1.0mm
- Single photon counting
- Beam size = 0.1mm x 0.1mm
- Photon E = 20 keV
- GaAs:Cr stops 60% more events as expected.
- Noise performance in Si & GaAs:Cr limited by the ASIC.
- Evidence for polarisation...



Characterisation and Development of GaAs:Cr High Frame Rate X-Ray Imaging Detectors – Dr Matthew Veale R03-5

Optical/IR Lasers

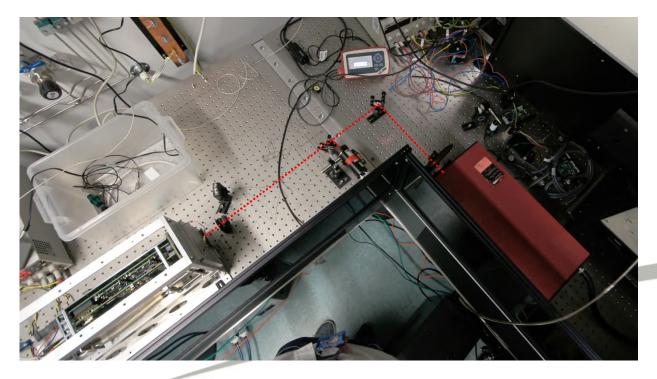


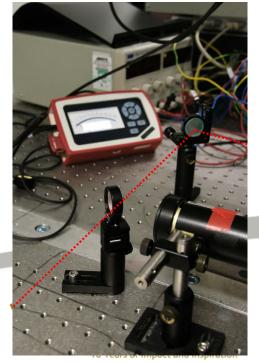
Beamtime at RAL - CLF

Central Laser Facility

- Class 4 laser
- ND filter attenuation
- Intensity monitors
- Single shots synchronised to LPD

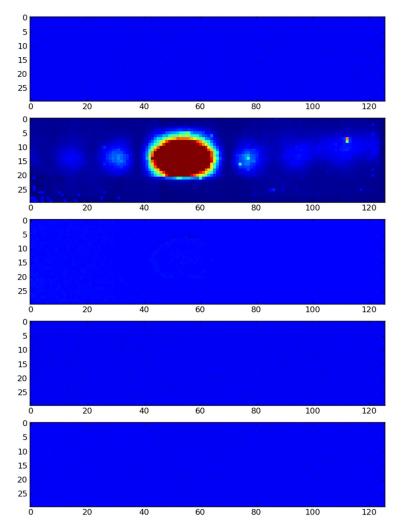




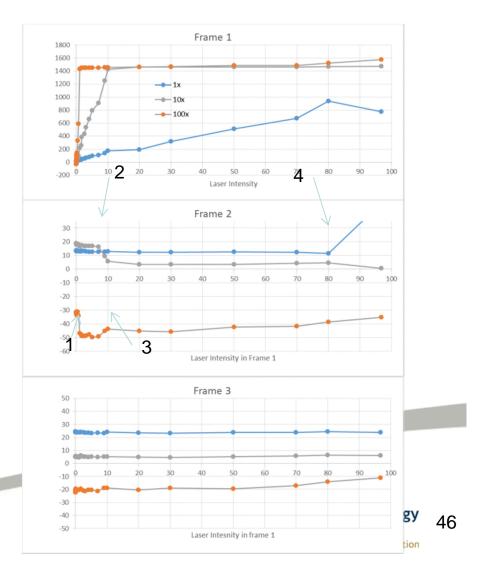


Beamtime at RAL - CLF

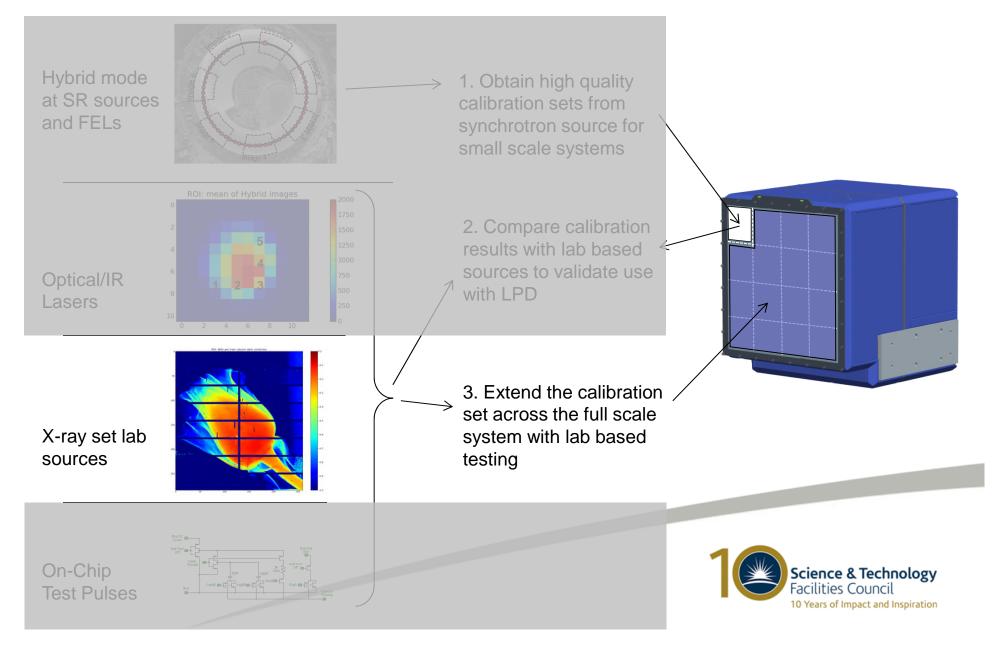
5pF x100 – Average of 10 Trains with dark subtraction



Single Slit Diffraction – 4.5MHz Train

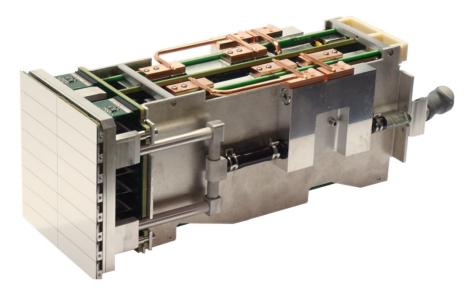


Xray Set Transmission Images



Transmission Imaging

X-ray Set – Each LPD super-module to be tested on RAL x-ray set to provide flat field information and bad pixel maps.



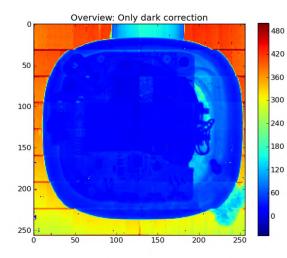
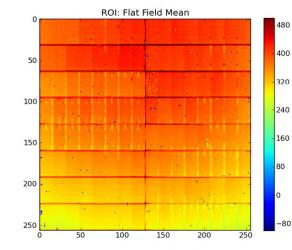
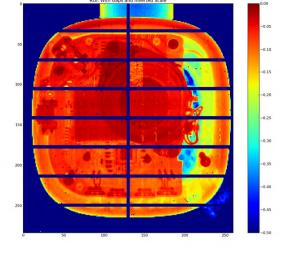


Image Data With Dark Correction



Flat Field Data



Corrected Data

All SM Screened to 'Map' pixels

- Identification by two methods
 - Out of range signal Unconnected or shorted pixels.
 - Out of range noise Noisy pixels, coupling to shorted pixels, bad power supply units

1. Take all 16 flat fields or 2. Create the 'ideal' super-3. Threshold individual module map from the noise maps data against ideal to median produce bad pixel map Science & Technology Facilities Council 10 Years of Impact and Inspiration



LPD Shipping

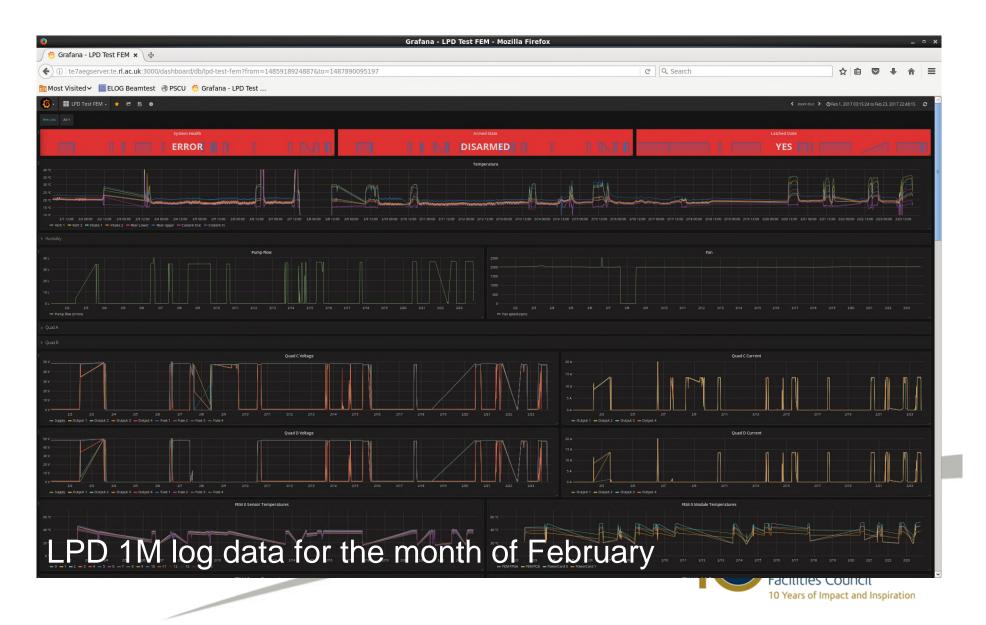


Jan 2017 – LPD Complete at RAL





LPD 'Soak Testing' prior to shipment



Disassembling the System

• Video Clip <u>link</u>



LPD Packing up

- LPD Super-modules shipped in flight cases. 4 groups of 4.
- 4 Shipping crates built for 1M housing, Power Supply, Chiller and Accessories (Cables etc.)
- RAL Transport drove the equipment to Hamburg.



Super-module flight cases



LPD at Hera South

LPD Shipped to Hera South – Feb 27^{th} Operational at Hera South - March 8^{th}







Arrives 2nd March 2017







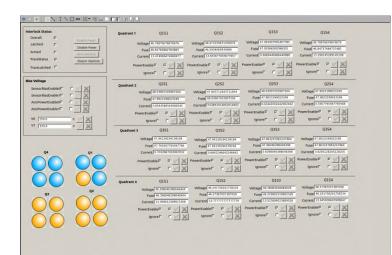


LPD Commissioning



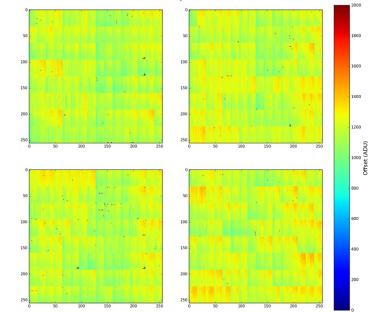
Hera South End-to-end Test



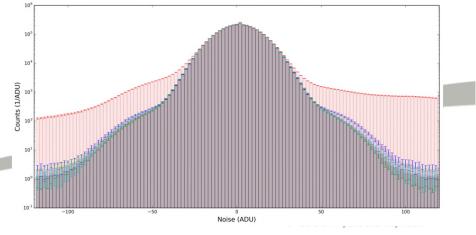


GUI bringing together multiple LPD Karabo Devices

Offset Maps for Q4



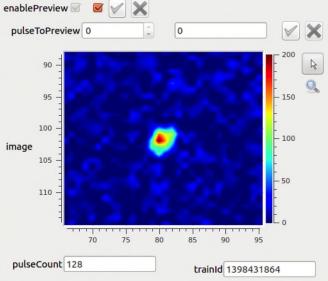
Noise histogram for Q4 (log scale in y)



LPD First X-ray Light



Large Pixel Detector (LPD) First Light at Big Amber



4.5MHz with C&C providing trigger, clock, and 504 noVeto pulses X-ray tube with Mo target and focussing optics at 30kV and 40 mA 500 trains with 128 memory cells after online offset correction



Arrival at XHQ



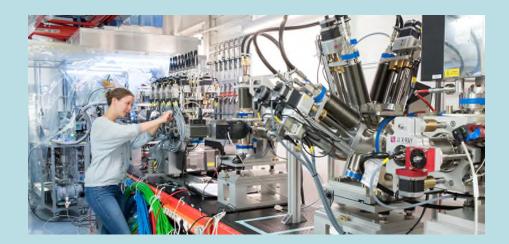


Wedding – Support Meets LPD Detector

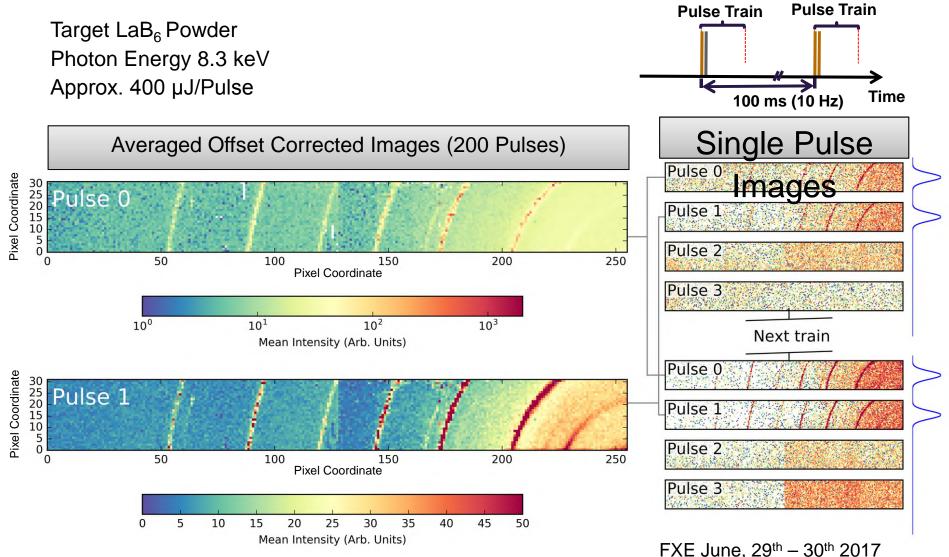




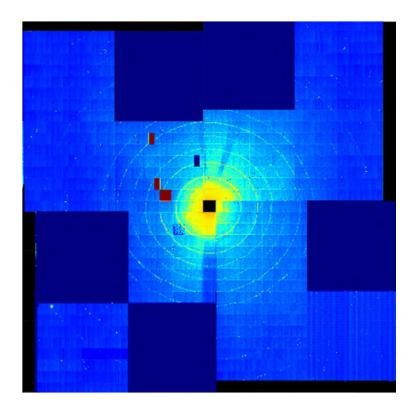
First Xrays at FXE



First Single Pulse Images @ FXE LPD Two Tile



Commissioning at FXE



- First Powder diffraction with main detector
- Not all SM wired in to DAQ yet
- Some clock timing issues





Conclusions and Lessons Learned



10 Years, 4 Months and a Few Days...

(April, 24th 2007 till Sept 7th, 2017)



- Budget (European XFEL)
 - Phase I
 - Phase II
 - LPD Prototype system
 - Spares and Support etc



- **Project Changes**
- 3 Project Change Requests
 - Detector Cooling System
 - Housing size
 - Power connectors
- ASIC Resubmission

PROJECT PROGRESS REPORT

leporting Period om November 2016 to April 2017

Aatthow Hart	
ame of Institution	
The Science and Technology Facilities Council (ST	FC)



21 Reports to the European XFEL Detector Advisory Committee

Various manuals, drawings intermediate reports



- 21 Meetings of the Detector Advisory Committee
- 12 Calibration Working Group Meetings
- 11 Integration meetings
- 2 Project readiness review meetings
- 1 Go/No-Go Review



Σ=4394 project related mails→on average 1.8 mails per working day



XFEL Lessons Learned

- Modular design and early availability of test systems played off in many respects
 - Performance testing and optimization
 - Early DAQ and software integration

- Verification of integration and calibration conceptsLPD 1 Mpixel detector is installed at FXE and will be commissioned during the next weeks
- End-2-end test could not be completely finished; commissioning and calibration tasks will be finished at beamline
- Testing and commissioning at photon beam lines and with laboratory sources is essential incl. data analysis
- Project scheduling is typically more on the optimistic than realistic side
- Clear definition of scientific requirements and use cases avoids design iterations, extra cost and delays
- Avoid "last minute" changes to designs or requirements



RAL Lessons Learned

- Testing of the system took much longer than expected
- Many technical challenges
 - The flat geometry
 - Radiation shielding
 - Heatsink technology
 - Power supply noise
 - Sensor bias <u>link</u>
 - Interconnect technology
 - Multiple jigs and packaging
 - Numerous firmware revisions
 - Software development
 - Data handling and visualisation
- But its still been fun!



Tiles with low resistance sensor bias

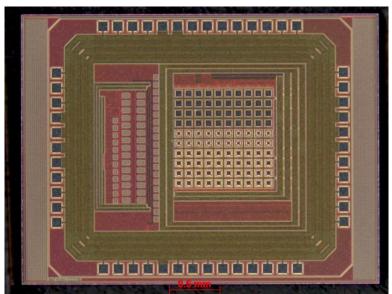


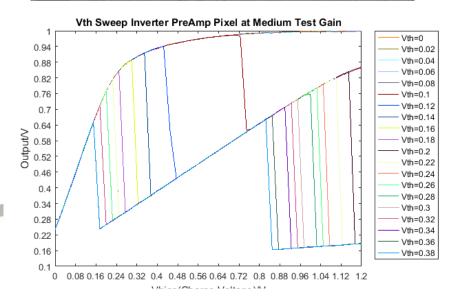


The Future?

Prototyped various pixels for LPD upgrades

- Built on IBM130nm
- 10 x10 array with various amplifier designs
- Could be imbedded to replace 50pF mode in the current ASIC
- Other significant improvements possible following the learning from all the LPD testing





Planning for future Data Rates

Today we are building CMOS sensors with:

16 Million (4096x4096) pixels with a frame rate of 1,000 fps

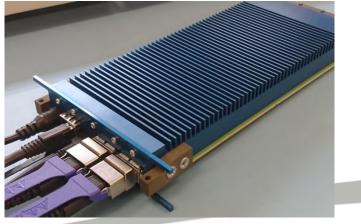
So - 16Gpix/s

Digital output @10+1 bits => 176Gbit/s

256 LVDS Double Data Rate lines at 720Mb/s

- FEM II DAQ Board for next generation detectors
- Has dual 40Gb data output 10GB/s capacity for each board.
- Increase of 4x on the current LPD DAQ board, of which there were 16 inside the LPD system.







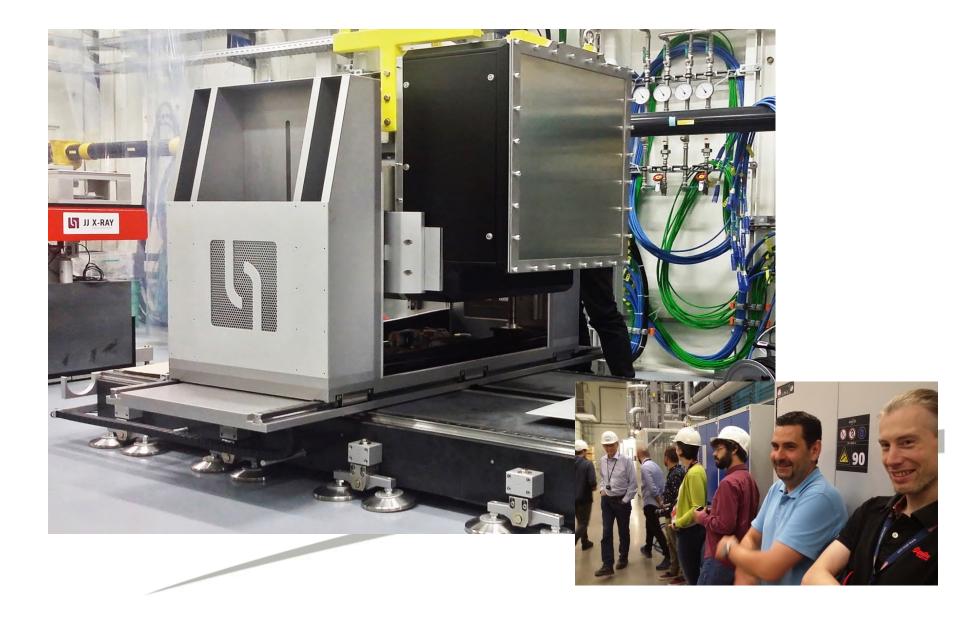
Closing Thoughts

• With LPD we have a flexible system

- Super-Modules could be upgraded with smaller pixels
- The ASIC was delivered in 2012 and significant performance improvements are possible today
- STFC is proud that the system is ready for day 1
- Lets look forward now to the new science that will come from this exciting facility!



Thank you very much!



Acknowledgements

Key People:

LPD Team

- Matt Hart
- ...

XFEL Team

- Philipp Lang
- Markus Kuster
- Andreas Koch
-

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Science & Technology Facilities Council Rutherford Appleton Laboratory





flythrough link

