

Four-quadrant Silicon and Silicon Carbide Photodiodes for Beam Position Monitor Application: Electrical Characterization and Electron Irradiation Effects

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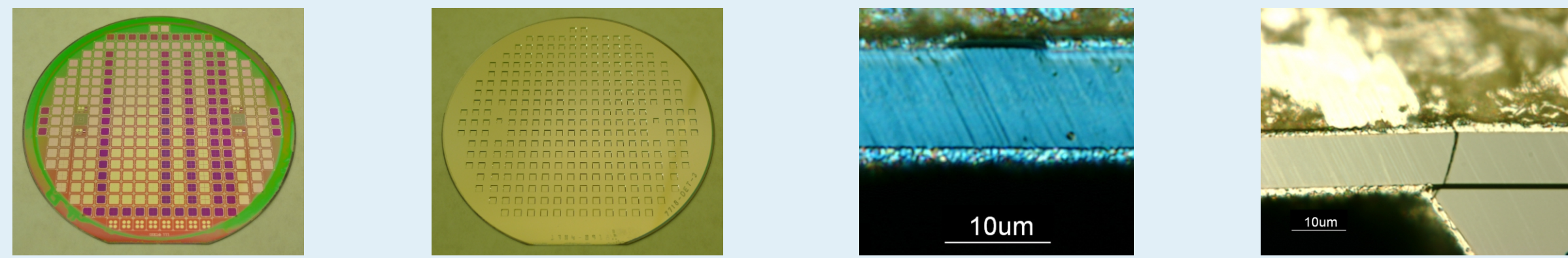
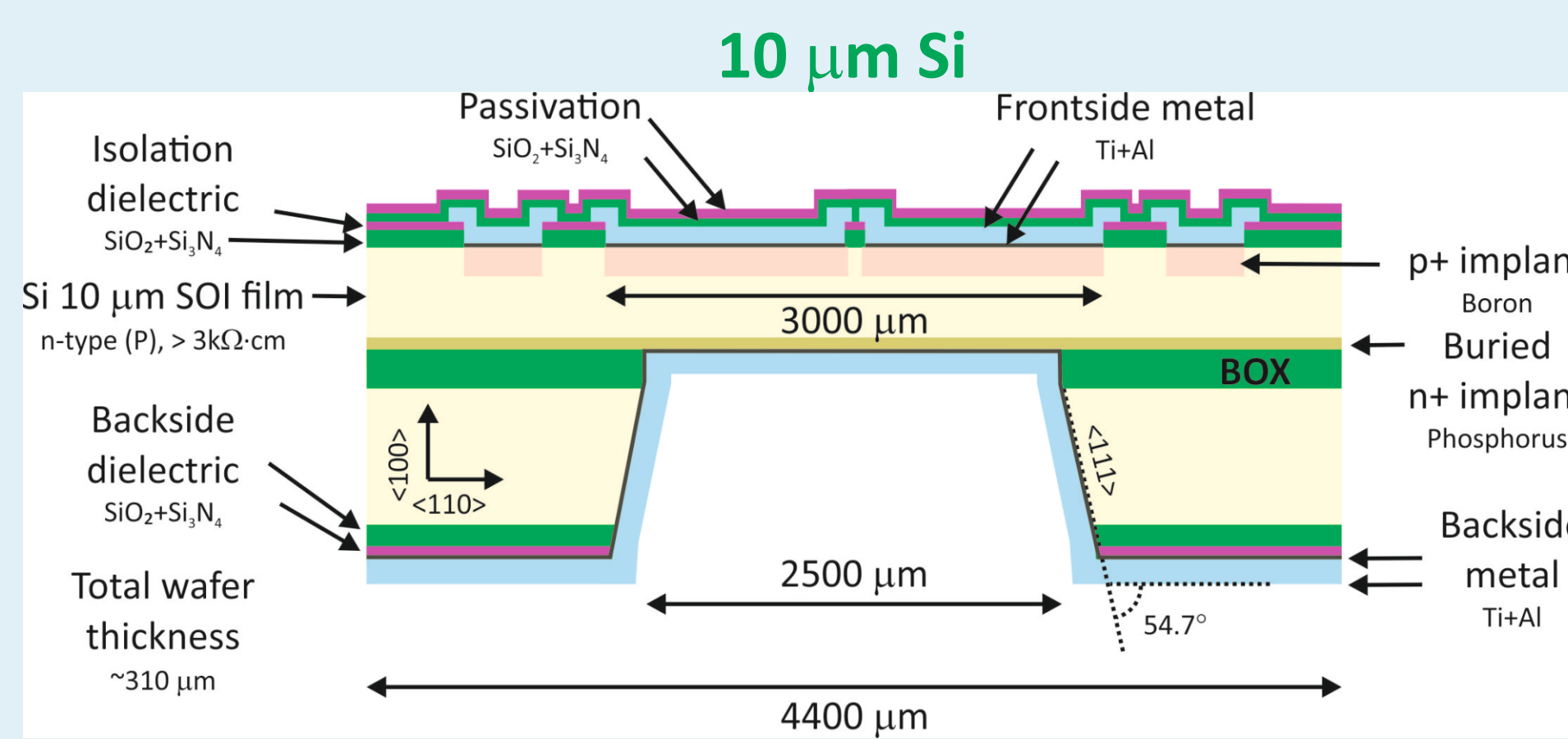
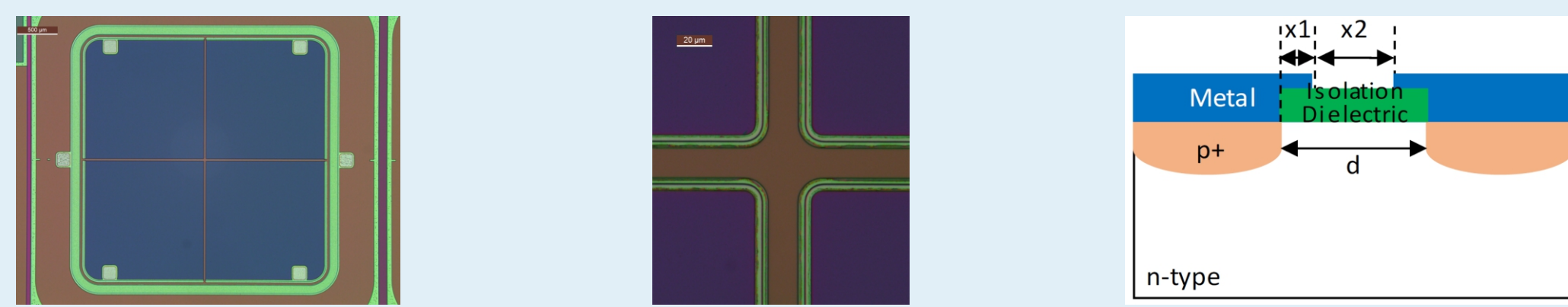
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Introduction

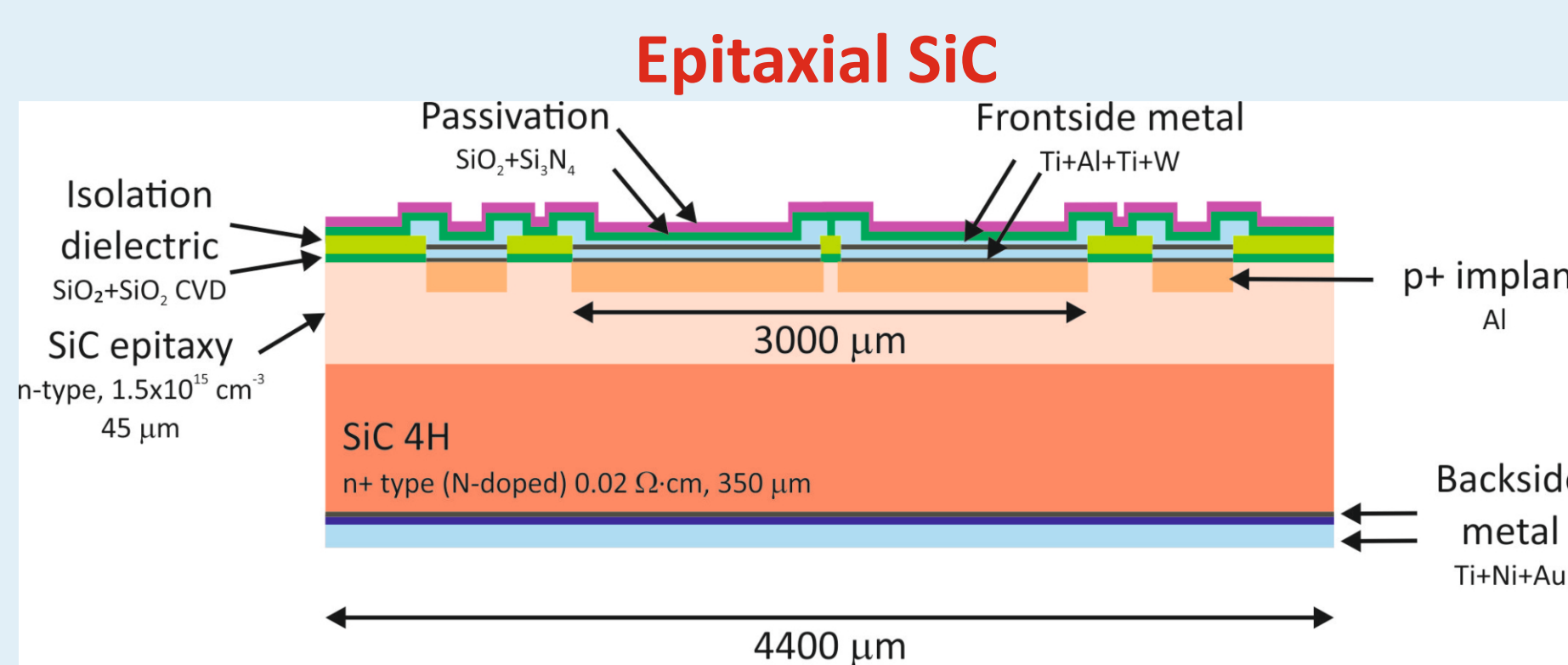
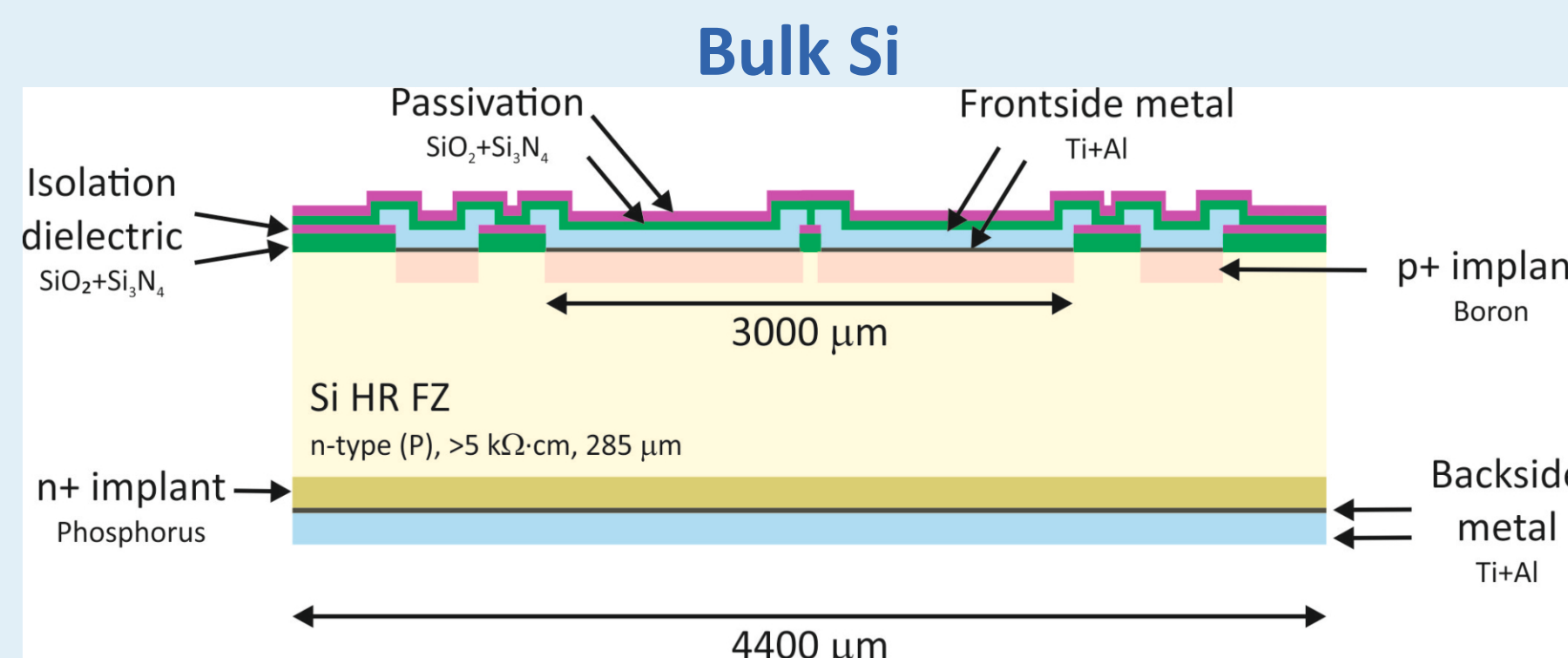
Silicon photodiodes are very useful devices as **X-ray beam monitors** in **synchrotron** radiation beamlines. In order to be used in **transmissive mode** and given the absorption properties of silicon, the devices must be **thinner than 10 µm** to achieve X-ray transmission higher than 90% for photon energies above 10 keV. On the other hand, **bulk silicon** segmented devices are also of interest for **astronomy** and **space applications**, such as solar tracking systems. Owing to their **lower susceptibility** to variable **temperature** and **illumination** conditions, there is also a special interest on **silicon carbide** devices for some of these applications. Moreover, **radiation hardness** of the involved technologies is a major concern for **high-energy physics** and **space applications**

Fabricated 4-quadrant diodes on 3 substrates

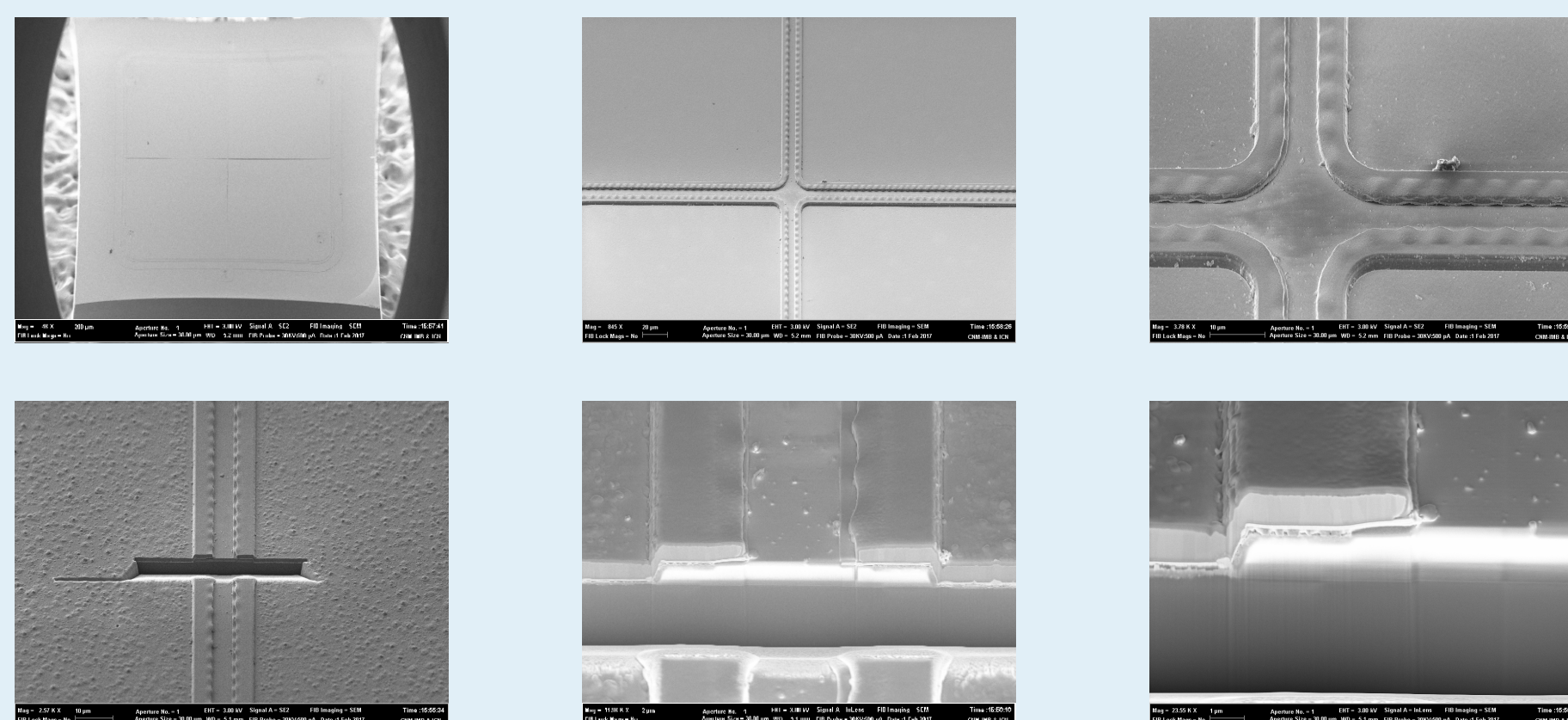
Single and 4 quadrant diodes with different geometries for interquadrant distance & metal layer. MOS capacitors with interquadrant isolation oxide



Wafer front side Wafer back side SOI film Back etch corner



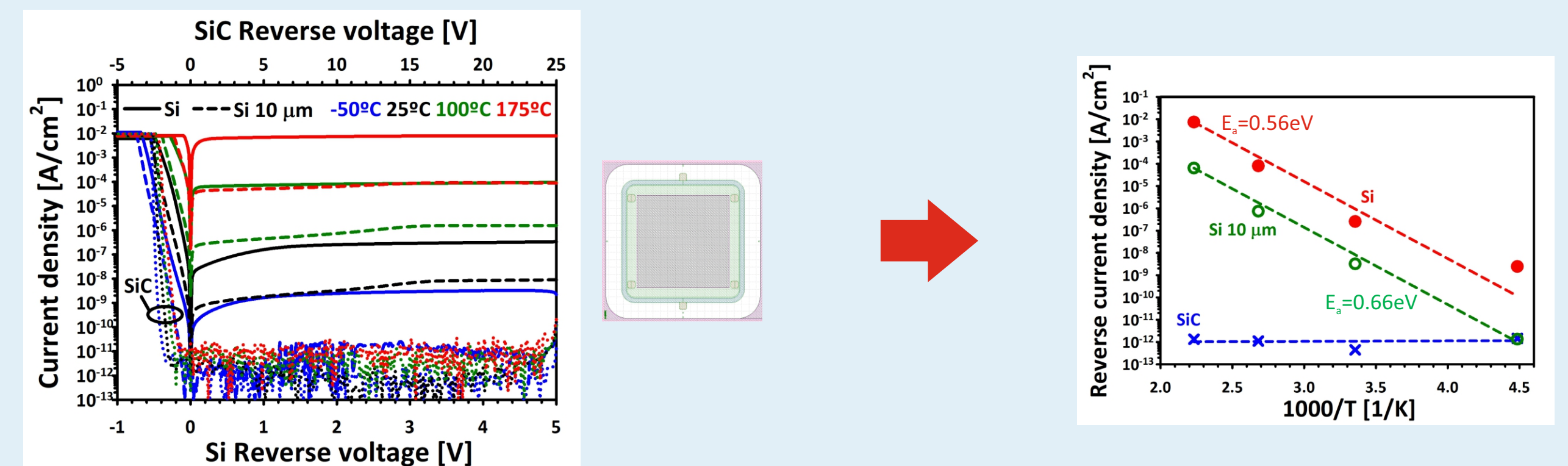
Inspection: **Optical Microscopy**, **Scanning Electron Microscopy** (SEM), **Energy Dispersive X-ray Spectroscopy** (EDX), **Focused Ion Beam** (FIB)



Summary: Four-quadrants diodes have been fabricated on **bulk Si**, **ultrathin Si** and **epitaxied SiC** substrates. The **improved performance** shown by **SiC** devices at **variable temperature** and **visible light illumination** conditions could simplify some **applications experiments** in which silicon devices are used. However, **radiation-induced behavior** at high irradiation fluences, as well as **further SiC technology** developments to process on high resistive (semi-insulating) bulk SiC substrates, **need to be further studied**

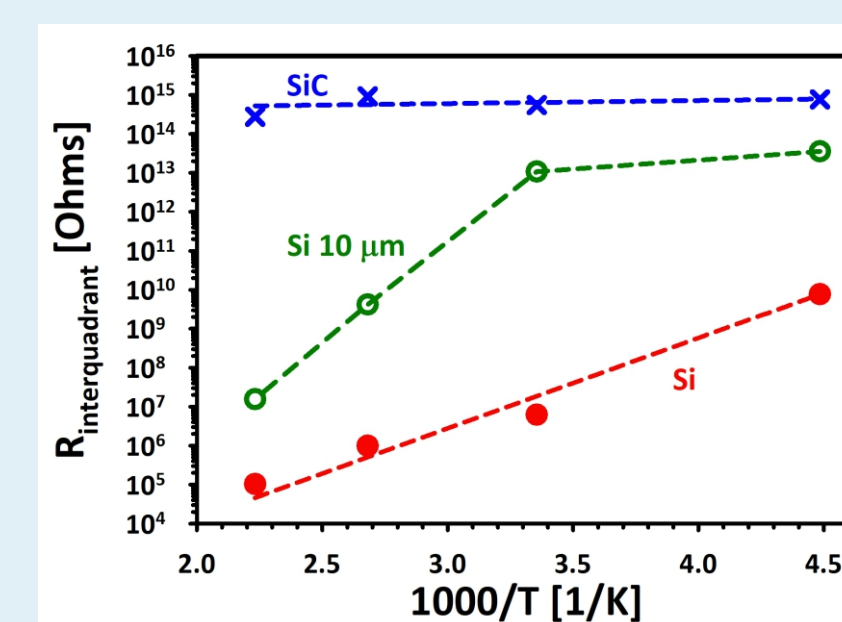
Electrical characterization & 2 MeV e- irradiation

Temperature effects

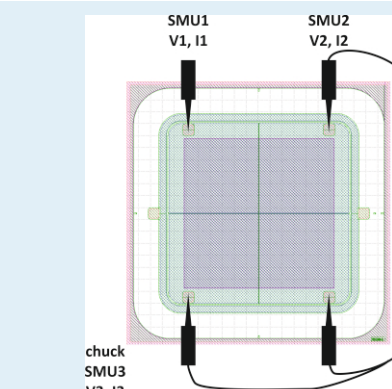


$$T \uparrow \Rightarrow \begin{matrix} \text{Si} \\ \text{Si 10 } \mu\text{m} \end{matrix} \begin{matrix} I_{\text{reverse}} \uparrow \\ I_{\text{direct}} \uparrow \end{matrix} \quad \text{SiC} \quad \begin{matrix} I_{\text{reverse}} \leftrightarrow \\ I_{\text{direct}} \uparrow \end{matrix} \quad \begin{matrix} (E_{\text{BSi}} < E_{\text{BSiC}} \approx 3.2 \text{ eV}) \\ (n_i(T) \uparrow \Rightarrow V_{\text{th}}(T) \downarrow) \end{matrix}$$

Reverse bias diode breakdown (>40 V for Si 10 µm, >100 V Si & SiC)



$$R_{\text{interquadrant}} = \frac{1}{dI_1/dV_2}$$



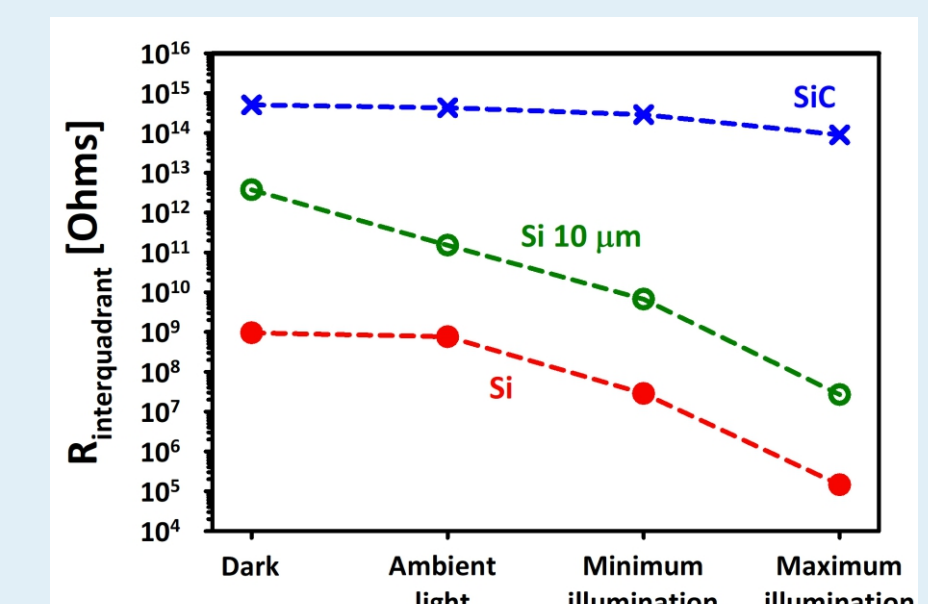
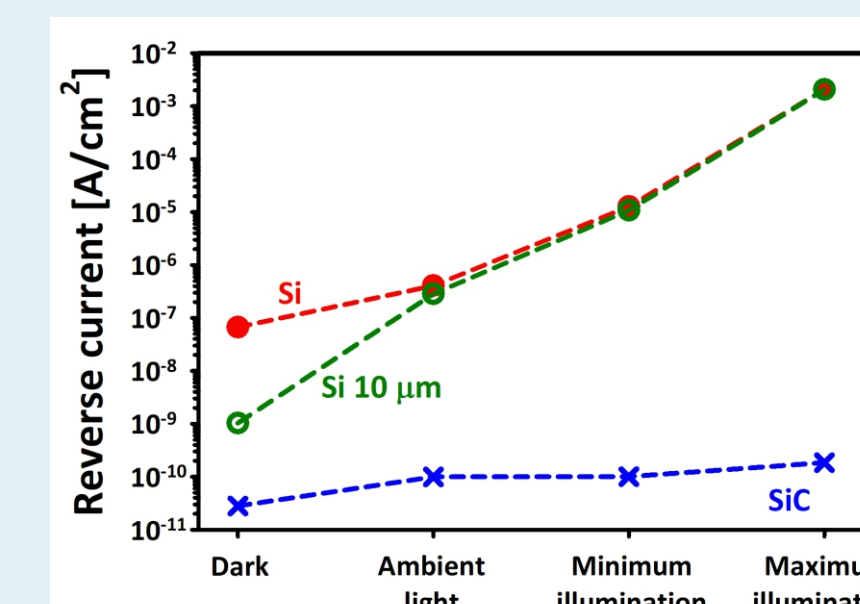
$$T \uparrow \Rightarrow \begin{matrix} \text{Si} \\ \text{Si 10 } \mu\text{m} \\ \text{SiC} \end{matrix} \begin{matrix} R_{\text{interquadrant}} \downarrow \\ R_{\text{interquadrant}} \downarrow \\ R_{\text{interquadrant}} \leftrightarrow \end{matrix}$$

Visible light effects

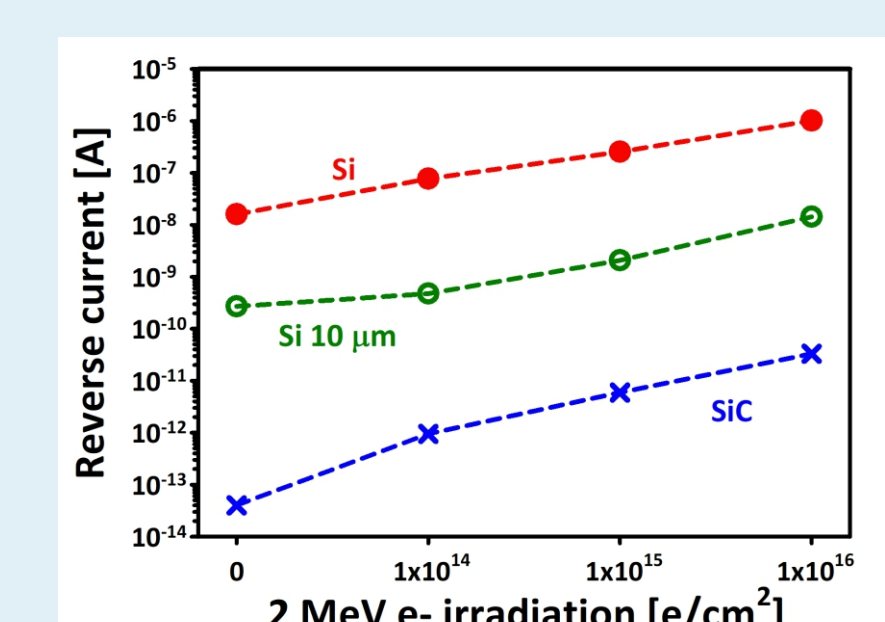
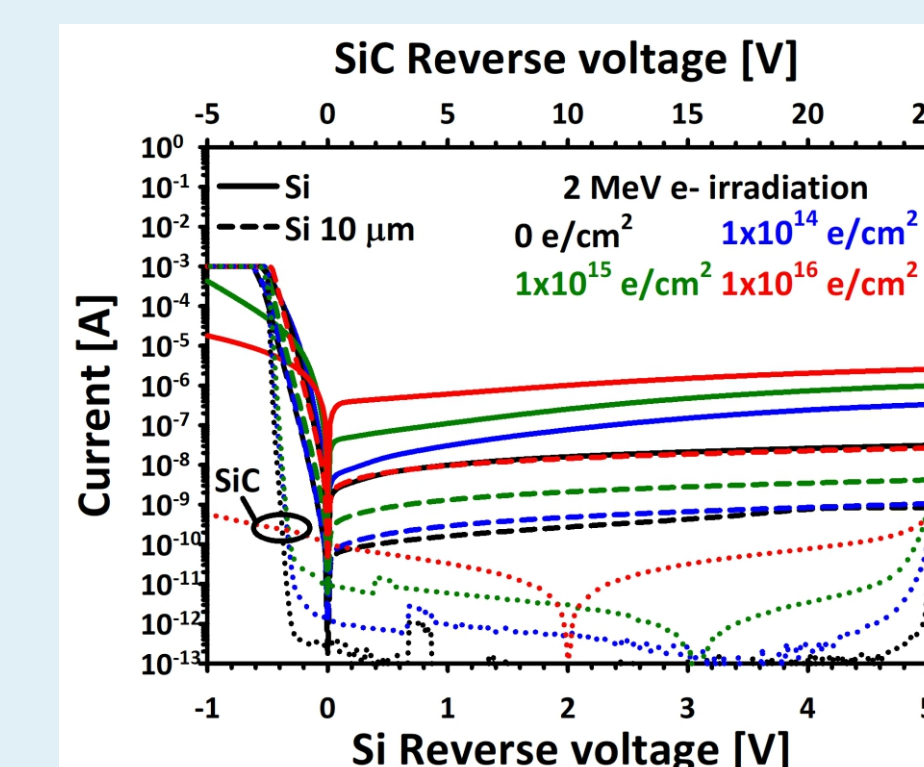


$$\text{Visible light} \uparrow \Rightarrow \begin{matrix} \text{Si} \\ \text{Si 10 } \mu\text{m} \\ \text{SiC} \end{matrix} \begin{matrix} I_{\text{reverse}} \uparrow \\ I_{\text{reverse}} \uparrow \\ I_{\text{reverse}} \leftrightarrow \end{matrix} \begin{matrix} R_{\text{interquadrant}} \downarrow \\ R_{\text{interquadrant}} \downarrow \\ R_{\text{interquadrant}} \leftrightarrow \end{matrix}$$

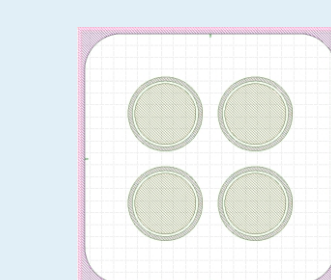
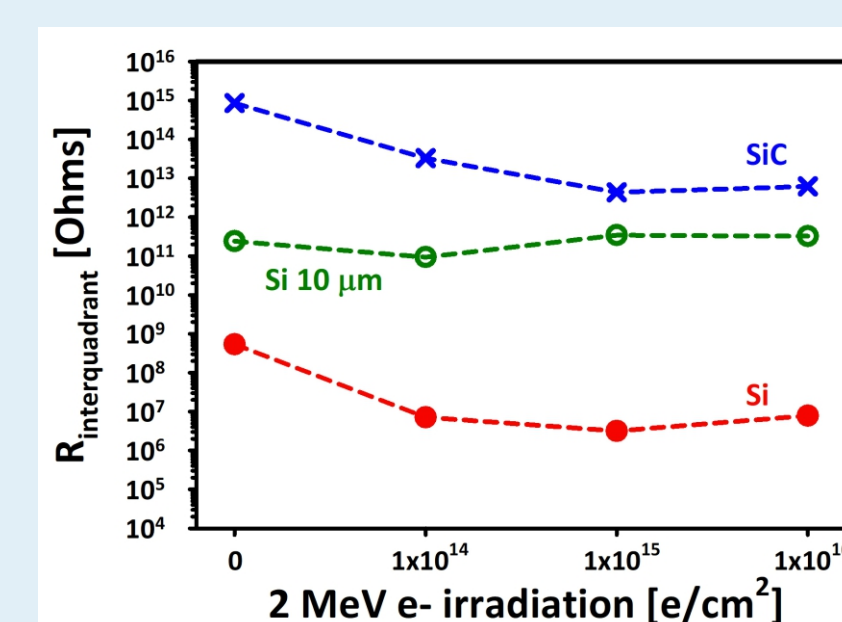
Visible light: 400 nm-700 nm (3.1 eV-1.65 eV)



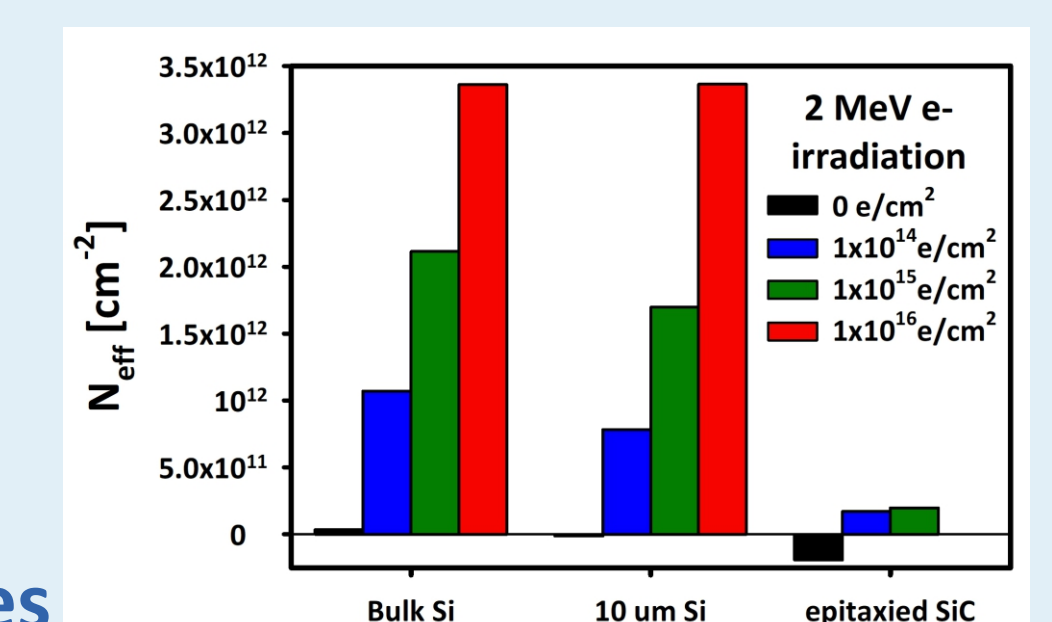
2 MeV e- irradiation effects



e-irradiation $\Rightarrow I_{\text{reverse}} \uparrow$ **Si, Si 10 µm & SiC**
Relatively low I_{reverse} in **SiC** diodes for 1×10^{14} and 1×10^{15} e/cm^2
however, **no clear diode** for 1×10^{16} e/cm^2 (**needs further SiC studies**)
Si bulk: e-irradiation $\uparrow \Rightarrow I_{\text{direct}} \downarrow$ ($R_{\text{series}} \uparrow$?)



MOS C-V
Isolation
oxide charges



e-irradiation \Rightarrow **Si, & SiC** $R_{\text{interquadrant}} \downarrow$, **Si 10 µm** $R_{\text{interquadrant}} \leftrightarrow$
positive charge buildup in **Si & Si 10 µm** isolation dielectric ($\text{SiO}_2 + \text{Si}_3\text{N}_4$)