## The 26th International Workshop on Vertex Detectors



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## **Edge-TCT Characterisation on TowerJazz CMOS Sensor Towards the ITK Phase II Upgrade**

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The ATLAS experiment is planning a major upgrade of its tracking detectors during the Phase-II LHC shut down, to better take advantage of the increased luminosity of the HL-LHC. To this end, new CMOS sensors are being developed. The Edge-Tracient Current Technique (Edge-TCT) measurement method and its latest subsequent preliminary results on the TowerJazz 180nm CMOS technology sensor are presented. The suitability of various pixel sizes is assessed including 50um and 10um-sized structures. Charge sharing is quantified using multi-pixel measurements also at various pixel sizes.

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Presenter: SHARMA, Abhishek (University of Oxford (GB))Session Classification: Poster Exposition and coffee break