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## The Timepix family ASICs

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The Timepix chip is composed of a matrix of  $256 \times 256$  square pixels at a pitch of  $55\mu\text{m}$ . It can be programmed on a pixel-by-pixel basis to record particle arrival time, Time-over Threshold (ToT) or particle counts. This has made it a very versatile device and it has been used in the readout of various segmented semiconductor detectors, micro channel plates and different kinds of gas gain grid (GEM, InGrid etc). The chip has been used in a large variety of applications including classroom experiments, space science, space dosimetry, X-ray diffraction and spectroscopy, neutron imaging and finally back to High Energy Physics. This paper will attempt to summarize the Timepix family of chips and its applications.

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