Session Program

10-15 Sept 2017



The 26th International Workshop on Vertex Detectors

Electronics and System Integration

Tuesday 12 September

17:00	Electronics and System Integration Session Location: Hotel Enclave Las Caldas, Asturias, Spain
	17:00-17:25 Advanced cooling techniques Speaker Paolo Petagna
	17:25-17:35 Questions
	17:35-18:00 The NA62 GTK, from silicon microchannel cooling plates to tracking detectors Speaker Alessandro Mapelli
	18:00-18:10 Questions
	18:10-18:35 Microchannel Cooling techniques at LHCb Speaker Oscar Augusto De Aguiar Francisco
18:45	18:35-18:45 Questions

Wednesday 13 September

0	Electronics and System Integration: First block Session Location: Hotel Enclave Las Caldas, Asturias, Spain
	09:30-09:55 RD53A: a Large Scale prototype of a New generation Pixel Readout ASIC for the HL_LHC experiment Speaker
	Roberto Beccherle
	09:55-10:05 questions
	10:05-10:30 Solutions for flip chip bonding of future pixel detectors
	Speaker Mr Sami Vahanen
	10:30-10:40 Questions
	10:40-11:05 3D IC integration
	Speaker Ronald Lipton
5	11:05-11:15 questions
5	Electronics and System Integration: 2nd block Session Location: Hotel Enclave Las Caldas, Asturias, Spain
	11:45-12:10 The Timepix family ASICs
	Speaker Xavi Llopart Cudie
	12:10-12:20 Questions