LHC BLM SYSTEM: SUMMARY OF CHANGES AND RE-COMMISSIONING AFTER EYETS

Machine Protection Panel 07/04/2017

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Overview

Status of checks to be performed during:

- Hardware Commissioning
- Machine Checkout
- Beam Tests
- Resolved issues
- Outstanding issues
- Summary

STATUS OF CHECKS PERFORMED

17/04/2015

Hardware and Machine Checkout

- All Hardware Commissioning and Machine Checkout checks need to be completed successfully.
- Those include:
 - 8 Hardware checks
 - 3 Machine Checkout checks
- See attached xls file for some details (more info will be added) and
- MPS document <u>LHC-OP-MPS-0009 v.3.2</u> for explanations on reasons and conditions
 - New version to be distributed with minor changes

Beam Tests

	Rep.	Action								
	S	Validate Interlock Request functionality of the BLM crates.								
	FW-CS	S Decrease thresholds to very low value (trim application).								
1	FW-TC	Inject low intensity pilots in regular intervals in Beam 1 and/or Beam 2.								
-		At each interval close sequentially collimator jaws around the ring in order to force different BLM crates to trigger interlock requests.								
		Aim to have the majority of crates at least once.								
		Time estimate: 2h								
		Validate Interlock Request functionality of the BLETC modules.								
		Decrease thresholds to very low value (trim application).								
2		Inject a low intensity pilot in Beam 1 and/or Beam 2.								
2	FW-CF	Create a local bump until an interlock request is sent by the system.								
		Aim to have up to the 1.3 s Running Sum measurement over its Threshold value.								
		Time estimate: 1h								
		Measure the interlock request system latency.								
		Decrease thresholds to very low value (trim application).								
3		Close one TCP collimator jaw in Point 3 and a second in Point 7 in order to block completely the passage of beam.								
3	FW-CF	Inject a low intensity pilot in Beam 1 and Beam 2.								
		Calculate the system latency to initiate an interlock request by making the difference between the timestamps recorded by the BIS and the Injection								
		Kicker. (for added accuracy the propagation delays can be removed for the beam to reach from the injection region to the detector the requested the								
		interlock)								
		Time estimate: 2h								
	S	Test interface of direct BLMs with the beam dumping system (same test as 7.3.4 of [1]).								
		Reduce the voltage setting of the abort threshold.								
		Dump the injected beam on the collimator TCDQ and TCSG (with local bump). The threshold must have been lowered sufficiently, to provoke a beam								
		dump request.								
4		Record the beam dump.								
		This test must be repeated for each beam and for both TCDQ and TCSG.								
		From the amount of lost beam and the BLM reading, deduce the nominal threshold setting.								
		Are there variations with respect to the impact conditions?								
		Measure delay between the time where the loss signal exceeds the threshold and the time of the beam dump (time stamps in logging DB). <i>Time estimate: 2h (without the two accesses: could be scheduled during injection tests)</i>								
	S. O	Verify Injection Interlock Inhibit functionality								
	3, 0	Tests with pilot beam during commissioning of injection protection system.								
		Create losses above dump threshold and modify blindout time								
		Record interlock input from blindable/non-blindable crates								
5		If losses above dump threshold cannot be reached, lower the monitor factor of blindable crates BLMs								
Э		Tests with trains (288 b or what is being used for scrubbing)								
		Tighten TCDIs from 5 sig to 4.5 sig (likely settings for Hilumi)								
		Tighten monitor factor								
		Time estimate: 1h (first time might be more)								

Injection Interlock Inhibit

Tests with pilot beam

- During commissioning of injection protection system
- Create losses above dump threshold
- Modify blindout time
- Record interlock input from blindable/non-blindable crates
- If losses above dump threshold cannot be reached, lower the monitor factor of blindable crates BLMs
- Tests with trains
 - 288 b or what is being used for scrubbing
 - Tighten TCDIs from 5 sig to 4.5 sig (likely settings for Hilumi)
 - Tighten monitor factor

Criteria for successful test:

- Functionality: detectors in blindable crates DO NOT interlock within given blindout time and losses above threshold
- Redundancy: detectors in non-blindable crates DO interlock in case losses go above threshold on those, while blindable ones do not interlock
- Inhibit time: get feeling for which blindout time is needed not critical, can be adjusted later

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RESOLVED ISSUES

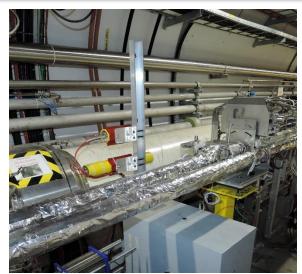
Issues resolved to date

Tunnel Installation

ECR <u>LHC-BLM-EC-0008</u> v.0.1 summarises the changes in the tunnel installation and the name changes during EYETS

In summary:

- Removal of IP1 D1 detectors
- New or renames of detectors for TCPCV.A6R7.B2, TCPCH.A5R7.B2 and TCSPM.D4R7.B2
- New detectors for ATLAS/AFP
- Rename of detectors next to ALICE BCMs



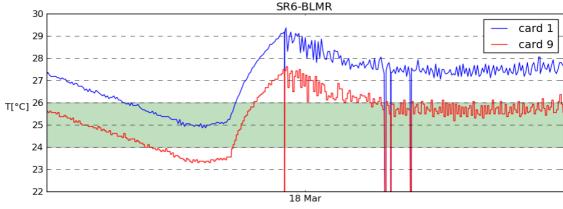


Rack Waterflow Valves

BIBML-1205 & BIBML-1298:

Replaced damaged waterflow valves of the thermalized rack in SR6 and SR7





HV Power Supplies

BIBML-1257: HV PSU Issues

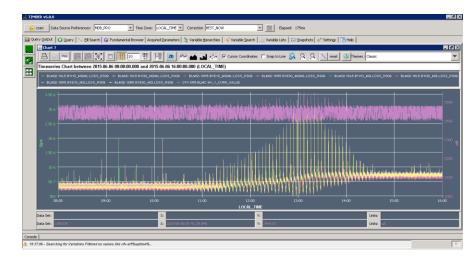
Investigated deeper the issues seen with the high voltage power supplies of the detectors such as:

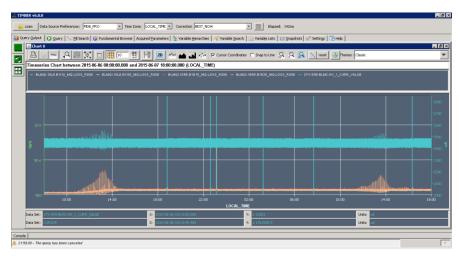
BIBML-1256 BIBML-1036 BIBML-1037

which have costed several hours of downtime.

- Replacement of some components
- Redesign of the output stage

New design given to the manufacturer and replaced all PSUs.





BOBR (BST Timing)

BIBML-1310: Several BOBR modules with old firmware found

- Added a test to check firmware version
- Several crates found running old firmware (7/27)

Firmware was upgraded to latest release

Some hardware discrepancies have been detected in cfv-sr8-blmr captured by feedback event on 03-APR-2017 14:59:26: MODULETYPE SLOT SUBSLOT FIRMWARE_VERS ID DRIVER_VERS STATUS ERROR MSG Firmware version BDI BOBR 12 0x33 DISCREPANCY discrepancy!! Value expected: 0X35 For more details, you can consult the report in CC Data Browser with the latest feedback clicking on the following link: https://apex-sso.cem.ch/pls/htmldb_dbabco/f?p=CONFIG_BROWSER:FEC_CONFIG_FEEDBACK:::::P180_DSC:cfvsr8-blmr You can find more information about these HW Discrepancy Feedback notifications and why you have received this email in the following page: https://wikis.cem.ch/display/config/CCS+Domains#CCSDomains-DriverConfigFeedback This email has been sent automatically. For any further questions please contact the Controls Configuration Support, Visit CCS portal at: ccdb.cern.ch or our Wikis.

RBAC & Concentrators

- RBAC roles have been revised
 - All not necessary or old roles cleaned
 - Will need to test during dry-runs
- Concentrators have been redelivered
 - Were blocked due to library changes

Thanks to CO for the support

Some disconnections and timing missing is seen:

Console Running tasks Status Logger Ustration - No acquisition update since 30 seconds	
09:02:59 - No acquisition update since 31 seconds 09:02:59 - No acquisition update since 32 seconds	
09:02:59 - No acquisition update since 32 seconds	
D9:02:59 - No acquisition update since 33 seconds	
09:02:59 - No acquisition update since 34 seconds	
09:02:59 - No acquisition update since 73 seconds	
09:02:59 - JAPC exception with for parameter name TGM.DIST.LHC.TN.2/Event. Description= RdaException	
cern.japc.SubscriptionProblemException: Disconnected from 'TimingEvtDistributor_RC_DU.cfc-ccr-ctidelhc' :no heart-beat from ser Caused by:	rver 'tcp://cfc-ccr-ctidelhc:6922/19'
cern.cmw.rda3.common.exception.TransportException: Disconnected from 'TimingEvtDistributor_RC_DU.cfc-ccr-ctidelhc' :no heart-be	eat from server 'tcp://cfc-ccr-ctidelhc:6922/1
09:02:59 - No acquisition update since 74 seconds	
09:02:59 - JAPC exception with for parameter name TGM.DIST.LHC.TN.1/Event. Description= RdaException	
cern.japc.SubscriptionProblemException: Disconnected from 'TimingEvtDistributor_DU.cfc-ccr-ctidelhc' :no heart-beat from server	r 'tcp://cfc-ccr-ctidelhc:2835/18'
Caused by:	
cern.cmw.rda3.common.exception.TransportException: Disconnected from 'TimingEvtDistributor_DU.cfc-ccr-ctidelhc' :no heart-beat	from server 'tcp://cfc-ccr-ctidelhc:2835/18'
09:02:59 - No acquisition update since 77 seconds	
09:02:59 - LHC TGM Time Out	
09:02:59 - No acquisition update since 78 seconds	
09:02:59 - No acquisition update since 79 seconds	
09:02:59 - No acquisition update since 80 seconds	
09:02:59 - No acquisition update since 81 seconds	
D9:02:59 - No acquisition update since 82 seconds	
09:02:59 - No acquisition update since 83 seconds	
🔕 09:02:59 – No acquisition update since 83 seconds	

OUTSTANDING ISSUES

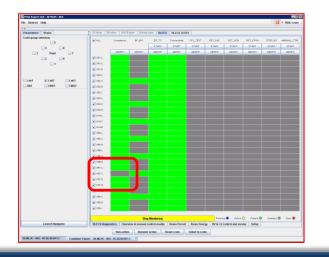
17/04/2015

Sanity Checks

<u>BIBML-937</u>: Sanity Checks' result in the Sequencer does not always gets registered in the BLECS

- Several calls from the CCC that checks cannot complete
- MCS Check had passed (thus the Sequencer showed as green, but the BLECS did not get the result as TRUE to release the permit). During the retries, MCS was being skipped.

Not yet sure what is the cause.



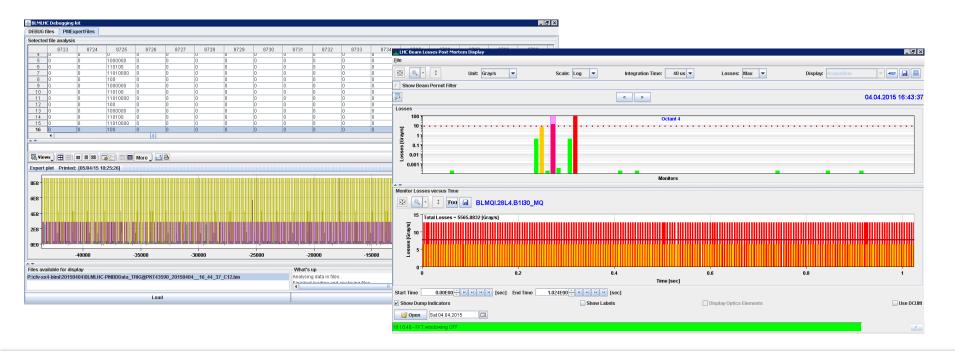
Beam monit	Application or			0000989898989898989	**********************		***********************	000000000000000000000000000000000000000		* " ຊີ 🗵	
Accelerator	Mode:	ACCESS				Beam 1 Present: NO					
Beam Mode		CYCLING				Beam 2 Present: NO			NO		
					Global Status						
		Check	being r	un at the	momen	nt 14.04	.2015 16	:49:55			
	Checks connected to BIS MCS Sanity Checks			Expert checks					BIS team		
Crates	Consistency	Connectivity	Internal Beam Permit	CFC_TEST	RST_DAC	RST_GOH	RST_FPGA	STOP_HV	MANUAL_C	External CTR Beam Permit	
SR1.L SR1.C											
SR1.R											
SR2.L SR2.C											
SR2.C											
SR2.I											
SR3.L											
SR3.C											
SR3.R											
SX4.L											
SX4.C											
SX4.R											
SR5.L									. •		
SR5.C						n th	ie m	lear	ntin	ne ai	dded a
SR5.R SR6.L								Cui	ICIII	nc, u	uucu u
SR6.C											
SR6.R					C	`+~+.		nnli	icat	ion +	o show
SR7.L						oldli	us d	มมเ	ILdl	ווטו	U SHOW
SR7.C								I- I			
SR7.R											
SR7.E						ho o	state	t∩ ב	62	ch cr	ato
SR8.L					L L		Juan		Ca		all
SR8.C											
SR8.R SR8.I											

Post-Mortem Data 1/2

BIBML-881: In few cases the GPM Buffer is not working correctly

- The issue is on the SRAM recording of the data.
- The spikes seen are the markers between channels.

Card has been exchanged (Note: had 5 similar cases in Run 1)



Post-Mortem Data 2/2

BIBML-1249:PM data sometimes not in sync between crates

- In few of the PM data recorded it is visible 1 ms misalignment
- A common factor on those observed events is that the BeamDumped1 & 2 events had a ms difference
- One possible explanation is that the PM data for the misaligned crates have been readout from a different buffer than the rest





Summary

Commissioning:

- Hardware and Machine checkout checks ongoing
- Tests with beam to be planned
 - New test for the 'blindable' channels

System Issues:

- No machine protection critical issues
- Some work still needed to bring the system into operational mode