

Session Program

16-20 Oct 2017

HEPiX Fall/Autumn 2017 Workshop
Computing and batch systems

KEK

1-1 Oho, Tsukuba, Ibaraki 305-0801 Japan 36°09'01.0"N 140°04'28.1"E 36.150290, 140.074485

Monday 16 October

17:50

18:15

Computing and batch systems

Session |

Location: KEK, 1-1 Oho, Tsukuba, Ibaraki 305-0801 Japan 36°09'01.0"N 140°04'28.1"E 36.150290, 140.074485

17:50-18:15

HEP Community White Paper

Speaker

Michel Jouvin

Wednesday 18 October

09:30

Computing and batch systems

Session |

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09:30-09:55

Integrating HPC and HTC at BNL -- a year later

Speaker

Dr Tony Wong

09:55-10:20

Techlab update

Speaker

Romain Wartel

10:20-10:45

HEPiX Benchmarking Working Group - Status Report October 2017

Speaker

Manfred Alef

10:45

11:15

Computing and batch systems

Session |

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11:15-11:25

Running Jobs Everywhere: an Overview of Batch Services at CERN

Speaker

Jerome Belleman

11:25-11:50

Migration from Grid Engine to HTCondor

Speaker

Thomas Finnern

11:50-12:15

Migrating a WLCG tier-2 to a Cray XC-50 at CSCS-LCG2

Speaker

Francesco Giovanni Sciacca

12:15-12:40

Optimising the resource needs for the LHC computing: ideas for a common approach

Speaker

Andrea Sciaba

12:40