

Investigation of modified ATLAS pixel implantations

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Andreas Gisen

Silke Altenheiner, Ingo Burmeister, Sascha Dungs, Claus Gößling,
Marius Grothe, Reiner Klingenberg, Kevin Kröninger, Raphael Michallek,
Anna-Katharina Raytarowski, Mareike Weers

GEFÖRDERT VOM



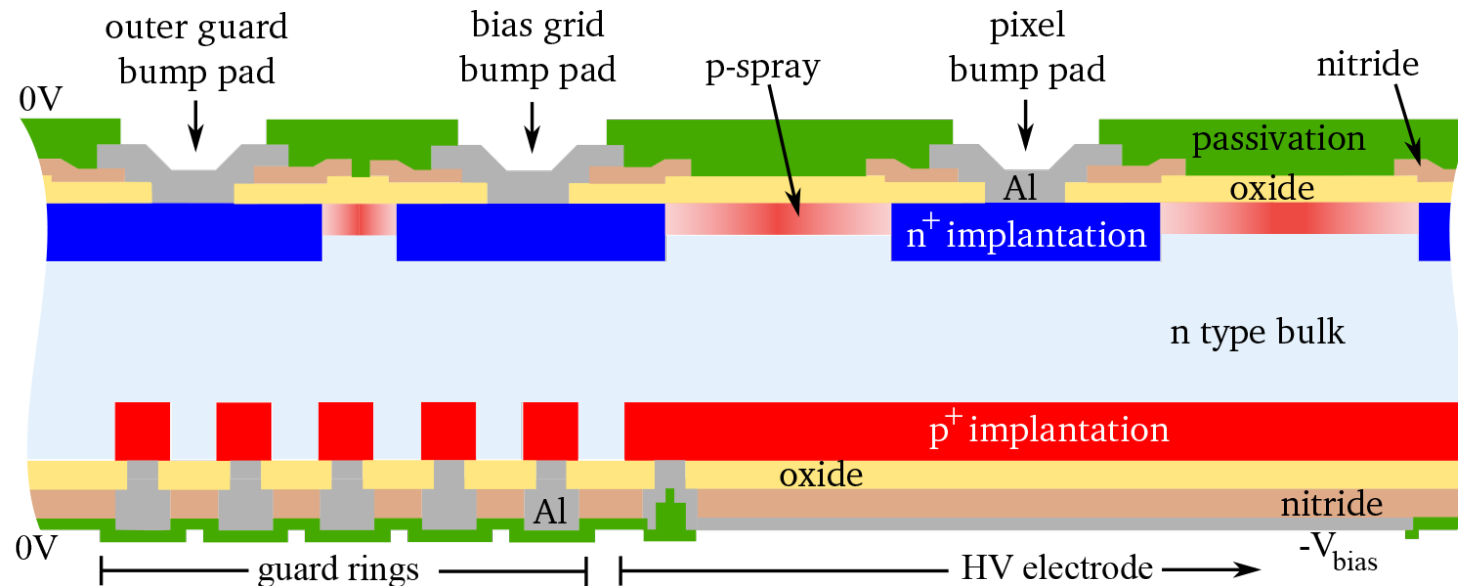
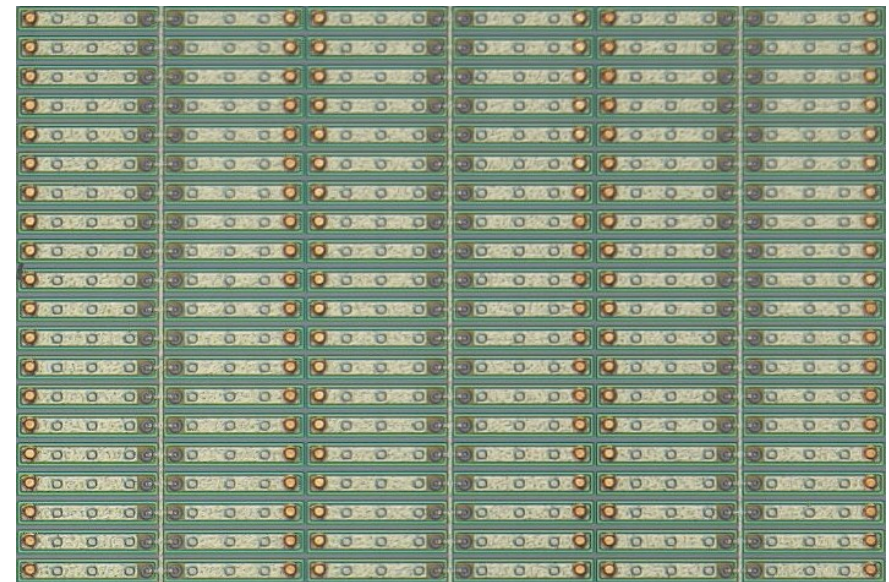
Bundesministerium
für Bildung
und Forschung



- 3-layer detector sensors:
 - Up to $1 \times 10^{15} \text{ n}_{\text{eq}} \text{ cm}^{-2}$
 - 250 μm thick
 - pixel pitch $400 \times 50 \mu\text{m}^2$
 - FE-I3 ASIC
- 4th layer (IBL) sensors:
 - Up to $5 \times 10^{15} \text{ n}_{\text{eq}} \text{ cm}^{-2}$
 - 200 μm thick
 - pixel pitch $250 \times 50 \mu\text{m}^2$
 - FE-I4 ASIC



- n⁺-in-n silicon
 - 200 μm n-type Bulk
 - n⁺ pixel
 - 80 columns x 336 rows
- HV pad & 13 guard rings on p-side
- Double sided wafer process

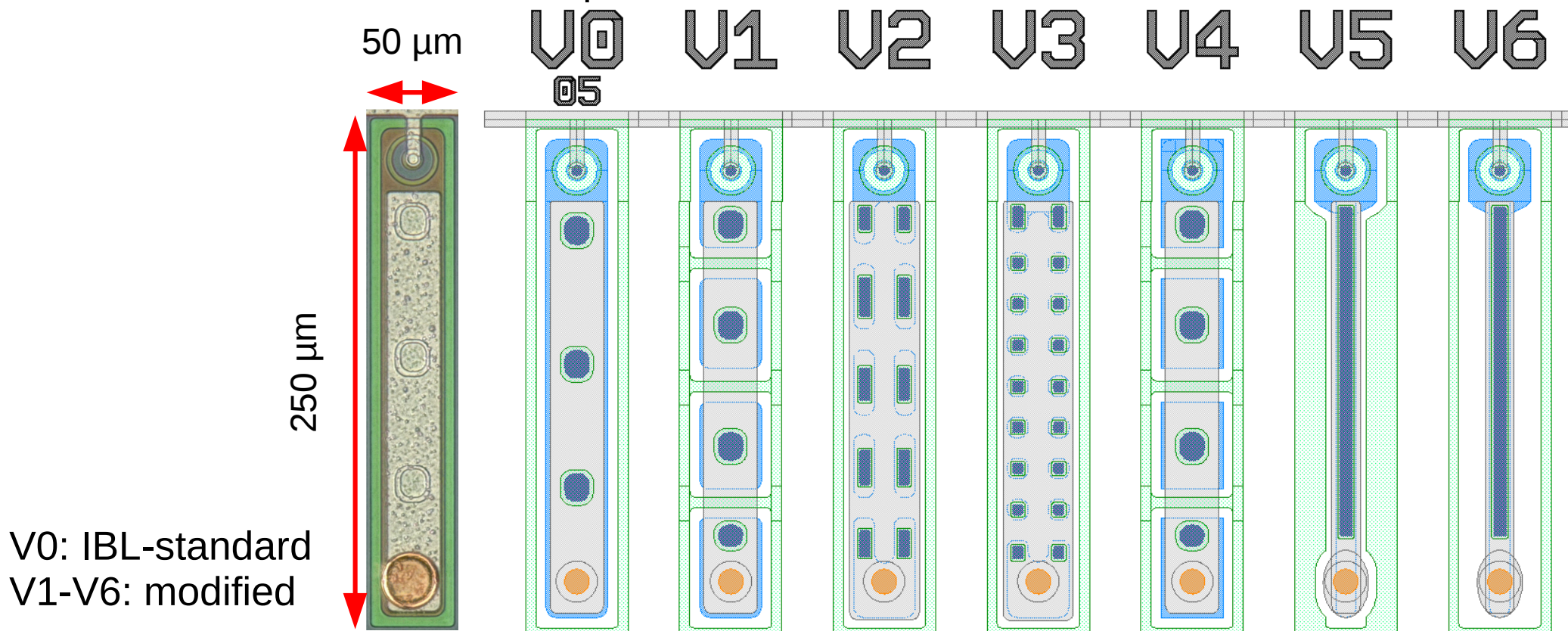


REINER pixel designs

REdesigned, INnovative, Exciting and Recognizable

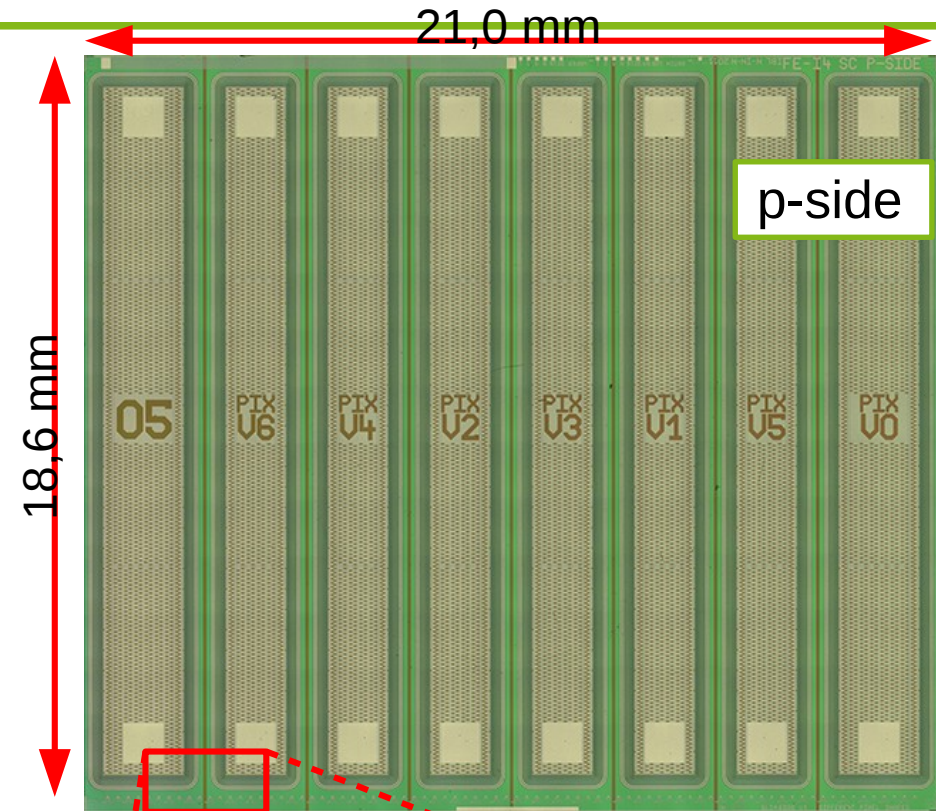
- Six new designs
 - Three divided in 4/10/18 sub implants
 - One with rectangular corners
 - Two with narrowed implant

Blue: n+
Grey: metal
Green: Ni openings

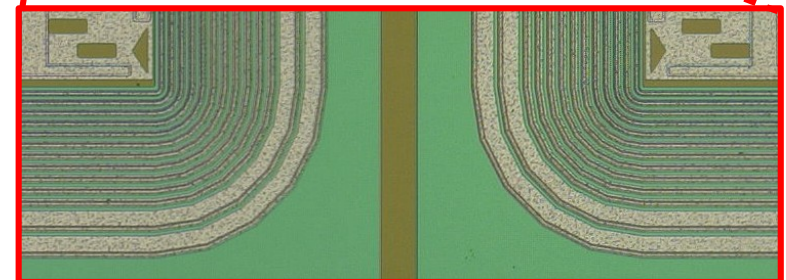
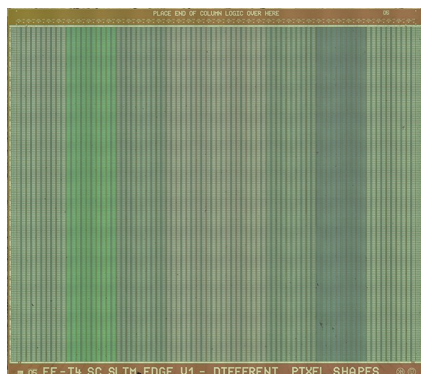


REINER pixel sensors

- Eight structures on one sensor
 - Two IBL designs
 - Six modified designs
- Each structure consists of 10 columns x 336 rows with the same design
- Separate HV pads
- Individual guard rings
- Readout by one FE-I4

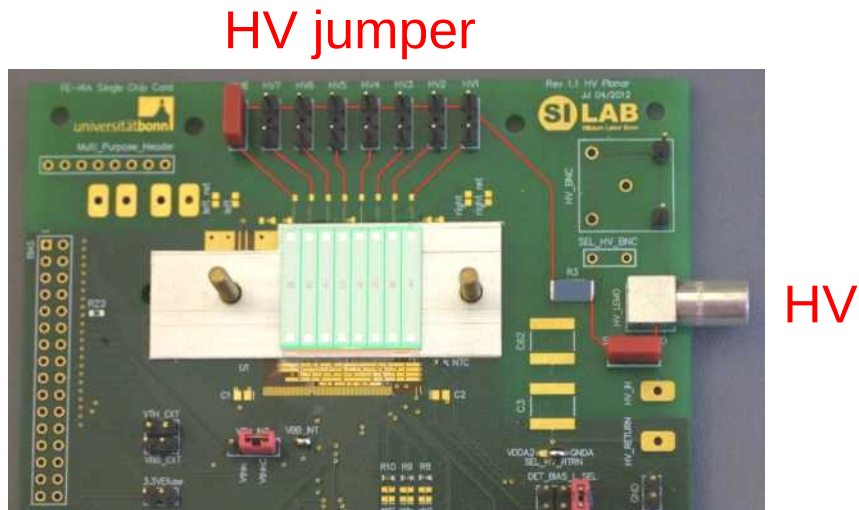


n-side

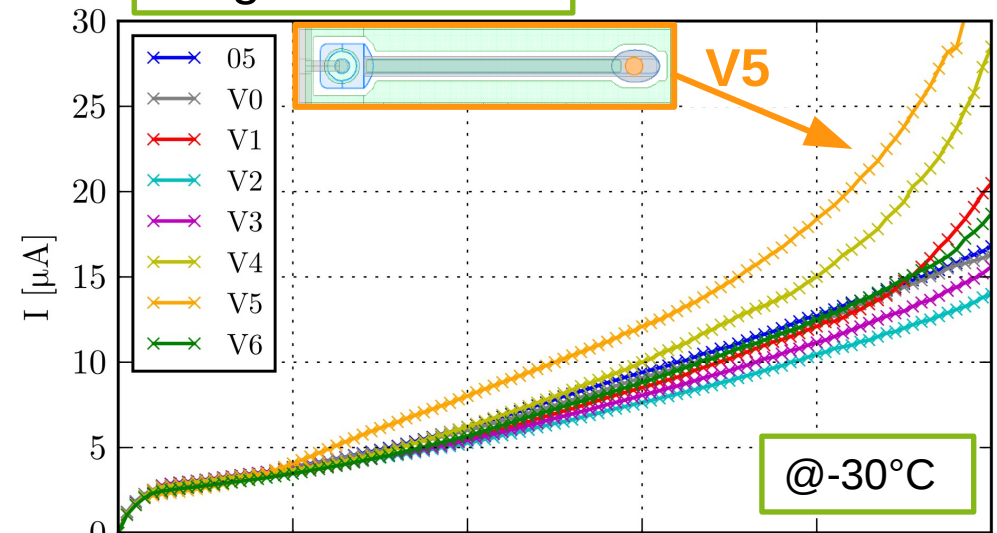


IV (irradiated)

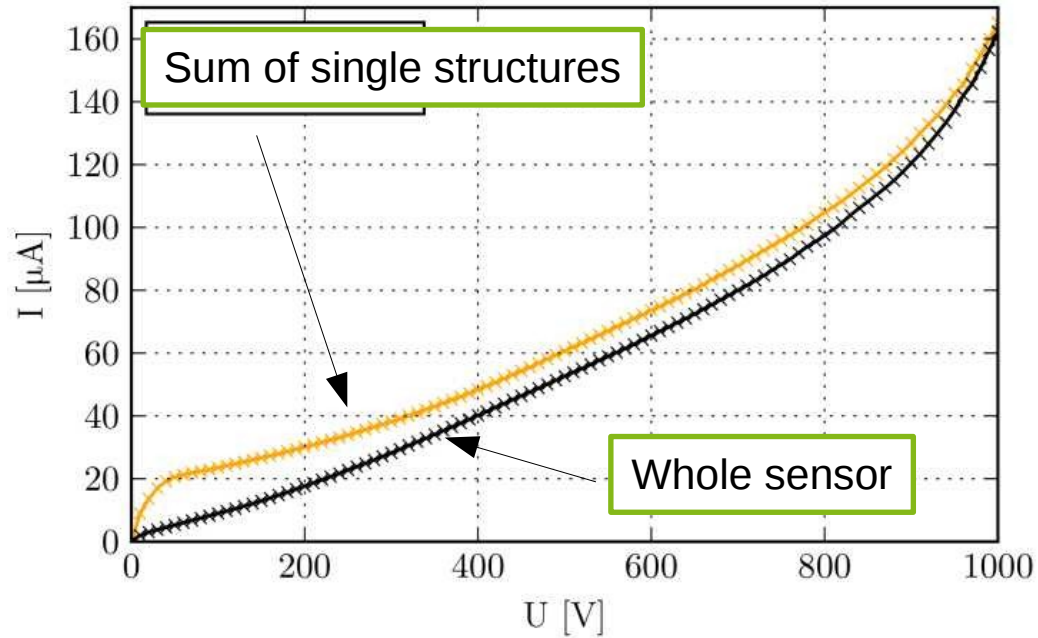
- $\Phi \sim 5e15 \text{ n}_{\text{eq}} \text{ cm}^{-2}$, Sandia ACRR
- Differences between structures
- Sum of single currents is greater than current of whole sensor
- Also observed at unirradiated sensors



Single structures

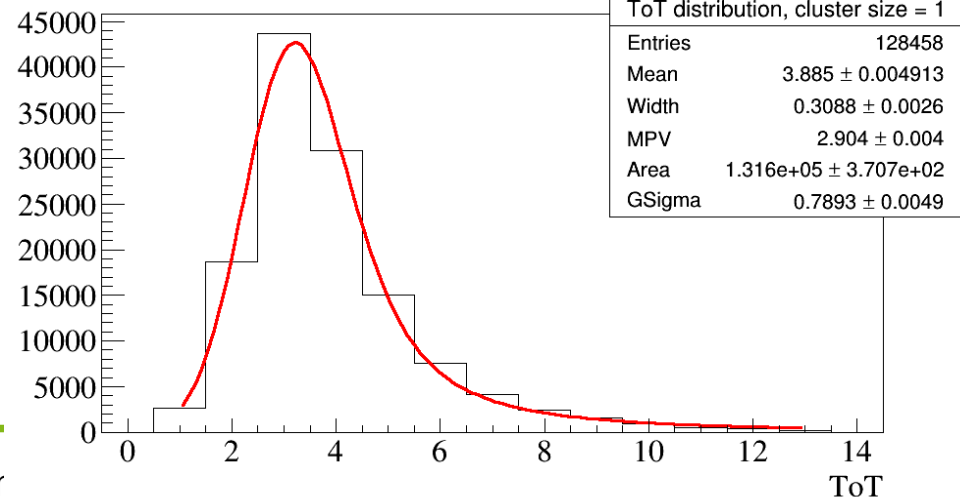
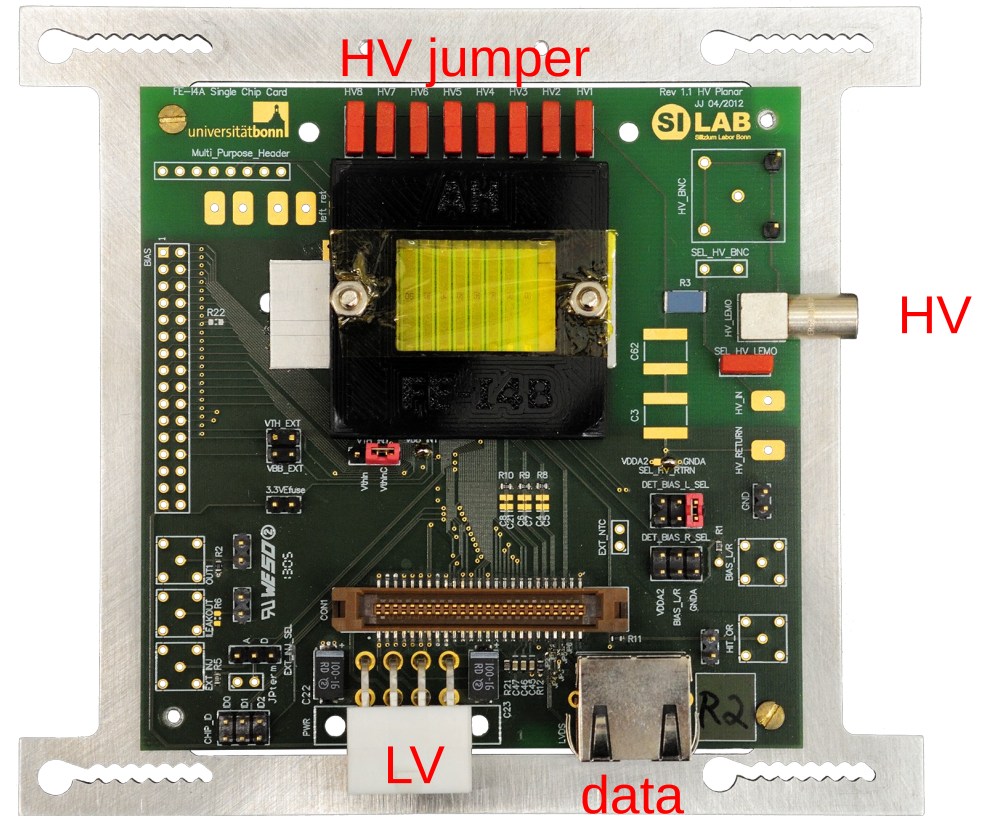
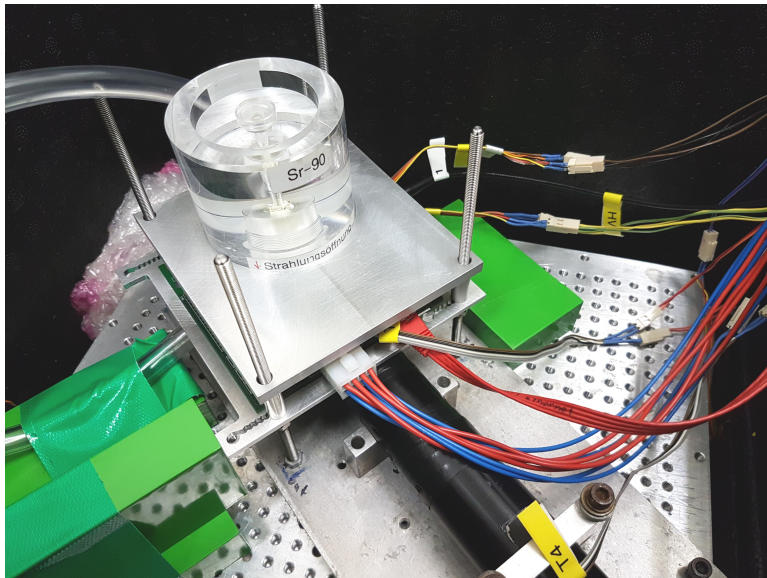


Sum of single structures



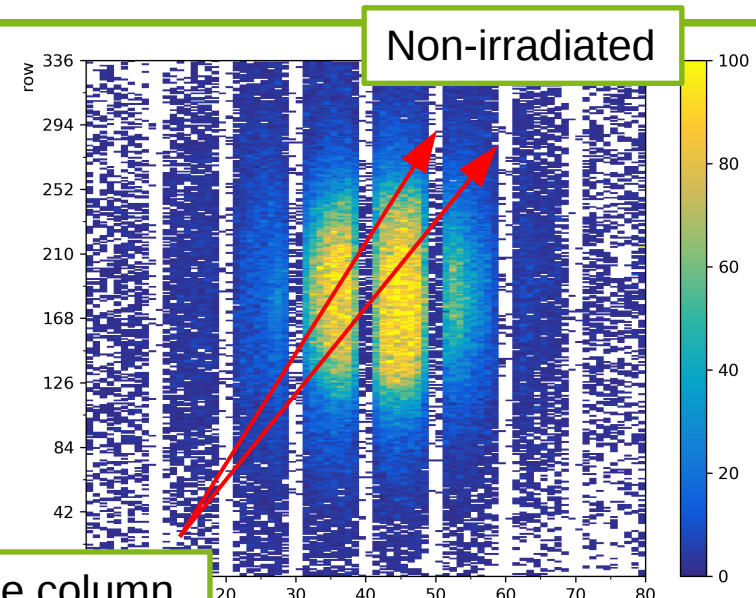
Lab setup source scans

- Special single chip PCB
 - Frame movable by 2,5 mm (10 columns)
- Sr-90 source + trigger scintillator
- Landau-Gaus fit

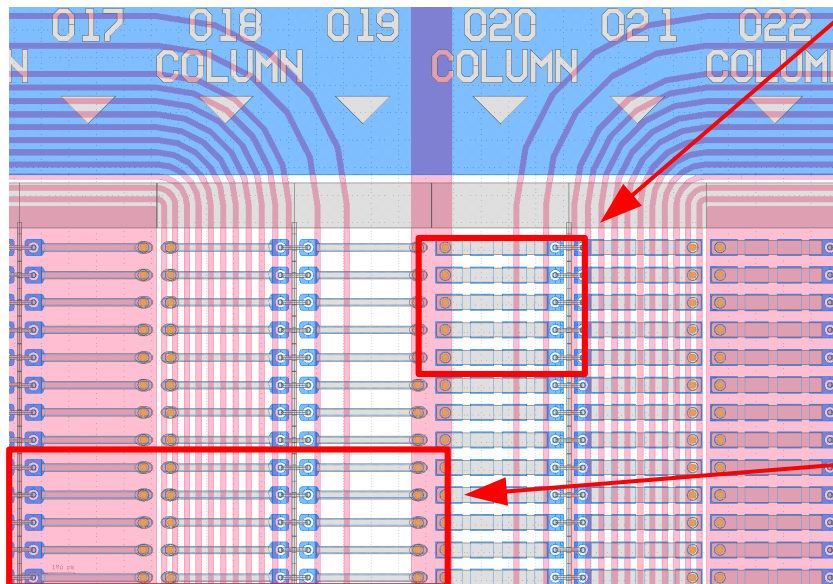


Guard ring design

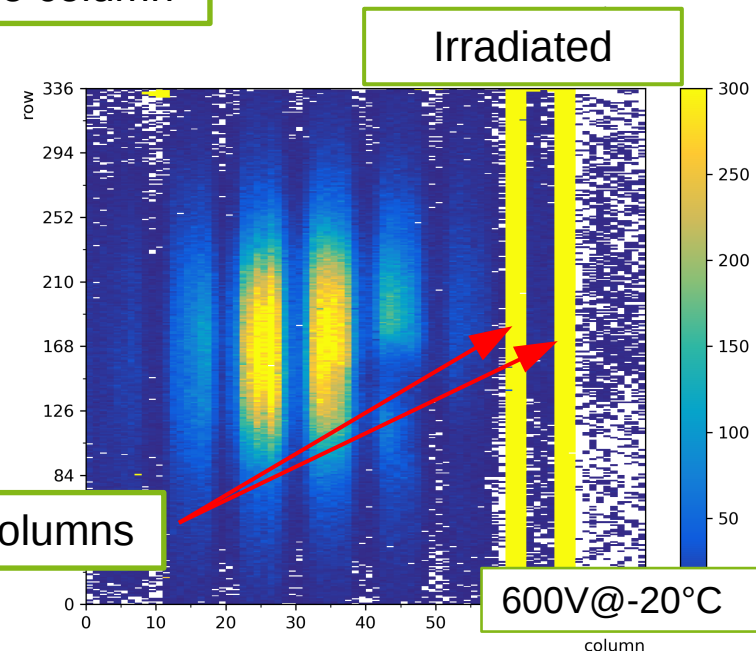
- Expected two grounded columns per design
- Observed current related issues in pixels beneath or near guard rings after irradiation
- Mask 6 columns of each design



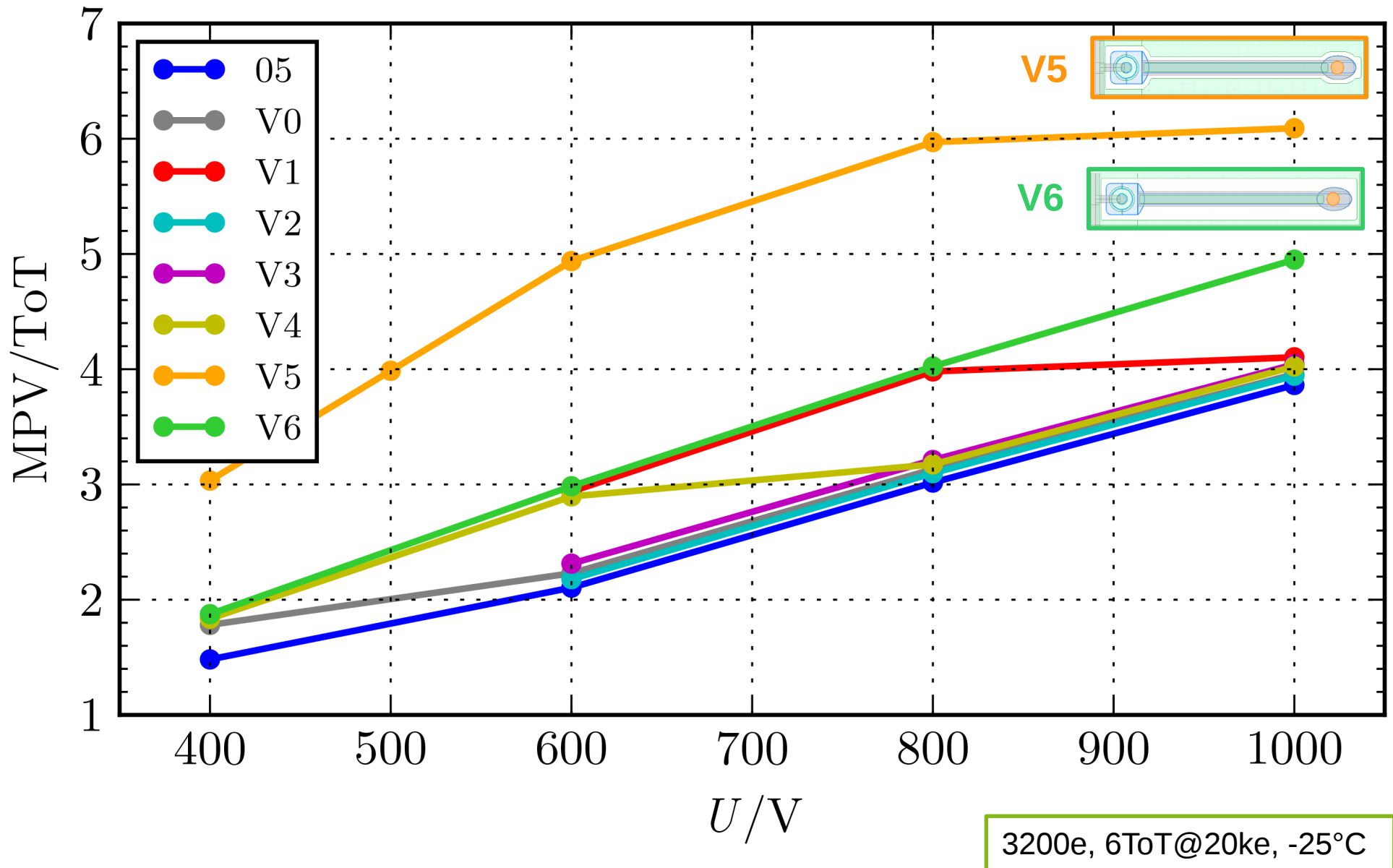
One column



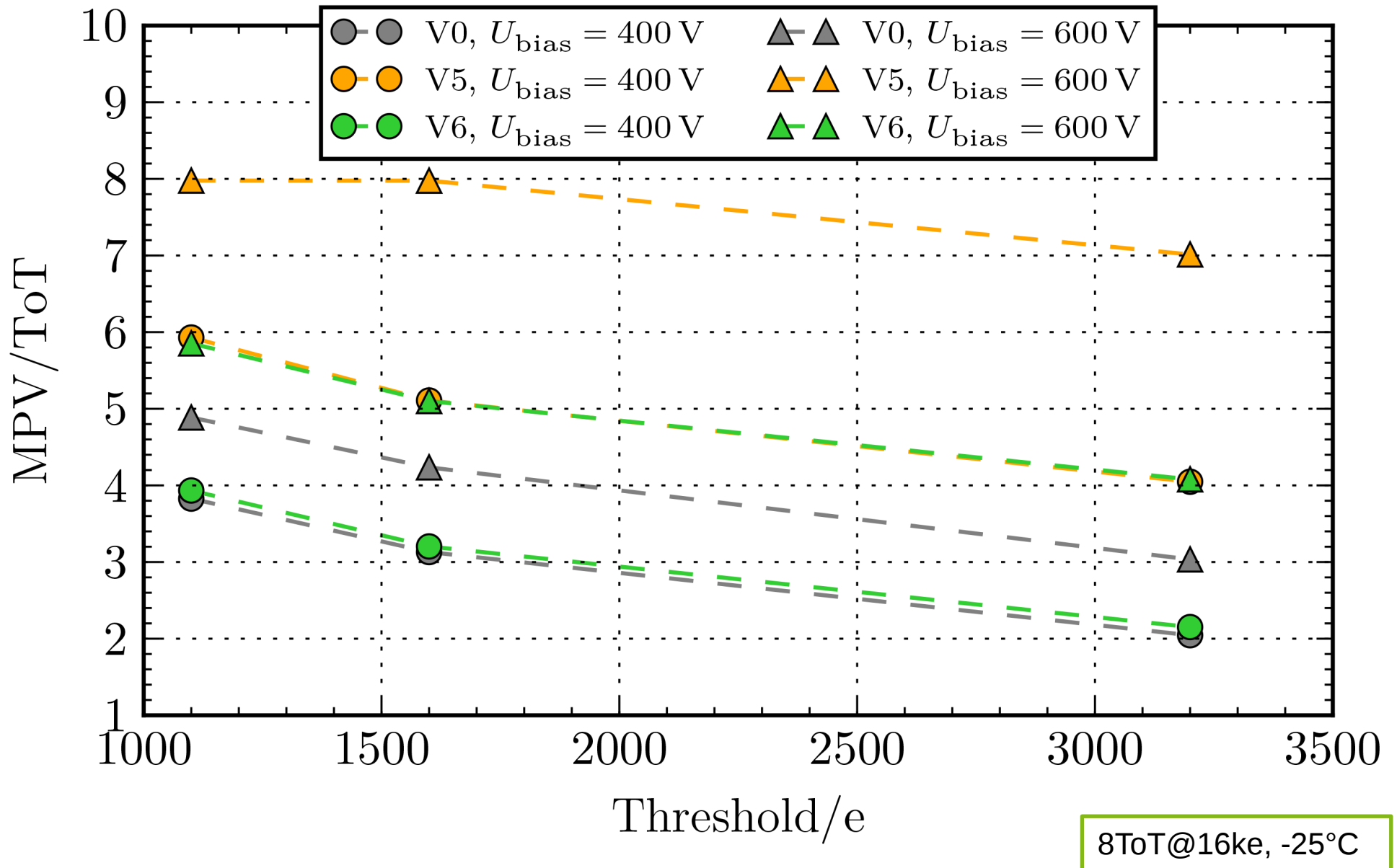
Three columns



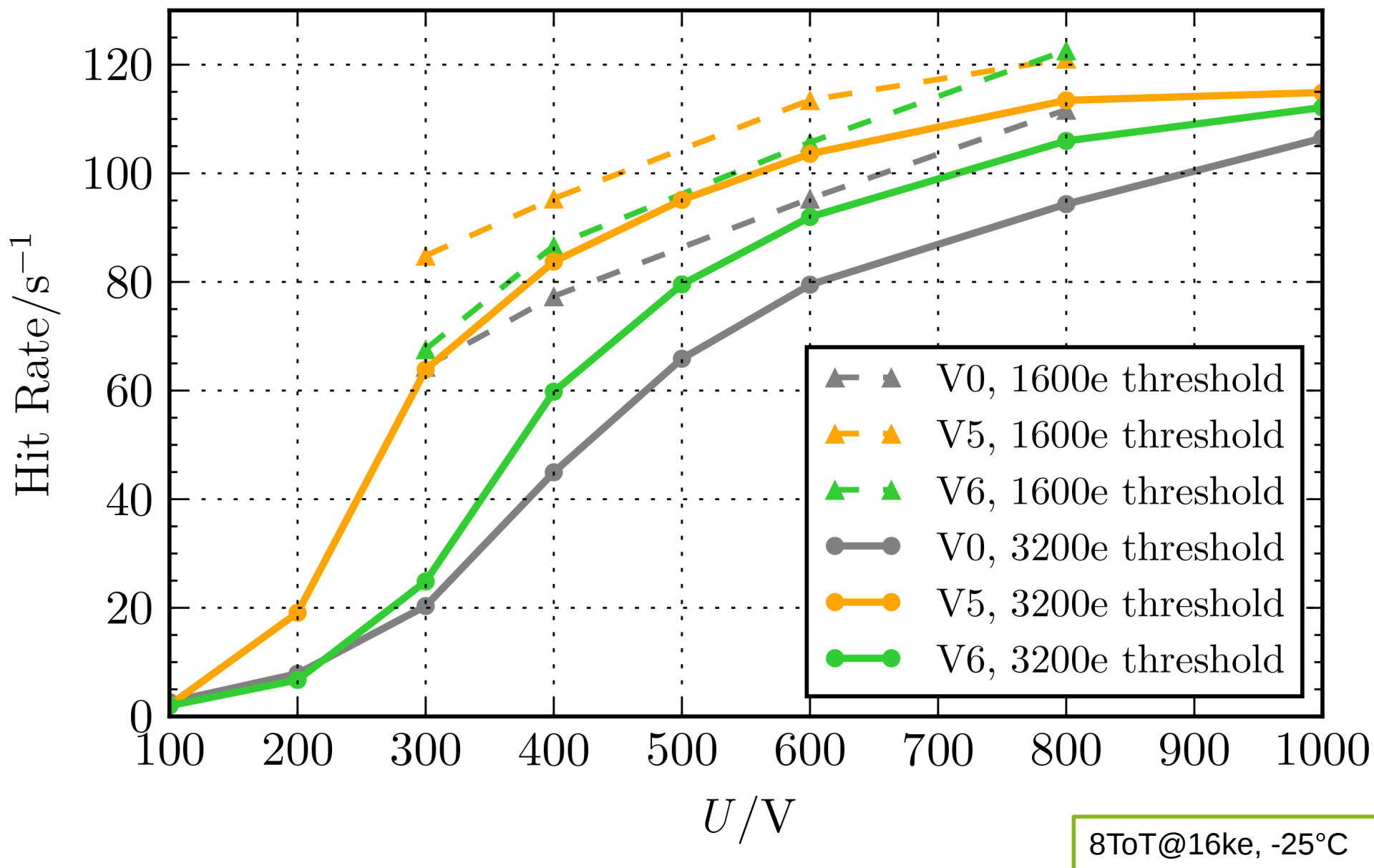
Charge collection vs. voltage



Charge collection vs. tuning

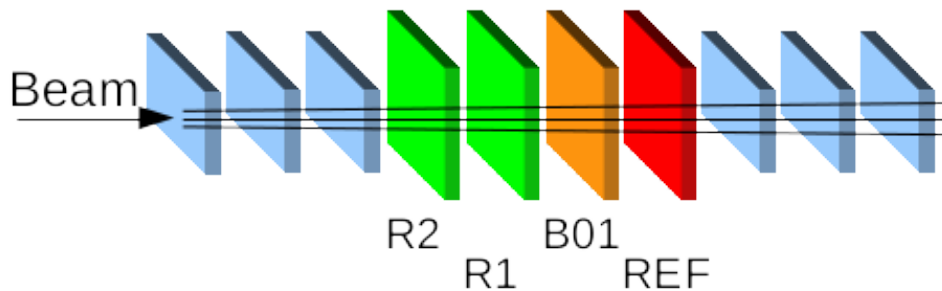


Hit rate (Counted hits divided by scan duration)



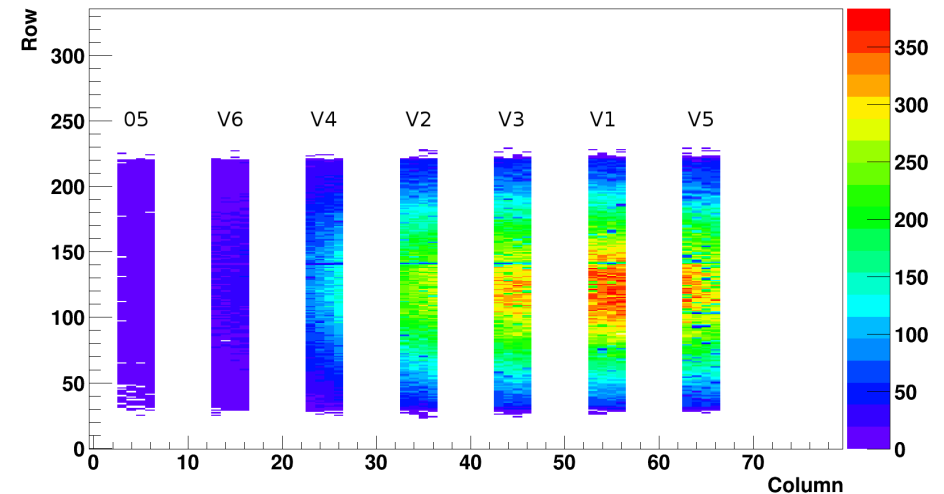
CERN Testbeam (Aug '16)

- R2 (unirradiated), R1 (irrad.)
- All structures biased
- 3200e / 6ToT@20ke
- -29°C on-sensor (derived from leakage current)
- 3-4 designs investigated at a time
- Repositioning of box necessary to cover the whole sensor

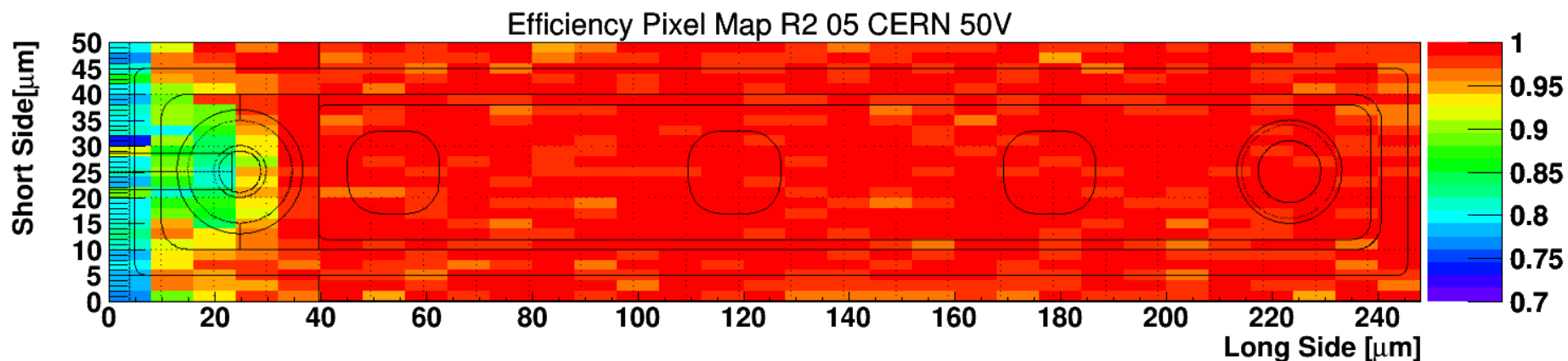


$$\text{efficiency} = \frac{\text{hits}}{\text{tracks}}$$

Track Map R1 CERN 400V right



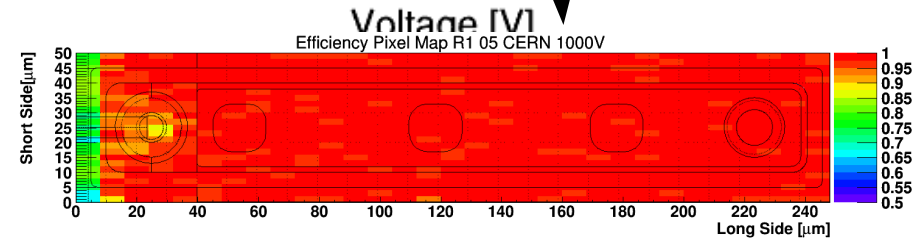
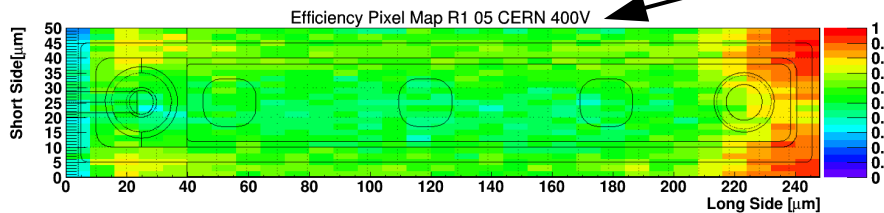
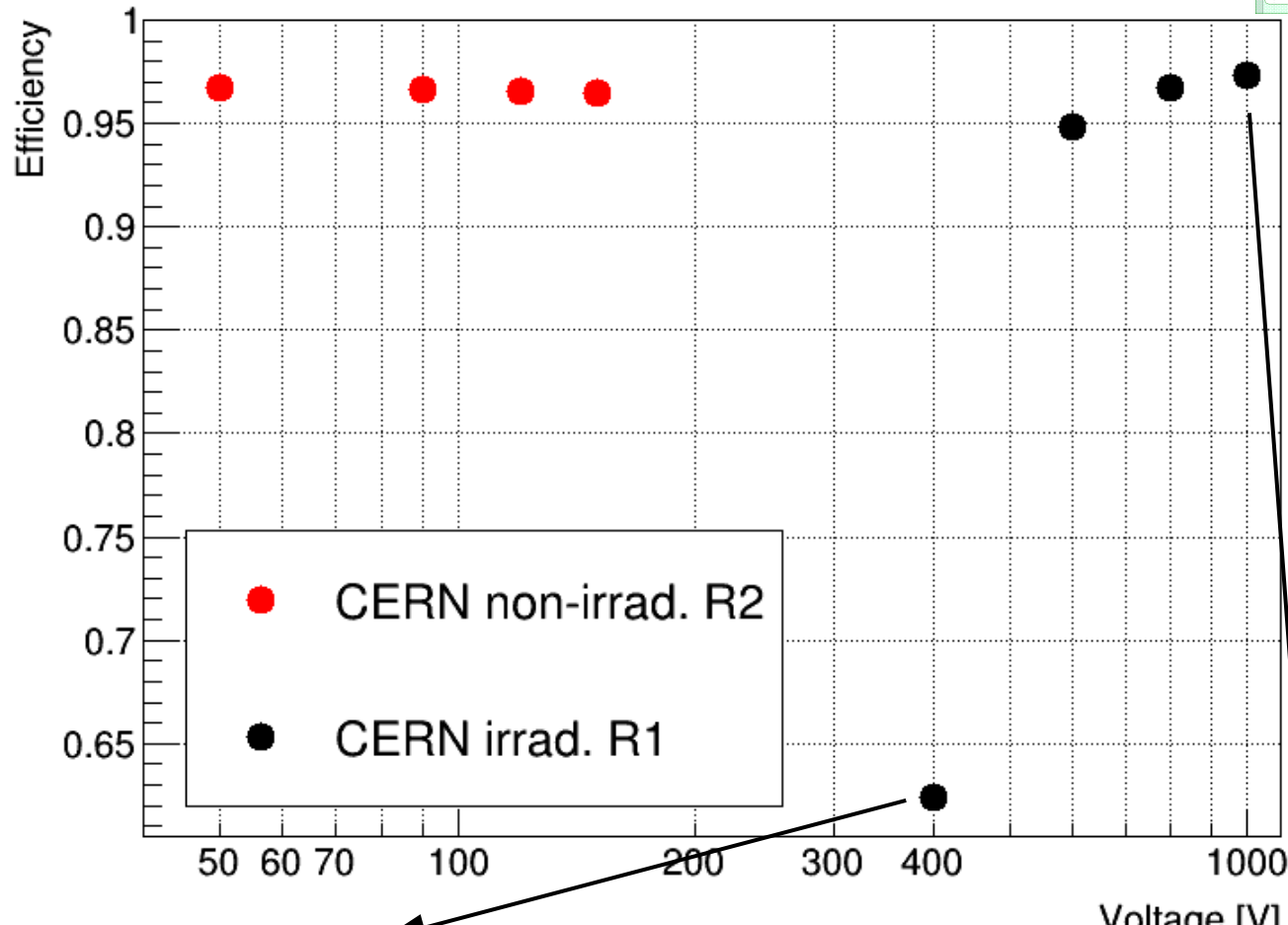
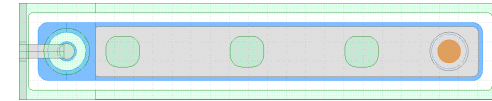
Efficiency R2 (unirradiated)



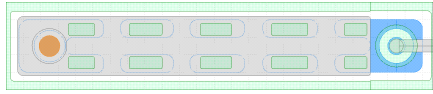
Pixel design	Efficiency [%]	# Tracks
05	97,1 ± 0,3	123 084
V6	95,8 ± 0,2	142 374
V4	96,3 ± 0,2	159 014
V2	96,2 ± 0,3	158 170
V3	96,3 ± 0,3	170 754
V1	96,4 ± 0,2	146 609
V5	96,2 ± 0,5	150 110

Efficiency R1 (irradiated): V05 (IBL)

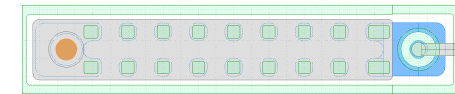
Efficiency Standard Pixel



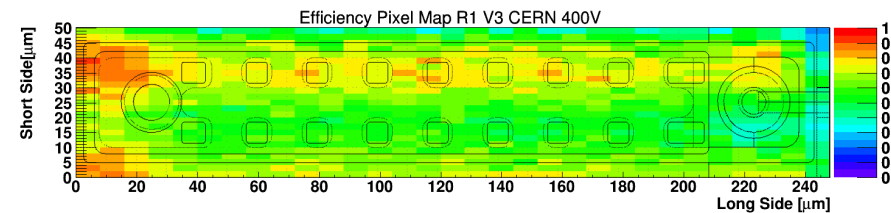
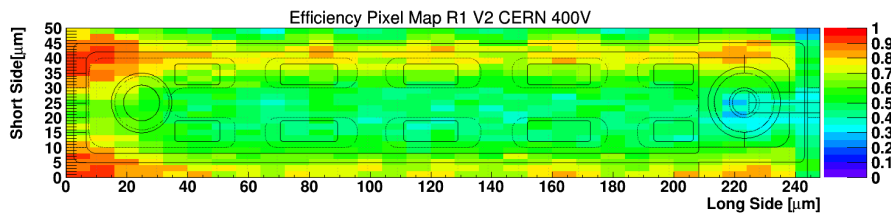
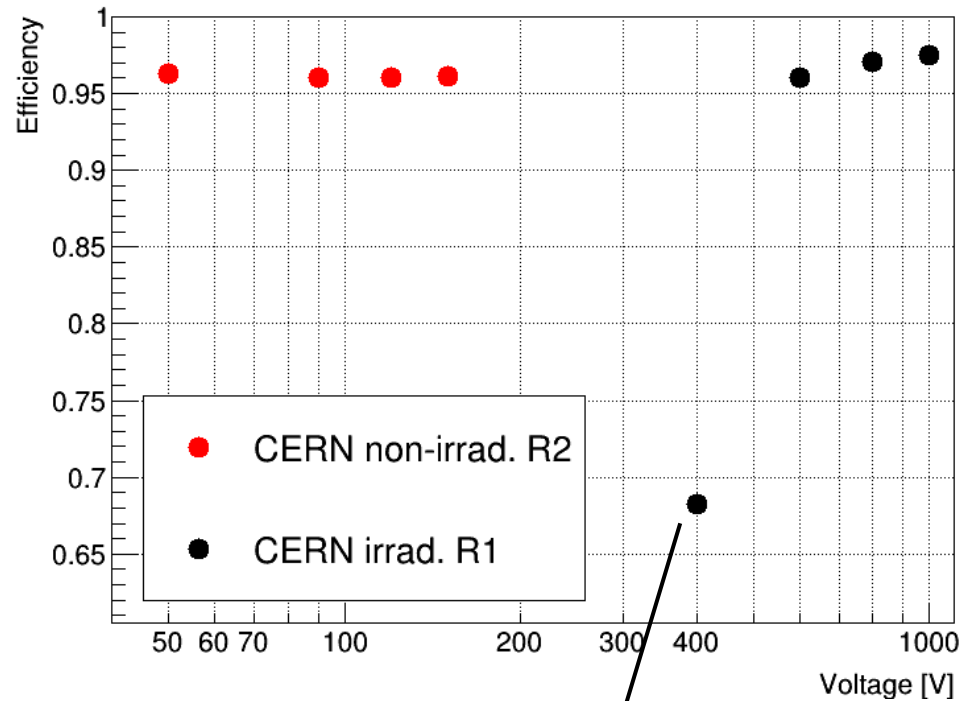
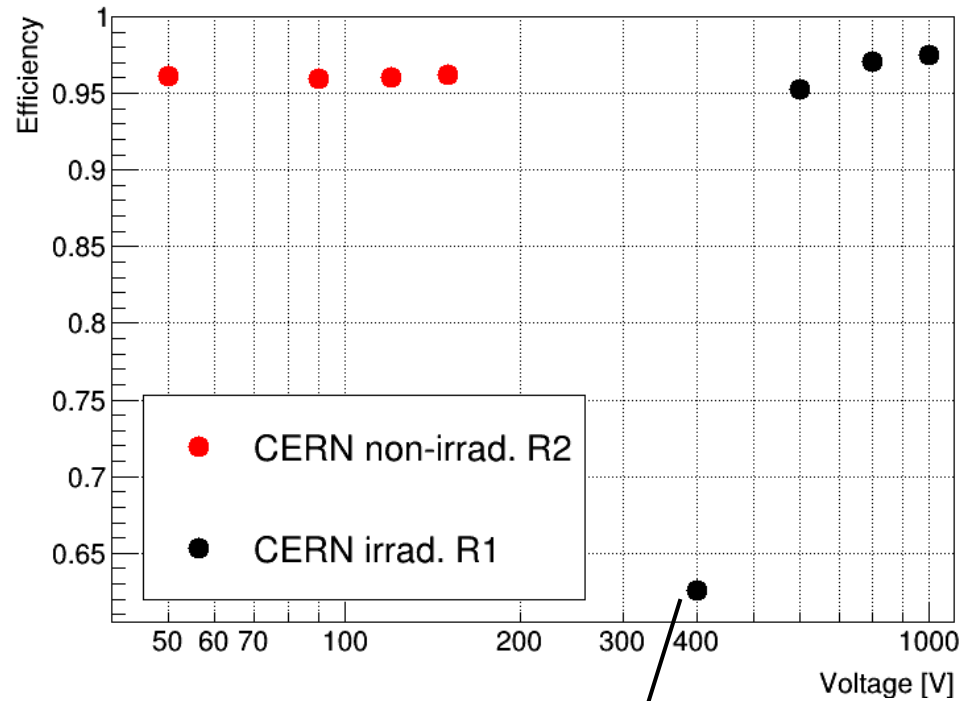
Efficiency R1 (irradiated): V2 & V3



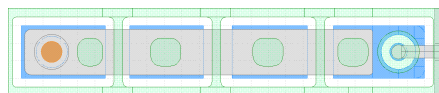
Efficiency V2



Efficiency V3



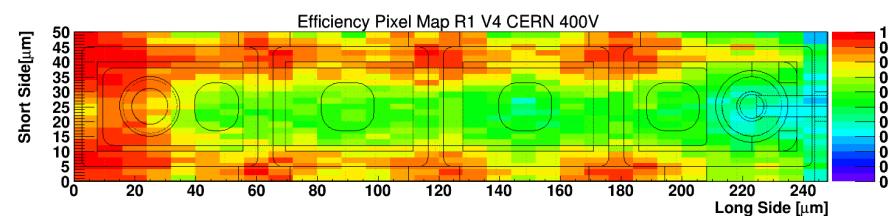
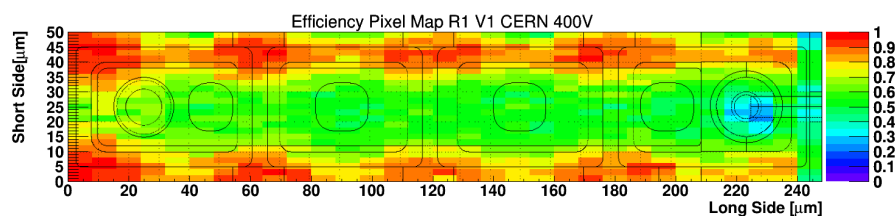
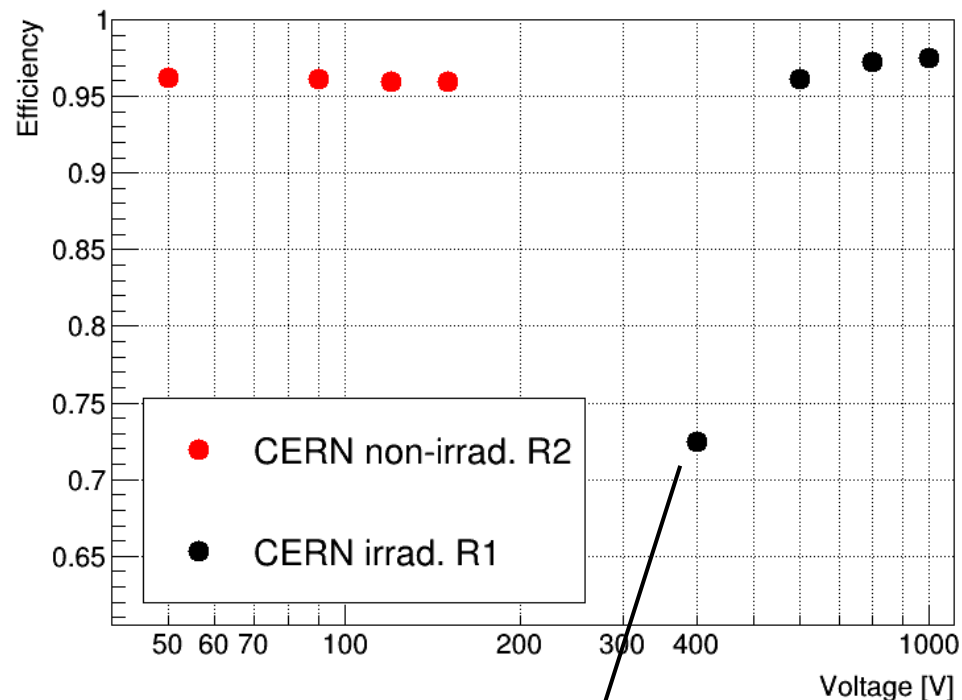
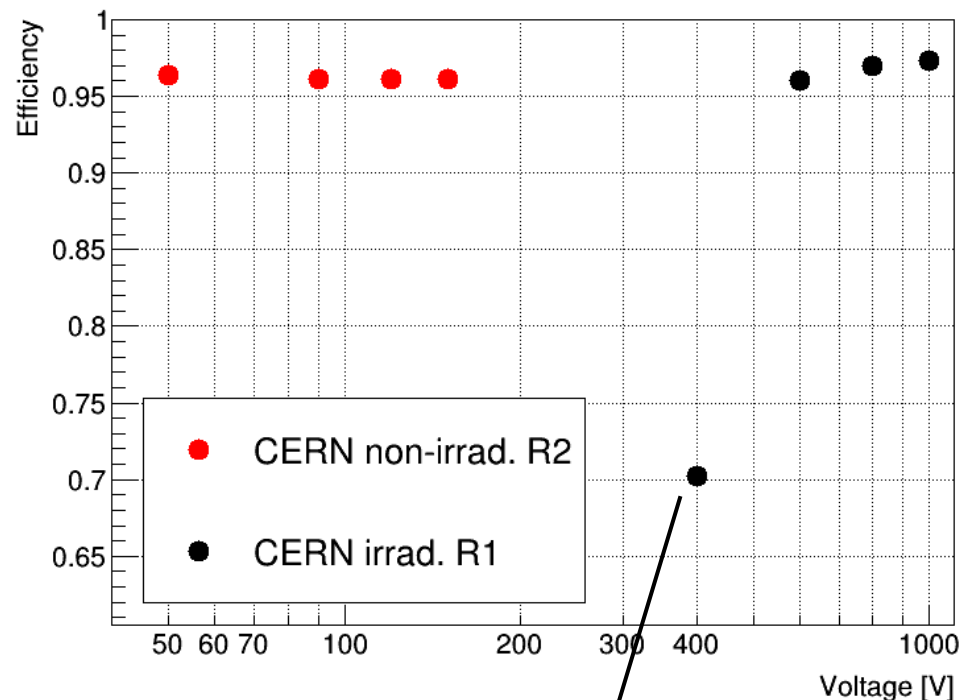
Efficiency R1 (irradiated): V1 & V4



Efficiency V1



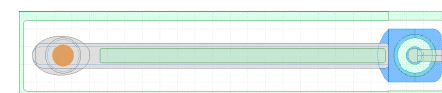
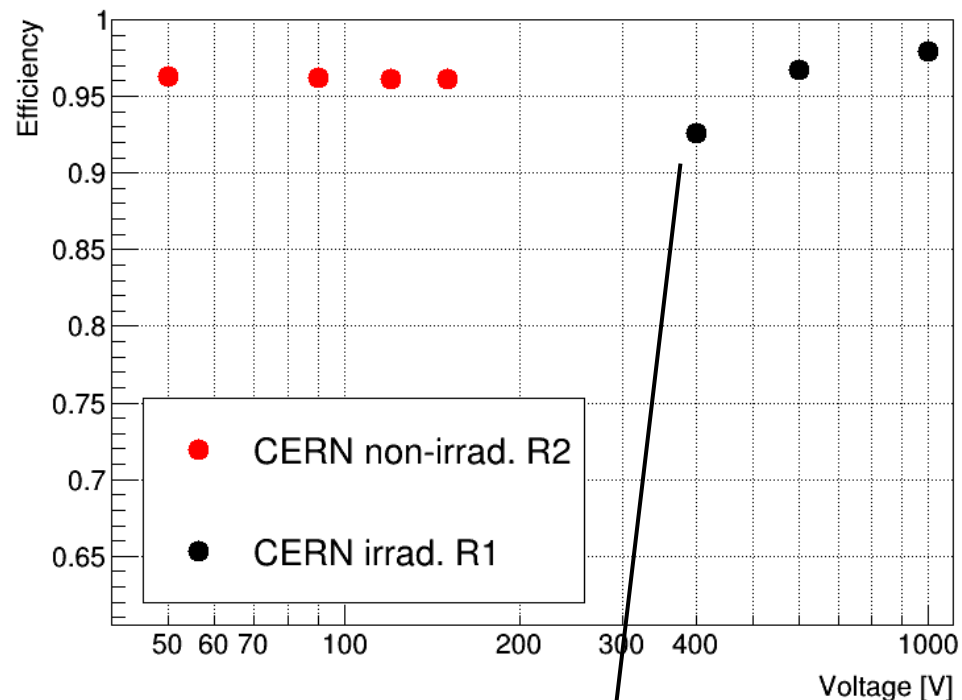
Efficiency V4



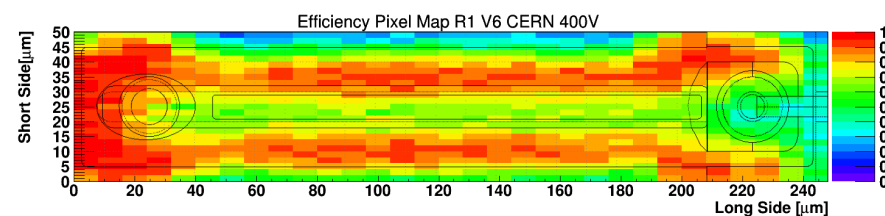
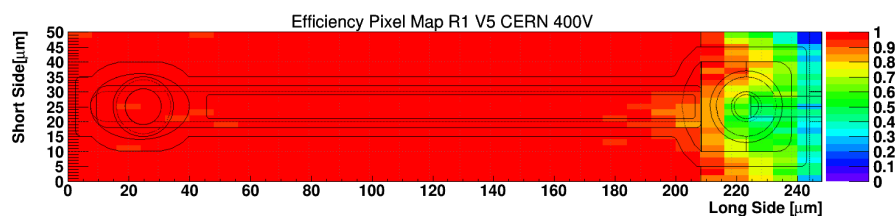
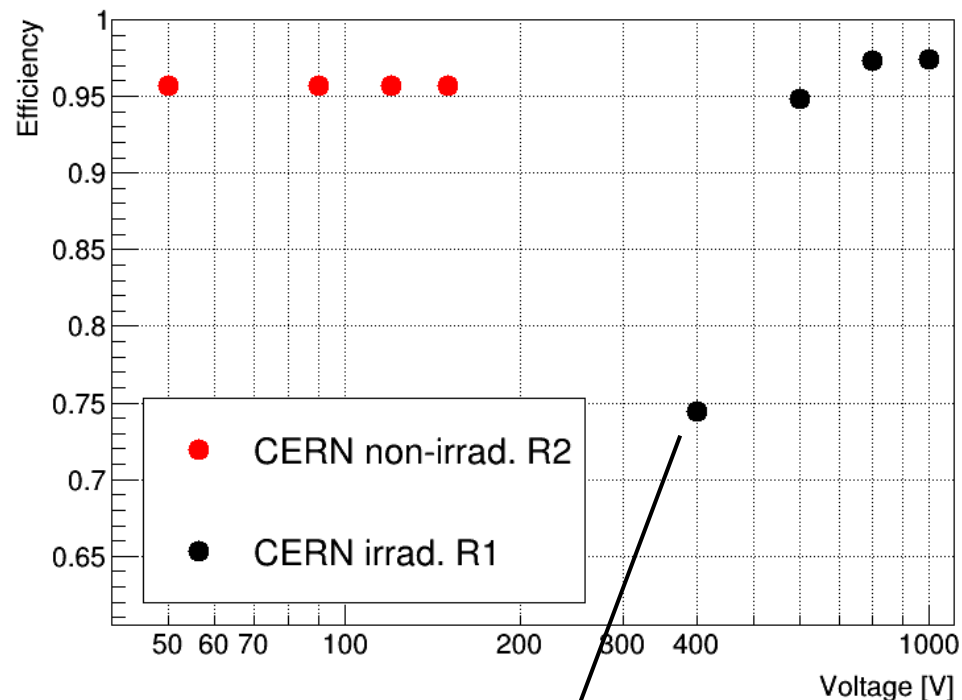
Efficiency R1 (irradiated): V5 & V6



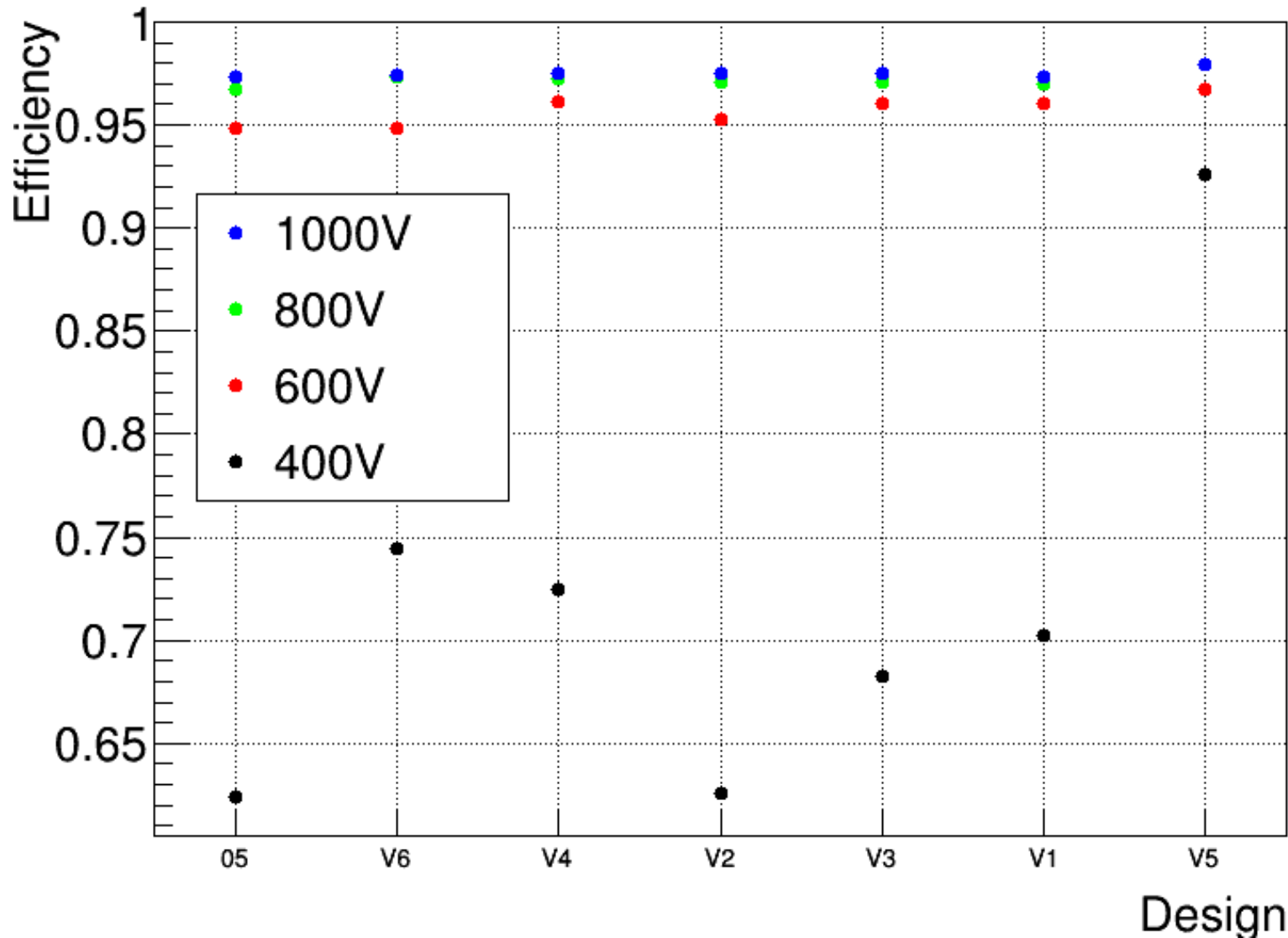
Efficiency V5



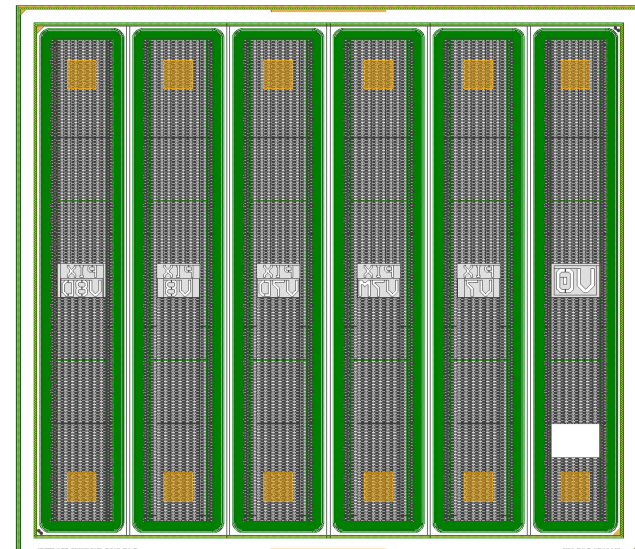
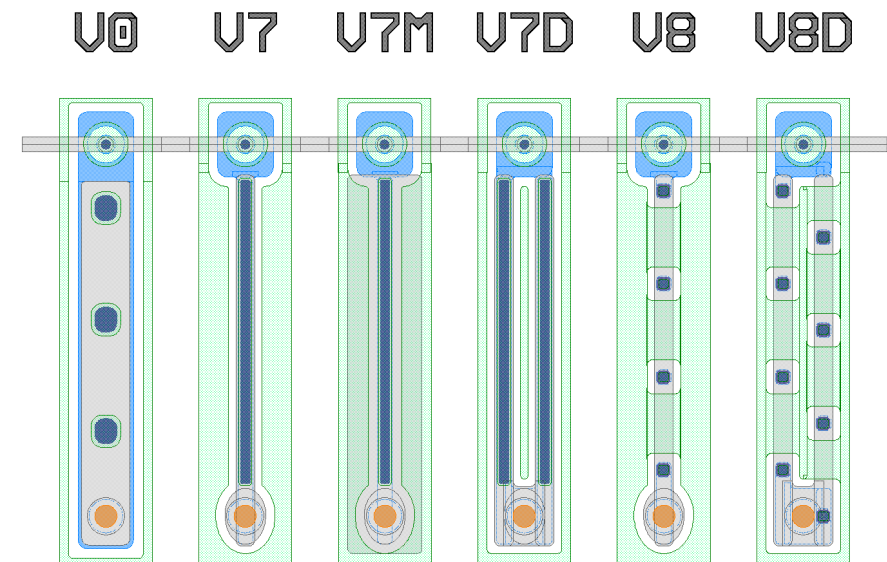
Efficiency V6



Efficiency R1 (irradiated)



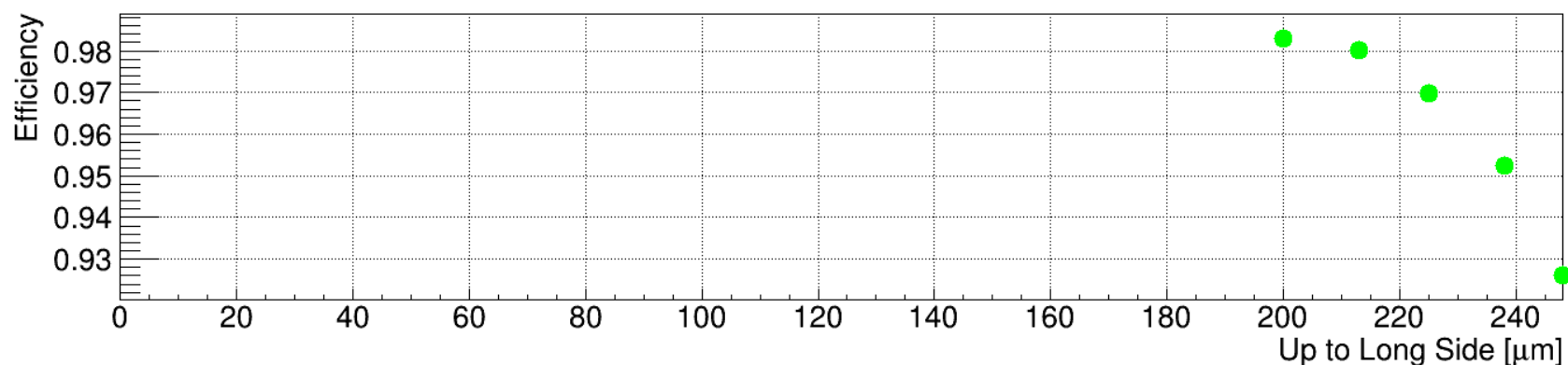
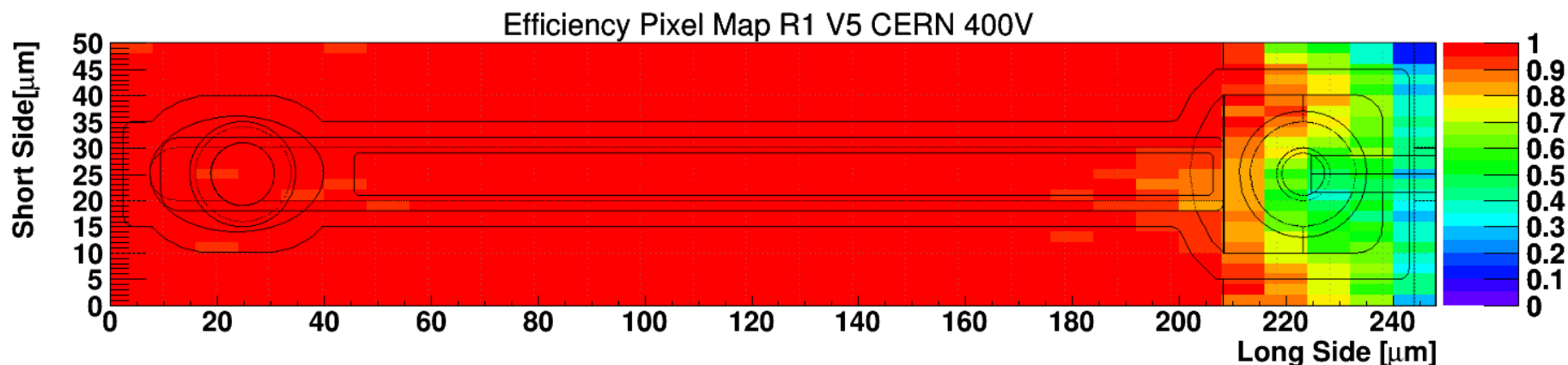
- New follow-up designs
- 12 + 4x14 + 12 columns
- 5 new designs, based on V5
- Narrow n+ implant (8 μm)
- Wide p-spray
- Influence of metal layer (V7M)
- Prestudies for pixel sizes of 25x100 μm^2 (V7D, V8D) or 50x50 μm^2 (V8)



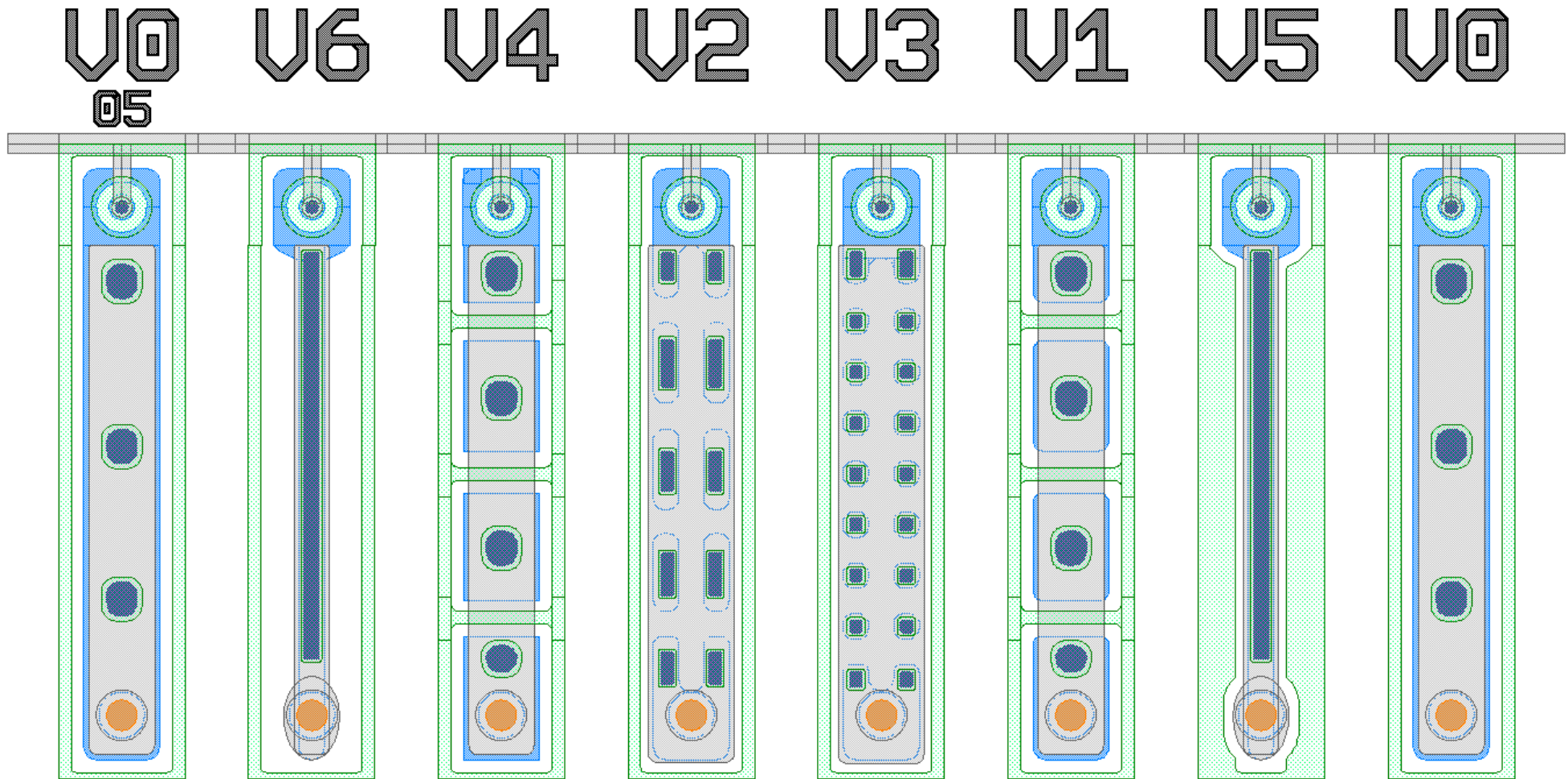
- Six modified IBL pixel designs have been investigated
 - After $5e15 n_{eq} cm^{-2}$, new designs similar or better than IBL design in case of charge collection and efficiency
 - Reach satisfying efficiency at lower voltages
 - V5 most promising new design
-
- R2 currently at CERN-PS irradiation
 - New R&D n-in-n production submitted



Efficiency vs. long pixel side



Overview REINER Mk I



Overview REINER Mk II

V0

V7

V7M

V7D

V8

V8D

