

## **Session Program**

**5-7 Jun 2017**

# **30th RD50 Workshop (Krakow)**

## ***Device simulation***

Krakow  
AGH UST Al. Mickiewicza 30 30-059 Krakow, Poland

# Monday 5 June

14:30

## Device simulation

**Session** | **Location:** Krakow, AGH UST Al. Mickiewicza 30 30-059 Krakow, Poland | **Convener:** Gregor Kramberger

14:30–14:50

### Depletion voltage and leakage current simulation of the ATLAS Pixel Detector according to the Hamburg model

#### Speaker

Julien-Christopher Beyer

14:50–15:10

### TCAD simulations of CMOS Hi-Res MAPS detectors

#### Speaker

Dr E.Giulio Villani

15:10–15:40

### Coffee break

15:40–16:00

### TCAD simulation of silicon detectors: A validation tool for the development of LGAD

#### Speakers

Dr Marco Mandurrino, Dr Marco Mandurrino

16:00–16:20

### Simulation and characterisation of a low gain avalanche detector for particle physics and synchrotron applications

#### Speakers

Mr Neil Moffat, Neil Moffat

16:20–16:40

### LGAD Simulations with the Ga doping: an exploration

#### Speaker

Prof. Francisco Rogelio Palomo Pinto

16:40–17:10

### Discussion

#### Speaker

Gregor Kramberger

17:10