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Development of a new Front-End electronics in Si and Si-Ge technology for the Resistive Plate Chamber (RPC) detector for high rate experiments

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The upgrade of the Resistive Plate Chamber (RPC) detector, in order to increase the detector rate capability and to be able to work efficiently in high rate environment, consists in the reduction of the operating voltage along with the detection of signals which are few hundred μ V small. The approach chosen by this project to achieve this objective is to develop a new kind of Front-End electronics which, thanks to a mixed technology in Silicon and Silicon-Germanium, enhance the detector performances increasing its rate capability.

The Front-End developed is composed by a preamplifier in Silicon BJT technology with a very low inner noise (1000 e⁻ rms) and an amplification factor of 3-4mV/fC and a new kind of discriminator in SiGe HJT technology which allows a minimum threshold of the order of 0.5 mV.

The performances of this kind of Front-End will be shown. The results are obtained by using the CERN H8 beamline with a full-size RPC chamber of 1 mm gas gap and 1.2 mm thickness of electrodes equipped with this kind of Front-End electronics.

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