

## **RPC performance v.s. Front-End electronics and detector parameters**

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The status of the art in realizing and upgrading the RPC performance is presented as an optimal combination of the detector and the front-end electronics features.

We show here a combined analysis of the different parameters characterizing the detector and the FE electronics, in order to obtain the best performance in terms of space and time resolution as well as of rate capability, in different applications.

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