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A large signal code for the optimization and design of high efficiency Klystron

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A code called KlyC is developed in CERN for the optimization and design of high efficiency Klystron based on new bunch mechanisms. This paper demonstrates the basic algorithm of the code and introduce its graphical user interface. After that, the design of FCC prototype Klystron based on KlyC will be presented. The benchmark and diagnosis of this device will also be attached to validate KlyC as a useful tool for high efficiency designers.

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