

Session Program

17-21 Sept 2017



EUCAS 2017

4EO1 - Digital and Quantum Processings

Geneva, CIG

Thursday 21 September

13:15

4E01 - Digital and Quantum Processings

Session | Location: Geneva, CIG Room 3+4

13:15-13:45

4E01-01 Recent research developments of adiabatic quantum-flux-parametron circuits technology toward energy-efficient high-performance computing

Speaker

Yoshikawa Nobuyuki

13:45-14:00

4E01-02 Theory and experimental analysis of the sensitivity of an adiabatic quantum flux parametron

Speaker

Thomas Ortlepp

14:00-14:15

4E01-03 One- and Two-Bit Reversible Fluxon Logic Gates

Speaker

Osborn Kevin

14:15-14:30

4E01-04 Coherent control of superconducting transmon qubits with an on-chip single flux quantum driver

Speaker

Leonard Jr, Edward

14:30-14:45

4E01-05 Direct Detection of Single Flux Quantum Generated in Superconducting Strip Photon Detectors

Speaker

Abe Yutaka

14:45-15:00

4E01-06 Superconductor Electronics Fabrication Process with Self-Shunted Josephson Junctions and Kinetic Inductors

Speaker

Tolpygo Sergey

15:00-15:15

4E01-07 Energy-Efficient Dual-Port 32-word 8-bit ERSFQ Register File

Speaker

Alex Kirichneko

15:15-15:30

4E01-08 Progress on development of an rf waveform synthesizer with quantum-based accuracy

Speaker

Manuel Castellanos Beltran

15:30