Session Program

17–21 Sept 2017

EUCAS 2017

Poster : 1LP7 - FCL : design and devices

Geneva, CICG

Monday 18 September

13:30

Poster : 1LP7 - FCL : design and devices

Poster Session | **Location:** Geneva, CICG

1LP7-01 Comparison of shell vs. core-type magnetic circuits in the performance of inductive superconducting fault current limiters under asymmetric faults

Speaker
Joao Murta-Pina

1LP7-02 Analysis of the effects of different types of faults in three-phase saturated cores SFCL

Speaker
Nuno Vilhena

1LP7-03 Peak Current Limiting Characteristics of Transformer Type SFCL with Two Non-Isolated Secondary Windings

Speaker
Tae-Hee Han

1LP7-04 SFCL cooling characteristics according to the control variables

Speaker
Hankil Yeom

1LP7-06 Comparison of dual peak current limiting with current source location of a series-connected SFCL using two iron cores

Speaker
Seok-Cheol Ko

1LP7-07 Volume Effect of Dynamic Breakdown Strength in LN2 for Insulation Design of Resistive Superconducting Fault Current Limiters

Speaker
Naoki Hayakawa

1LP7-08 Magnetizing Characteristics of a Flux-Lock-Type SFCL with Two Magnetic Paths Using Its Third Winding

Speaker
Sung-Hun Lim

1LP7-09 Engineering design and test of low-resistance high temperature superconducting short-circuited coils

Speaker
Joao Murta-Pina

1LP7-10 Improvement of recovery characteristics of resistive SFCL made by GdBCO tape with several surface conditions under pressurized condition

Speaker
Satoshi Takaya

1LP7-11 Increase of the normal resistivity of REBCO tapes for 320 kV superconducting fault current limiters

Speaker
Guillaume Escamez

1LP7-12 Winding Design and AC Loss Analysis of a 10-kV / 500-A Flux-coupling type Superconducting Fault Current Limiter

Speaker
Sinian Yan

1LP7-13 Magnetizing Characteristics of Transformer Type SFCL with Additional Secondary Winding Due to Its Winding Direction

Speaker
Shin-Won Lee

1LP7-14 Measurements of electromechanical forces in superconducting fault current limiters tapes under short circuit conditions

Speaker
Anabela Pronto

1LP7-15 A Design Methodology for Optimisation of three-phase SFCL of Saturated Cores Type

Speaker
Nuno Vilhena

1LP7-16 Operation of a SCFCL at 65 K

Speaker
Pascal Tixador

1LP7-17 Research on the Magnetic Property of Saturated Iron-core Superconducting Fault Current Limiter

Speaker
Chi Zhang

1LP7-18 Experiment and design of new type DC superconducting fault current limiter

Speaker
Chaoqun Zhao

1LP7-19 Design, building and test of a 220V/300A braid type superconducting fault current limiter

Speaker
Fei Liang

1LP7-20 Experiments on a new type of DC superconducting fault current limiter

Speaker
Ying Xin

1LP7-21 HTS Power devices with low cryo-consumption for current conditioning, switching and limiting

Speaker
Alexander Usoskin

1LP7-22 Short-Circuit Current Limitation Through 2G YBCO Resistive-Type SFCL Devices: Technical and Economic Comparison with Traditional Air-Core Reactors

Speaker
Giuliano Angeli

1LP7-23 Experimental tests and analysis of a small-scale Noval Hybrid Type DC SFCL Prototype

Speaker
Siyuan Liang

1LP7-24 Preliminary Investigation on Economic Aspects of Superconducting Magnetic Energy Storage (SMES) Systems and High Temperature Superconducting Transformers (HTST)

Speaker
Weijia Yuan

15:30