## International Workshop on Semiconductor Pixel Detectors for Particles and Imaging (PIXEL2018)



Contribution ID: 12 Type: POSTER

## A TOSA/ROSA-Based Optical Transmitter (MTx+)/Transceiver (MTRx+) for High-Energy Physics Experiments

Monday 10 December 2018 14:40 (10 minutes)

We present a dual-channel optical transmitter (MTx+) and an optical transceiver (MTRx+) for front-end readout electronics. MTx+ utilizes two Transmitter Optical Sub-Assemblies (TOSAs) (Truelight Part No. TTF-1F59-427) and MTRx+ uses a TOSA and a Receiver Optical Sub-Assemblies (ROSA) obtained from CERN. Both MTx+ and MTRx+ receive multimode fibers with standard Lucent Connectors (LCs) as the optical interface and can be panel or board mounted to the motherboard with a standard Enhanced Small Form-factor Pluggable (SFP+) connector as the electrical interface. MTx+ and MTRx+ employ a dual-channel Vertical-Cavity Surface-Emitting Laser (VCSEL) driver ASIC called LOCld65. LOCld65 is designed in a commercial 65-nm CMOS technology with a power supply of 1.2 V. Each channel of LOCld65 can be individually turned on or off. LOCld65 features input and output equalizers. LOCld65 is packaged in a 24-pin 4 mm x 4 mm opencavity Quad-Flat No-leads (QFN) package. MTRx+ has a GBTIA embedded in the ROSA. We design an optical latch to hold two LC connectors and two TOSAs/ROSA together and a metal cage to attach the module to the motherboard. We printed prototype latches with a 3-Dimension printer and are producing latches in injection molding. The prototype cage has been fabricated. The dimension of MTx+/MTRx+ is 44.5 mm (length) x 18.2 mm (width) x 5.8 mm (height). Each transmitter channel of MTRx+/MTRx+ is tested to operate up to 14 Gbps with typical power dissipations (the VCSEL included) of 68.3 mW/channel and 62.1 mW/channel at the VCSEL voltages of 3.3 V and 2.5 V, respectively. MTx+ and MTRx+ survive 4.9 kGy(SiO2). MTx+ and MTRx+ with an evaluation board can be obtained for further development.

Primary authors: Dr DENG, Binwei (Hubei Polytechnic University, Southern Methodist University); Dr ZHAO, Xiandong (Southern Methodist University); Mr ZHOU, Wei (Central China Normal University, Southern Methodist University); GONG, Datao (Southern Methodist University); Dr GUO, Di (Central China Normal University, Southern Methodist University (US)); Dr HUANG, Guangming (Central China Normal University); Dr HOU, Suen (Academia Sinica (TW)); Mr JIN, Kai (Central China Normal University); Dr LIU, Kent (Southern Methodist University); Mr LIU, Jun (Centra China Normal University); LIU, Tiankuan (Southern Methodist University (US)); Mr QI, Ming (Nanjing University (CN)); Mr THOMAS, James (Southern Methodist University); Dr XIAO, Le (Central China Normal University, Southern Methodist University)

**Presenter:** Dr DENG, Binwei (Hubei Polytechnic University, Southern Methodist University)

Session Classification: Poster section

Track Classification: Front end electronics and readout