

Experience and developments in ASIC design at INFN Torino

Thursday 9 November 2017 15:40 (25 minutes)

The presentation will describe the ASIC design activities at INFN Torino, with special focus on the developments for hybrid and monolithic pixel detectors, for timing measurements and for high speed data transmission circuit. ASICs currently used in various applications (ALICE ITS, COMPASS RICH, GBT common interface and medical interface) will be described, together with the recent developments in timing applications and HL-LHC prototypes.

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Session Classification: FEE development