





## TRD weekly meeting week 42: 16 – 23 October 2017

- Fill summary
- TRD performance
- AOB

Alexander Borissov, Heidelberg University, CERN, 23.10.2017

## **Fill summary**

Stable Beams Declared	Filling Scheme Name	Stable Beams Start	Stable Beams End	Stable Beams Duration	Data Taking Duration	Pause Duration	SOR/EOR Duration	Efficienc (%)
Yes	25ns_1868b_1866_1089_1749_128bpi_17i8b4e	22/10/2017 20:14:57	23/10/2017 09:20:20	13:05:23	12:19:27	00:02:33	00:22:54	94.15
Yes	25ns_1868b_1866_1089_1749_128bpi_17i8b4e	22/10/2017 02:10:03	22/10/2017 12:59:45	10:49:42	10:26:10	00:04:01	00:15:21	96.38
Yes	25ns_1868b_1866_1089_1749_128bpi_17i8b4e	21/10/2017 07:42:16	21/10/2017 22:04:15	14:21:59	13:20:12	00:02:13	00:31:57	92.83
Yes	25ns_1868b_1866_1089_1749_128bpi_17i8b4e	21/10/2017 04:00:08	21/10/2017 05:18:33	01:18:25	01:14:06	00:00:23	00:02:11	94.50
Yes	25ns_1868b_1866_1089_1749_128bpi_17i8b4e	20/10/2017 14:10:19	21/10/2017 01:33:08	11:22:49	10:43:31	00:02:49	00:39:06	94.24
Yes	25ns_1868b_1866_1089_1749_128bpi_17i8b4e	19/10/2017 22:26:46	20/10/2017 11:21:52	12:55:06	11:48:06	00:02:38	00:32:07	91.36
Yes	25ns_1868b_1866_1089_1749_128bpi_17i8b4e	19/10/2017 17:27:04	19/10/2017 18:21:57	00:54:53	00:31:57	00:00:00	00:02:11	58.21
Yes	25ns_1868b_1866_1089_1749_128bpi_17i8b4e	19/10/2017 01:57:39	19/10/2017 14:43:59	12:46:20	12:16:58	00:02:32	00:16:29	96.17
Yes	25ns_1868b_1866_1089_1749_128bpi_17i8b4e	18/10/2017 12:58:57	18/10/2017 21:08:05	08:09:08	07:37:24	00:04:08	00:15:20	93.51
Yes	25ns_1868b_1866_1089_1749_128bpi_17i8b4e	18/10/2017 01:33:11	18/10/2017 10:40:22	09:07:11	08:51:56	00:02:14	00:07:46	97.21
Yes	25ns_1868b_1866_1089_1749_128bpi_17i8b4e	17/10/2017 10:17:18	17/10/2017 23:20:00	13:02:42	11:05:20	00:24:10	00:34:44	85.00
Yes	25ns_1868b_1866_1089_1749_128bpi_17i8b4e	16/10/2017 19:18:47	17/10/2017 07:38:34	12:19:47	11:54:59	00:04:01	00:12:41	96.65
Yes	25ns_1868b_1866_1089_1749_128bpi_17i8b4e	16/10/2017 01:05:49	16/10/2017 14:33:58	13:28:09	12:15:12	00:03:42	00:30:08	90.97

```
\implies Data taking with long-time fills
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## **TRD performance**

• 19/10/2017 18:03, run 280613

TRD PCU channels and PDB were off, as well as timeout set.

 $\implies$  PCU powercycling, switch on LV channels, setting timeout and switch on PDB LV channels TRD

- → Further investigation required. ...PDB problem?
- 21/10/2017 14:42 runs 280761, 280762 A 2-1-2, 21/10/2017 16:38 run 280764 A 2-1-2 tripped.

Markus Kohler, 22/10/2017 15:06, id=579384 voltage was reduced by: A2-1-22 -80V, A3-0-3 -40V, A17-4-1 -20V and current limit changed by D7-0-4 ITRIP set to 330uA

 $\implies$  Discussion: a problem of the CAN bus?

...High efficiency is required for pp reference run. Min time to restart the run  $\sim 2$  minutes.

## AOB

- TRD high rate test and LS2 plans presented on TB 19/11 by Yvonne.
- Access (tomorrow) for LV readout of sector 06 layers 0,1
- No TRD shifters: 28/10 5/11, from 20/11, from 25/11 pp reference runs

No HV TRD shifters: 26/10 18/11.