PhD RoadMap
ICCS view

Considering BlonD as the main driver application

PROPOSED PLAN

GOING WACI ML FOR BLOND

SCALE-OUT BLOND WITH HPC INFRASTRUCTURE

CODE RESTRUCTURING PERFORMANCE OPTIMIZATION

EXPLORATION OF SPECILIAZIZED MACHINE CONFIGURATIONS SUPER-BLOND

ACCELERATOR BASED PERFORMANCE BOOSTING
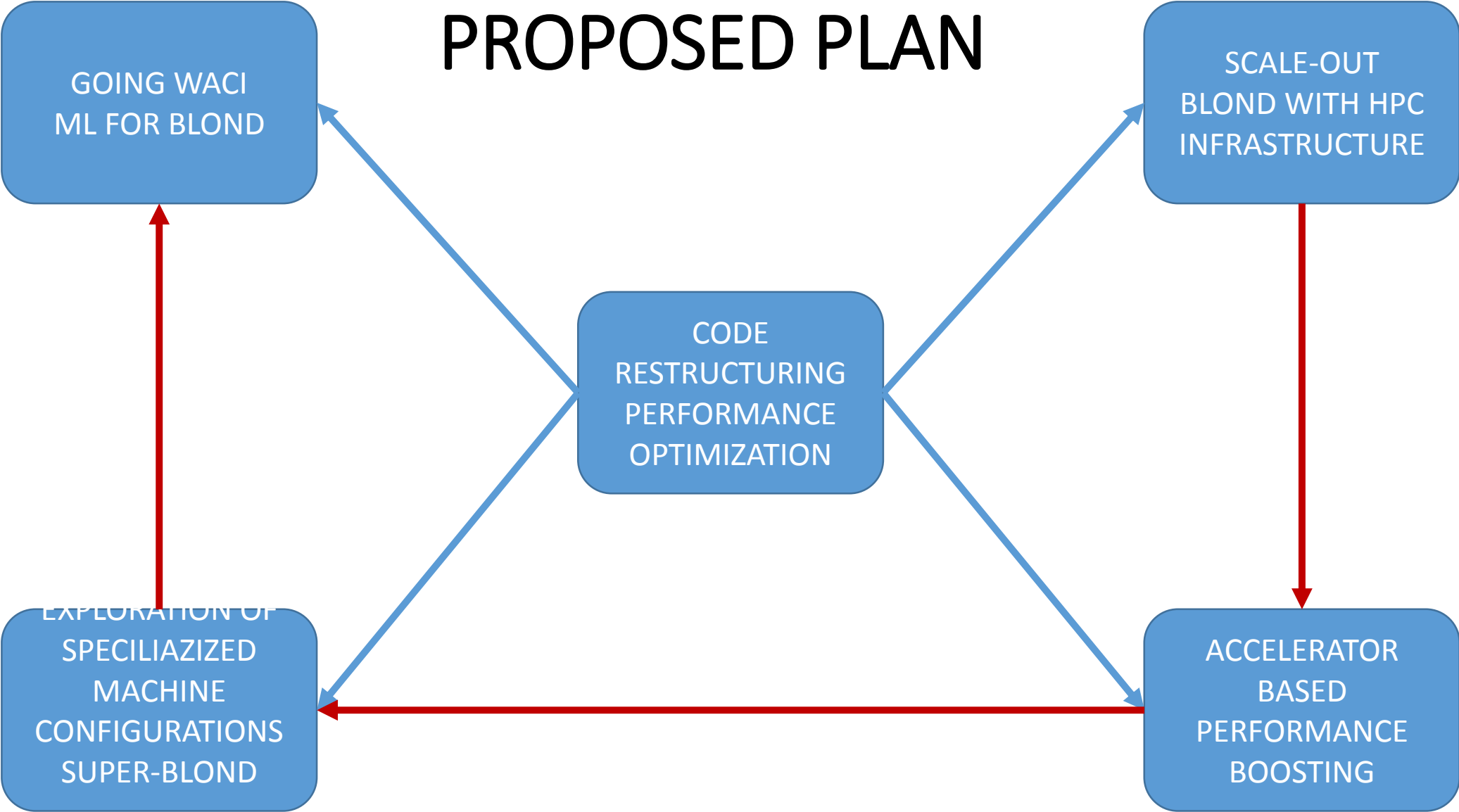
Reading list → what ever correlates with supercomputing + system architecture + accelerators

- ISC
- Journal of Supercomputing
- IEEE MICRO
- IEEE TC
- ISCA
- MICRO
- ACM TOCS
- ASPLOS
- DAC
- DATE
- ASP-DAC
- News from HPC Laboratories, Lawrence, Julich, NVIDIA, CINECA, MareNostum etc