Status of the R&D on the $\mu\text{-}$ RWELL technology

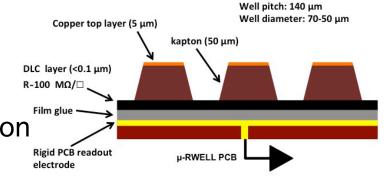
G. Bencivenni^a, R. de Oliveira^b, M. Gatta^a, <u>G. Morello^a</u>, A. Ochi^c, M. Poli Lener^a

- a. INFN-LNF
- b. CERN
- c. Kobe University

The $\mu\text{-}RWELL$ detectors

Drift cathode PCB

 They are composed of two elements:
a cathode glued on a frame, working as spacer and as limit for the conversion gas volume, and the μ-RWELL_PCB



- Nothing to say about cathode: a longer discussion could be held about its coating for photon/neutron conversion, but it is not the purpose of this talk
- The μ-RWELL_PCB is the core of the detector: this talk reports about its different versions developed taking into account the requirements of applications/experiments^{*}

The μ -RWELL_PCB

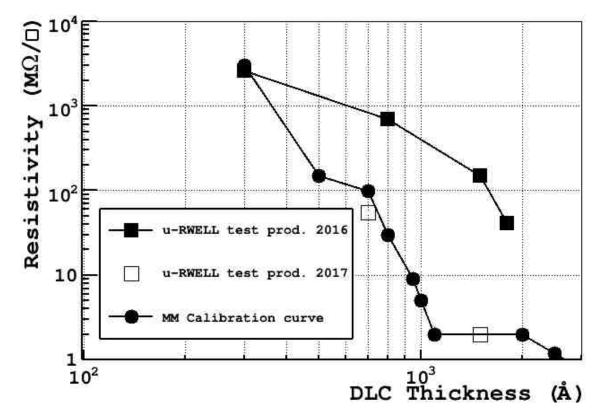
- The main element of the device is furthermore constructed coupling the **amplification stage** to **the readout plane** through an **insulating layer**
- The amplification stage is a kapton foil covered on one side by few microns Cu, and on the other side sputtered with DLC (Diamond-like Carbon). The amplification channels are created by etching, exploiting the consolidated technique used for GEMs
- The **resistive stage** can be adapted to the required performances of the detector in terms of rate capability

DLC

The foils sputtered with DLC are provided by Be-Sputter Co., Ltd. in Japan The carbon is sputtered on the kapton foil as an amorphous diamond like carbon (a-DLC) using a pure graphite target

The resistivity dependence as a function of the carbon

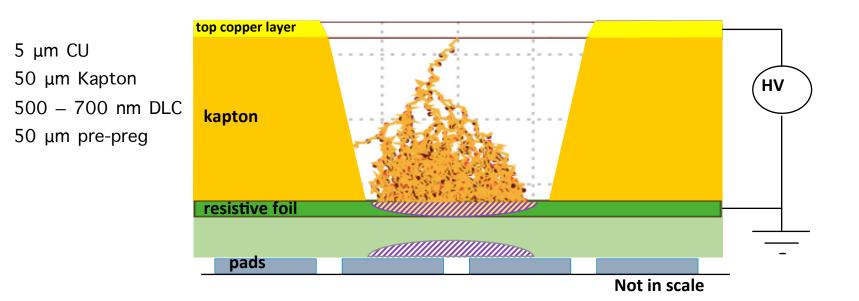
thickness for two different sputtering batches



The different trend for the two curves is supposed to be related with the humidity trapped by the Kapton before the sputtering process: higher humidity implying higher resistivity.

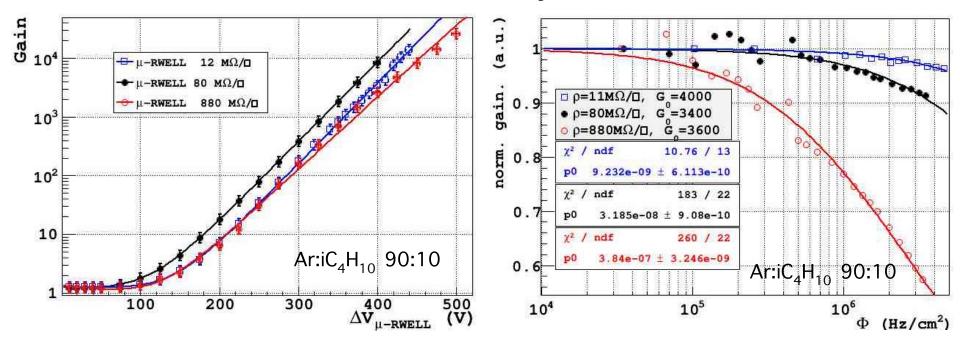
The single resistive layer

- The simplest version of the detector, actually the first one, has been extensively studied with several prototypes each characterized by different surface resistivities [JINST 10 P02008, NIM A 824 (2016) 565]
- In this case the etched kapton foil, sputtered with DLC, is coupled through an insulating layer to the readout plane



The single resistive layer: first tests

 Three prototypes with 10x10 cm² active area have been realized and first tested with X-ray (local irradiation) at LNF

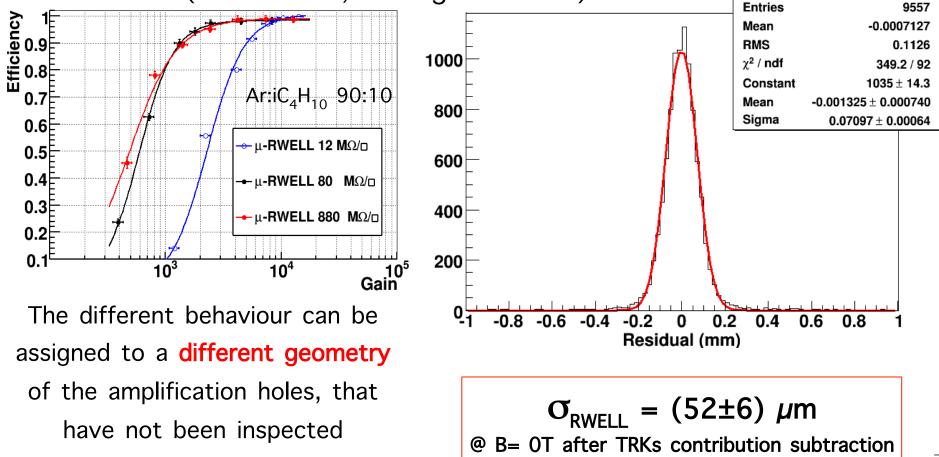


The **single resistive layer** layout is suitable for **low rate applications** 3% gain drop at

 $\Phi_{0.97} = 850 \text{ kHz/cm}^2;$ $\Phi_{0.97} = 77 \text{ kHz/cm}^2;$ $\Phi_{0.97} = 3.4 \text{ MHz/cm}^2;$

The single resistive layer: H4 test beam (2015)

 The devices have been then tested at H4-SPS North Area, equipped with strips-segmented readout (400 μm pitch) and APV25 (CC method, orthogonl tracks)



The single resistive layer: towards large area detectors

In the framework of CMS-phase2 muon upgrade we developed large size µ-RWELL in strict collaboration with Italian industrial partners (ELTOS & MDT)

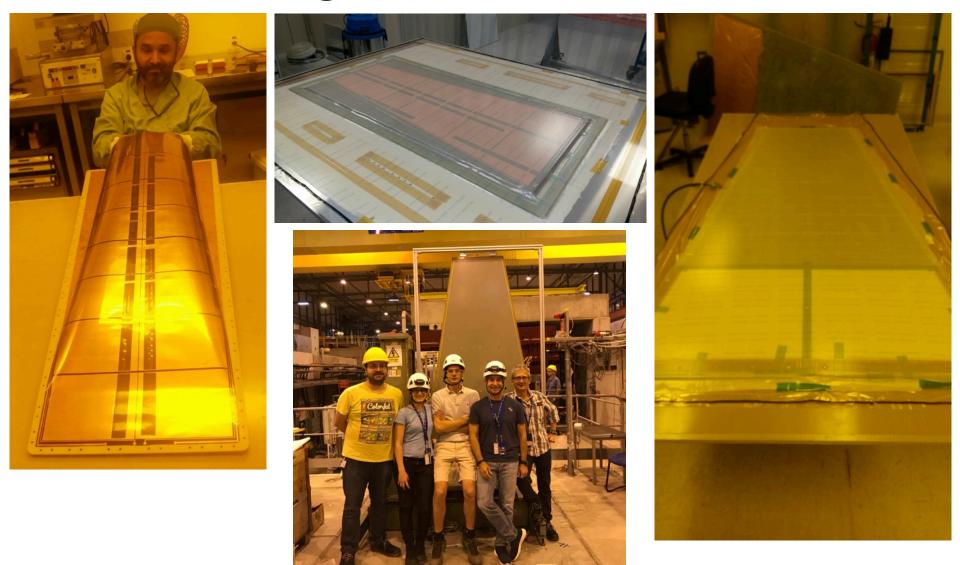


GE1/1 (1.2x0.5 m²). Active area segmented in 16 sectors which 4 were working.

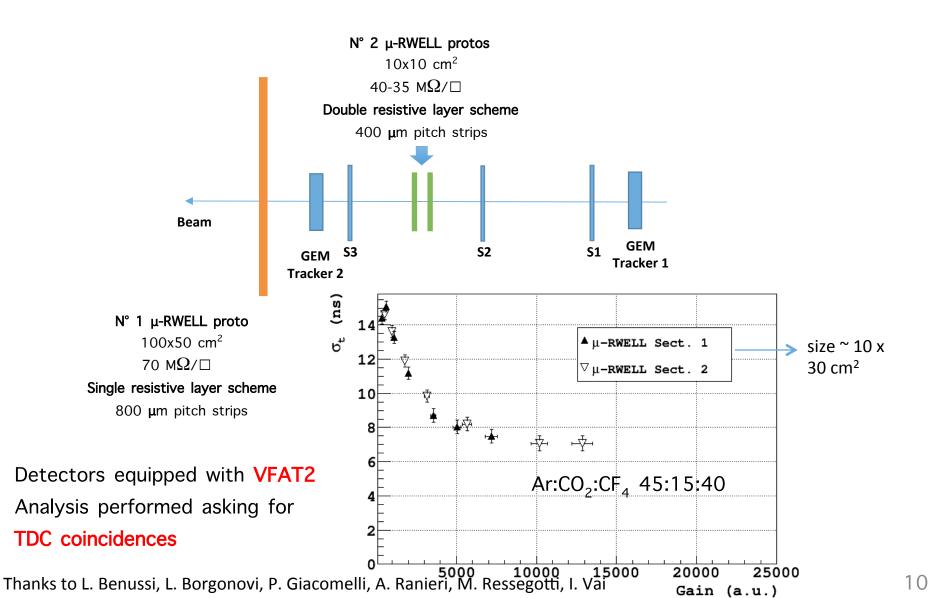
> GE2/1-like chambers. Only the lower part $(1.2x0.6 \text{ m}^2)$ is active. It has been obtained splicing two μ -RWELL_PCBs



The single resistive layer: towards large area detectors



The single resistive layer: H8C (2016) test beam

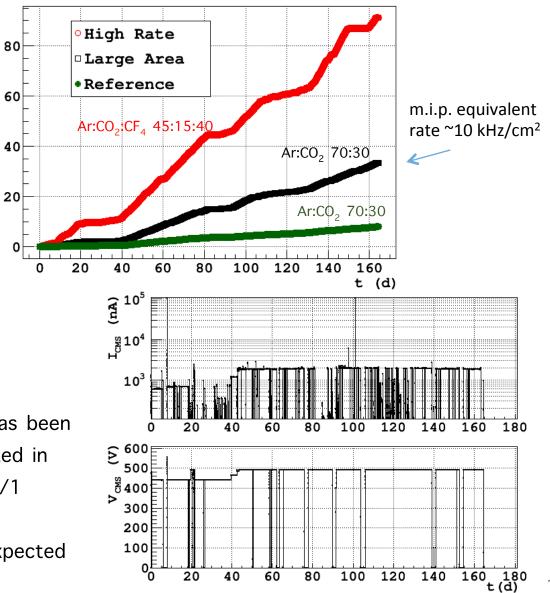


The single resistive layer: GIF++ exposure (2017)

(mC/cm²

0

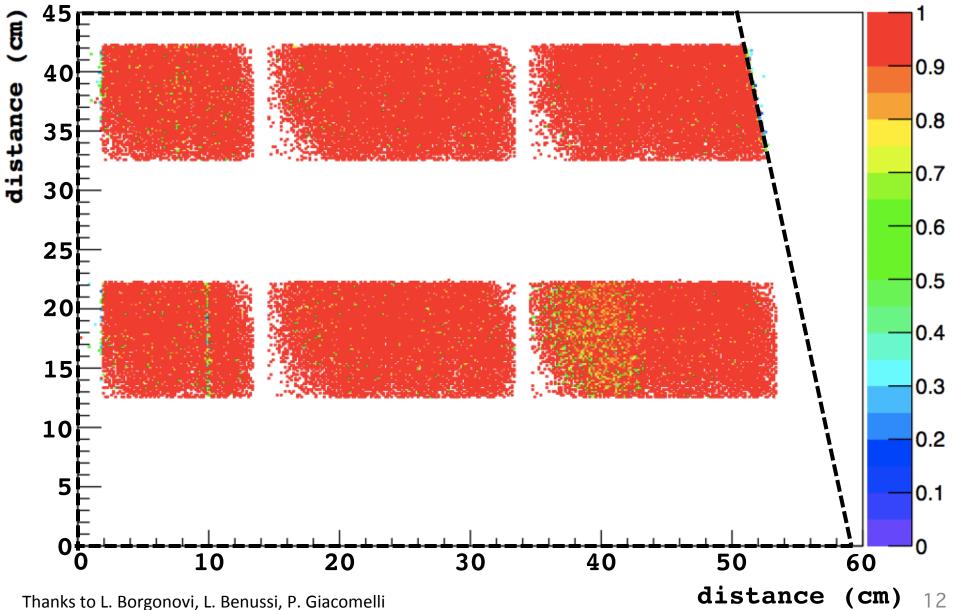




The study of ageing effects on DLC has been done by integrating the charge expected in 10 years of operation in the CMS GE2/1 region (1 kHz/cm²).

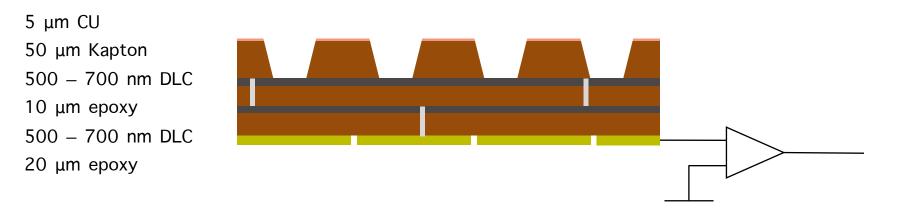
At a gain of 4000 the total charge expected is 2.6 mC/cm^2

The single resistive layer: H4 test beam (July 2017)



The double resistive layer

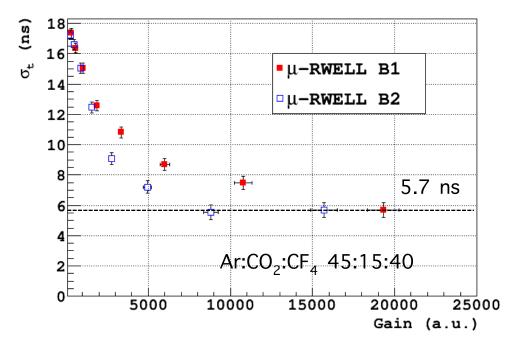
- The charges collected on the resistive layer move towards the ground with a characteristic time $\tau(R,C)$ [Dixit et al, NIMA 518 (2004) 721, NIMA 566 (2006) 281].
- The idea is to reduce the pattern covered by the electrons on the DLC



A matrix of conductive vias connects the two resistive layers. Another matrix of vias chains the second resistive layer to ground through the readout

The double resistive layer: H8C test beam (2016)

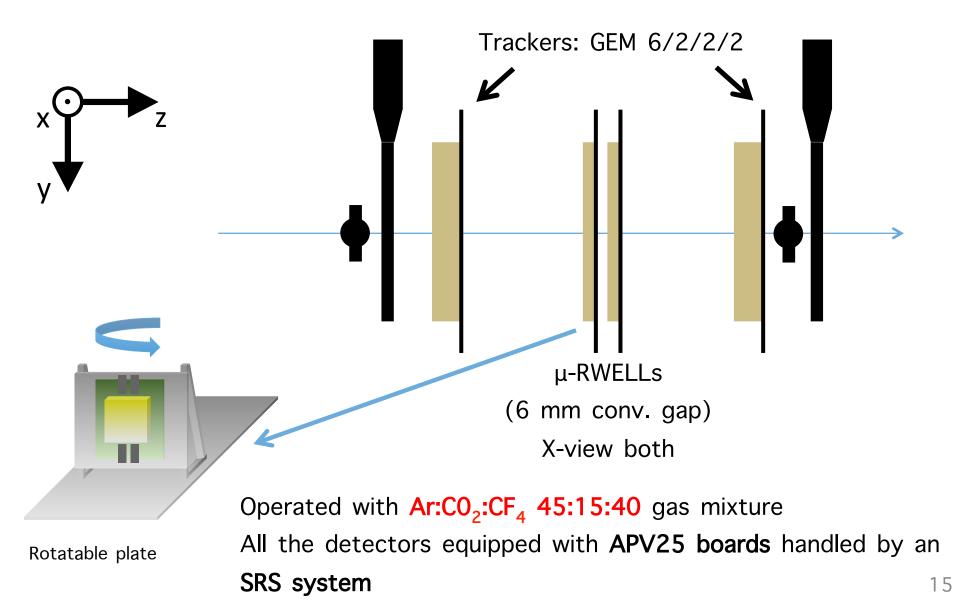
Two double resistive layer prototypes have been tested with muon beam and equipped with VFAT2



Same saturation observed in GEM detectors operated with the same FEE in the same test beam, while with GEM a time resolution of 4.8 ns has been obtained by LHCb [G. Bencivenni et al., NIM A 494 (2002) 156]

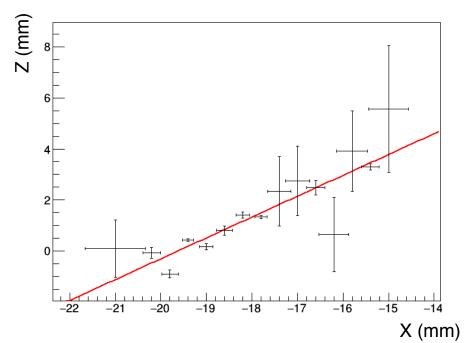
Thanks to L. Benussi, L. Borgonovi, P. Giacomelli, A. Ranieri, M. Ressegotti, I. Vai

The double resistive layer: H8C test beam (Oct. 2017)



The double resistive layer: H8C test beam (Oct. 2017)

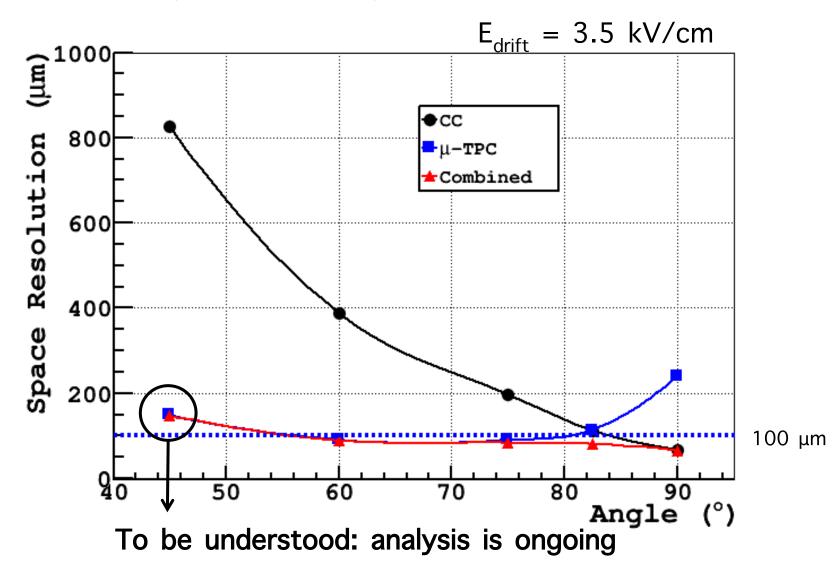
- Purpose of the test beam in the SHiP framework was the measurement of the spatial resolution vs. the track angle with respect to the readout plane
- We implemented the μ-TPC algorithm (T. Alexopoulos et al., NIMA 617 (2010) 161)



"Event display": hits projected in the conversion gap

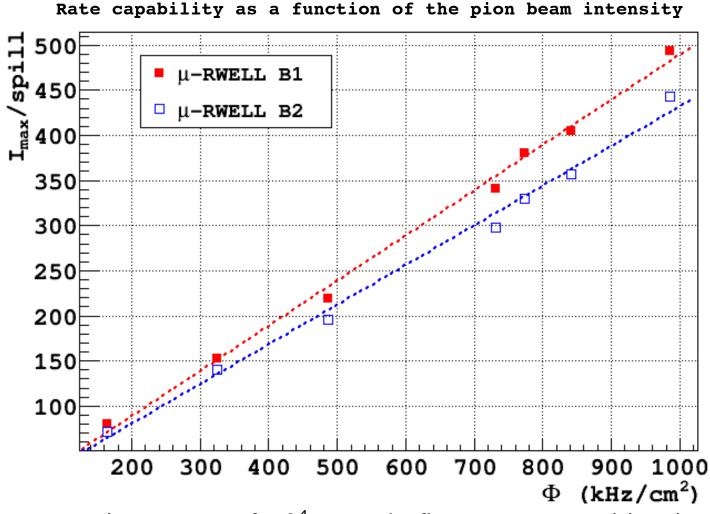
More details will be given tomorrow

The double resistive layer: H8C test beam (Oct. 2017)



Thanks to M. Giovannetti, L. Lavezzi, R. Farinelli

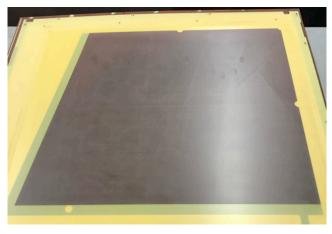
The double resistive layer: H8C test beam (Oct. 2017)



Detectors operated at a gain of 10^4 . Particle fluence estimated by the current drawn by a GEM detector. Beam spot ~2 cm²

Status of the technological transfer

• As already mentioned, the strict collaboration with **ELTOS** made possible the construction of large area detectors with single resistive layer (GE1/1-, GE2/1-like)





- This allowed us to well define the coupling procedure of the amplification stage with the readout
- ELTOS is now producing other μ -RWELL_PCB to be etched at CERN
- The industrialization of the double resistive layer construction is much more difficult
- Other (simpler) layouts must be developed in order to be included in an industrial process

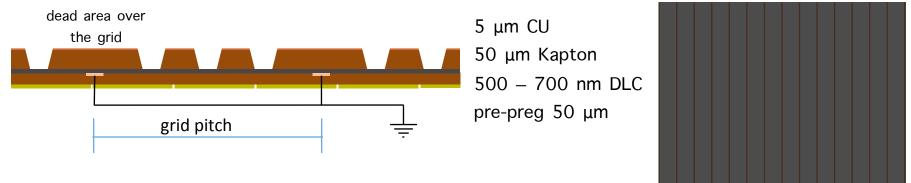
New layouts, new ideas, new challenges

The aim is to maintain a very short pattern for charges drifting on the resistive layer, while simplifying the construction process.

Two ideas are now under development: silver grid and resistive grid

Silver Grid (SG)

Small conductive strips are screen-printed on the bottom part of the DLC



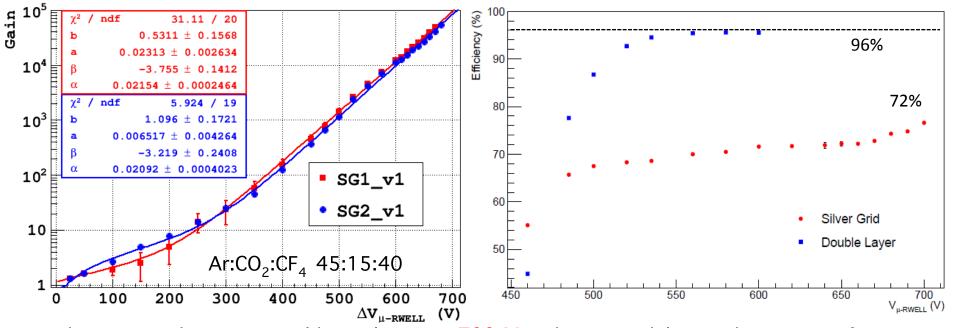
Clearly the introduction of a conductive strip on the bottom layer of the amplification stage can induce strong instabilities due to surface discharges.

> First prototypes of SG delivered in July and tested: grid pitch 6 mm, dead area around 1/3 of the total area

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Silver Grid v1: X-rays and H4 test beam (July 2017)

• A SG μ -RWELL has been installed inside the RD51 tracking system and characterized together with a Double Layer chamber



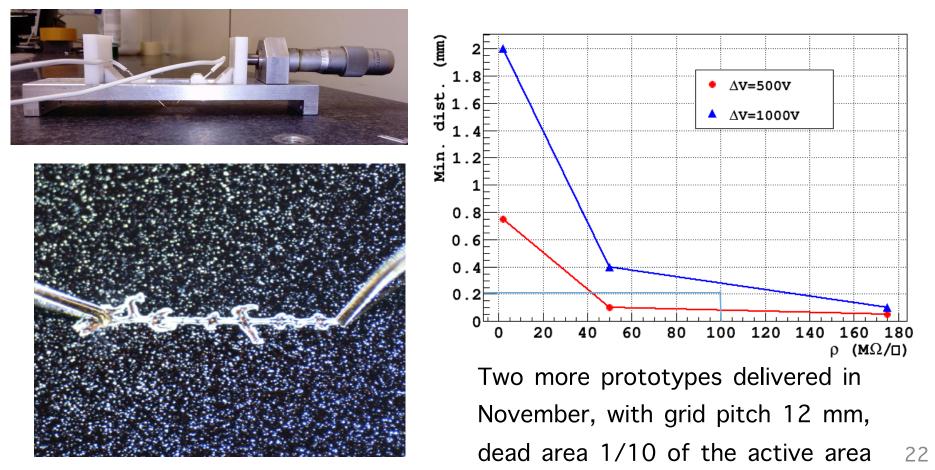
At the H4 test beam we could supply up to 700 V without instabilities. The reason of a so high breakdown voltage is under investigation.

The lower efficiency is due to the geometrical effects. The increasing gain improves the collection efficiency partially compensating this leak.

A dedicated study on the minimum distance between the strip and the holes has been done to increase the efficiency

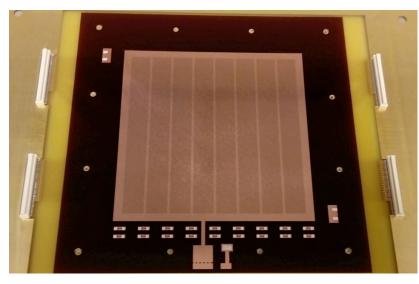
Silver Grid: optimization

In order to reduce the dead area, we measured the Distance Of Closest Approach between two tips connected to a PS. We recorded the minimum distance as a function of the ΔV supplied for different foils before a discharge on the DLC occurs

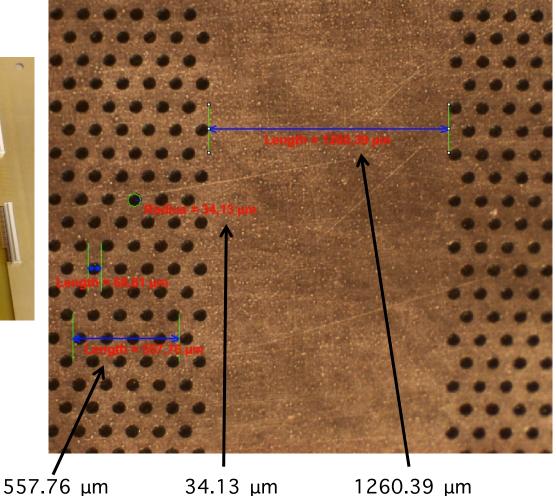


Silver Grid: 2nd generation

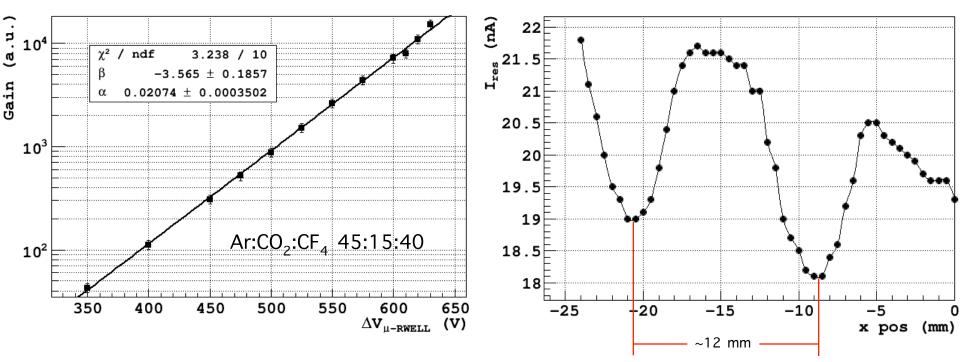
The two detectors have been equipped with 6 x 8 mm² pad-segmented readout

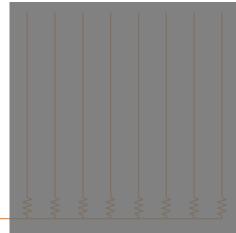


The grid lines are connected to the ground through the resistance provided by the DLC itself (9-10 $M\Omega$)



Silver Grid: 2nd generation





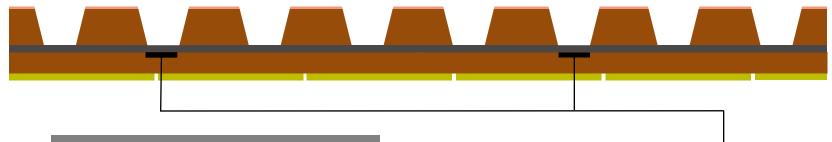
The detectors is mounted on a support moved by a stepper motor. The position is given within few tenths of millimeter.

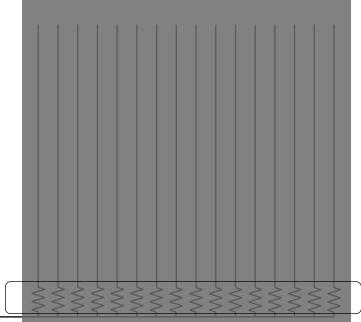
Scan along the coordinate orthogonal to the grid lines direction

24

Resistive grid

Small resistive strips are screen-printed on the bottom side of DLC



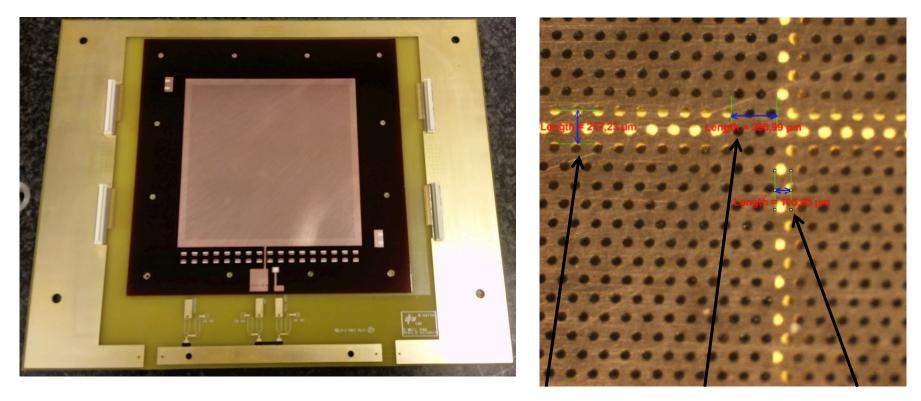


The grid grounding is similar to the one used for the 2^{nd} generation SG, as well as the readout segmented in pads.

Two prototypes delivered in November, with 6 mm grid pitch

Grounding through DLC

Resistive grid

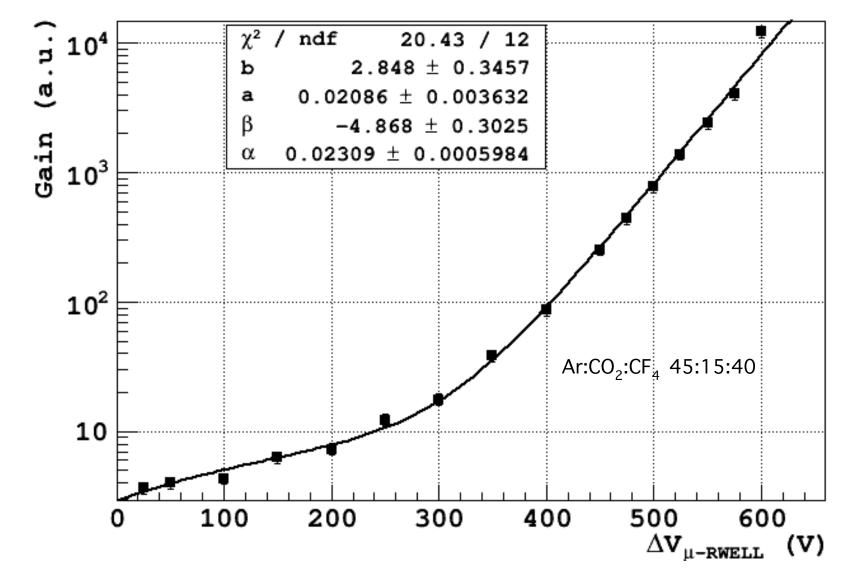


217.23 μm 296.99 μm 105.03 μm

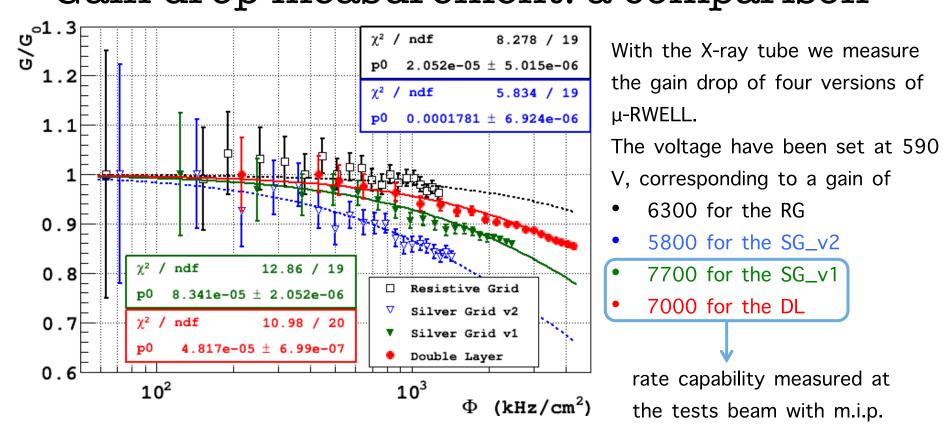
No dead areas

Grounding resistance: between 12 and 16 $\ensuremath{M\Omega}$

Resistive grid



Gain drop measurement: a comparison



The primary ionization of 5.9 keV is ~7 times the one created by a m.i.p.

The SG_v1 and the DL shown gain linearity up to 1 MHz/cm² with m.i.p. equivalent to a X-rays flux of about 140 kHz/cm².

By the fit function we expect a G/G_0 of:

99.7% for the RG; 97.6% for the SG_v2; 98.8% for the SG_v1; 99.3% for the DL

Conclusions & outlook

- Several prototypes have been realized, with different evacuation charge scheme
- The new resistive layout have the following parameters

	grid pitch	dead zone (2*DOCA-line/2)	Grid line width (mm)	Resistivity
SG_v1	6	2	0.2÷0.25	100 MΩ/□
SG_v2	12	1.2	0.2÷0.25	100 MΩ/□
RG	6	_	0.3	100 MΩ/□

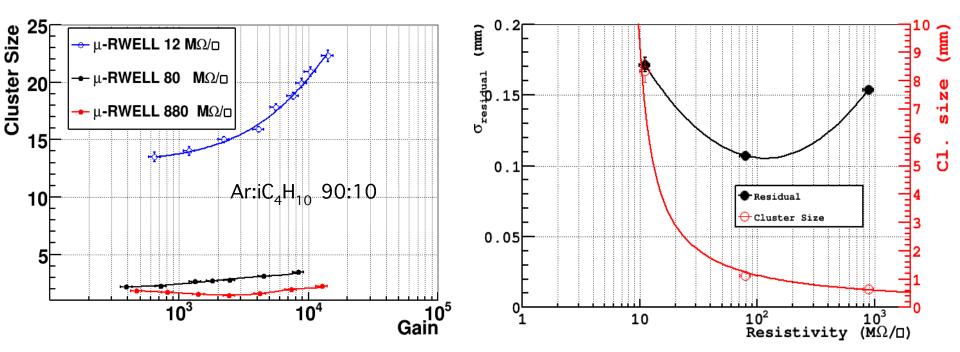
- So far the best measured performances are:
 - space resolution 52 \pm 6 μ m (80 M Ω / \Box , orthogonal tracks, no B field)
 - below 100 μm with non-orthogonal tracks, with the $\mu\text{-TPC}$ mode
 - time resolution **5.7 ns** (with FEE saturation)
 - 1 MHz/cm² rate capability with pion beam (Double Layer working at G=10000)
 - Both the Silver Grid v1 reached a gain of almost 10⁵ (to be understood) 29

Conclusions & outlook

- The single layer layout has been exploited to build large area detectors $(\sim 1/2 \text{ m}^2)$, but we also demonstrated that even larger detectors can be realized with the splicing technique
- This is done in strict collaboration with ELTOS company, within the Technological Transfer process
- In 2018 we plan to complete the technological transfer to industry of the single resistive layer $\mu\text{-RWELL}$
- In the same year we wish to complete the R&D of the high rates (DL, SG and RG) detectors (in order to be ready for TT in 2019)

Spare

The single resistive layer: H4 test beam (2015)



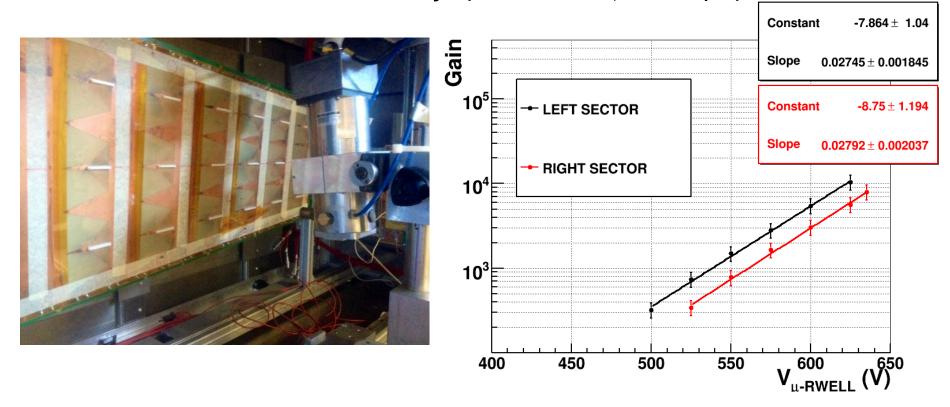
At low resistivity the spread of the charge (cluster size) on the readout strips increases, thus requiring a higher gain to reach the full detector efficiency.

The residuals exhibit a minimum width around 100 M Ω / \Box .

At low resistivity the charge spread increases \rightarrow worse spatial resolution At higher resistivity \rightarrow ~ 1 fired strip

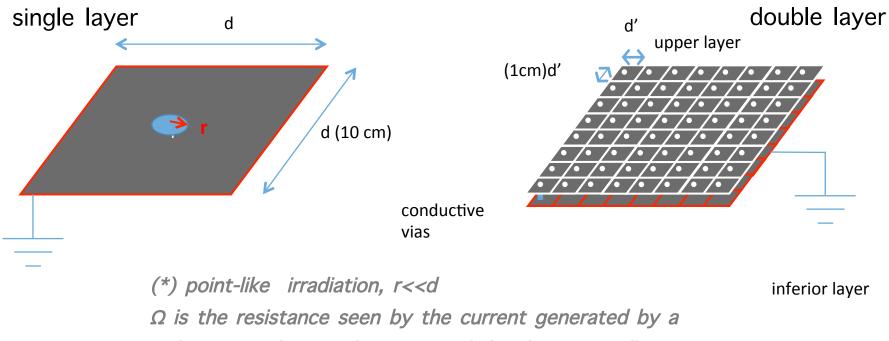
GE1/1 Detector Gain

The prototype has been characterized by measuring the gas gain, rate capability in current mode with an 5.9 keV X-rays (local irradiation, ~1cm² spot).



A shift of ~ 25 V has been measured between the two sectors probably due to the different geometry of the amplification stage (to be confirmed with microscope check – left/right asymmetry)

The two layouts



radiation incident in the center of the detector cell

 $\Omega \sim \rho_s x d/2\pi r$

Ω' ~ ρ_s' x d'/πr

 $\Omega / \Omega' \sim (\rho_s / \rho_s') \times d/3d'$

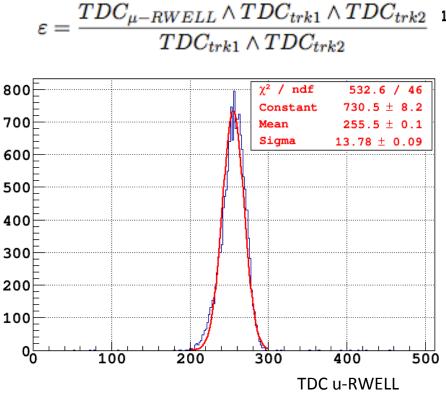
If $\rho_s = 2\rho_s' \rightarrow \Omega/\Omega' \sim \rho_s/\rho'_s * d/3d' = 2 * 5/3 = 3.3$

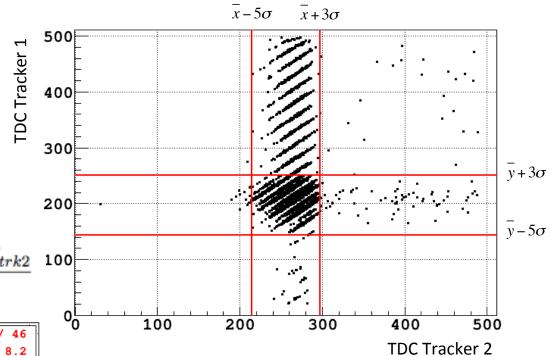
(*) Morello's model: appendix A-B (G. Bencivenni et al., 2015_JINST_10_P02008)

Efficiency & time resolution measurement

The efficiency (as extracted by TDC measurement) has been evaluated asking for TDC coincidence selected in a proper range.

Then the ratio of the triplets on the doublets gives the value.





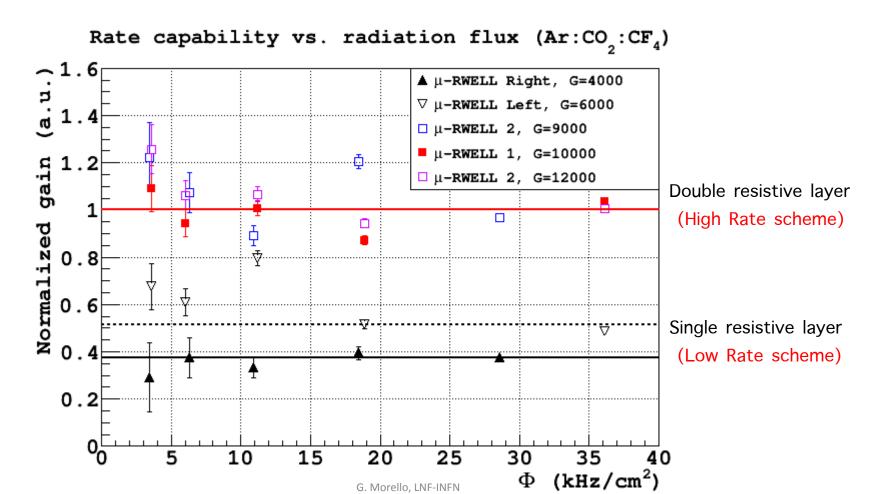
The TDC distribution is then fitted with a simple gaussian and the sigma is then **deconvoluted** by the contribution of the VFAT.

$$\sigma_t^2 = \sigma_{TDC}^2 - \left(\frac{25}{\sqrt{12}}\right)^2$$

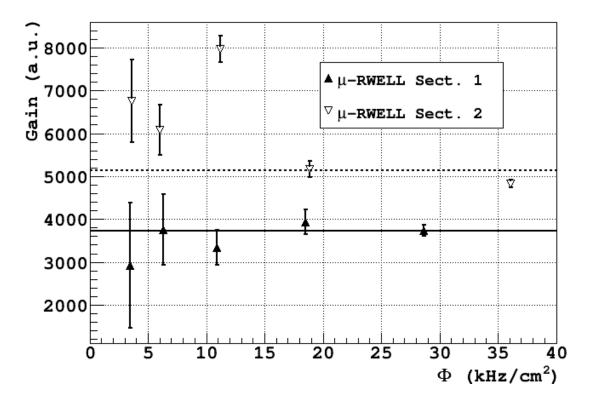
J. A. Merlin, Etude de fonctionnement à long terme de détecteur gazeux l'environment à haut flux de CMS, PhD thesis, 2016

Performance vs Rate

The detectors rate capability (with Ed=3.5 kV/cm) has been measured in current mode with a pion beam and irradiating an area of $\sim 3 \times 3 \text{ cm}^2$ (FWHM) ("local" irradiation, $\sim 10 \text{ cm}^2$ spot)

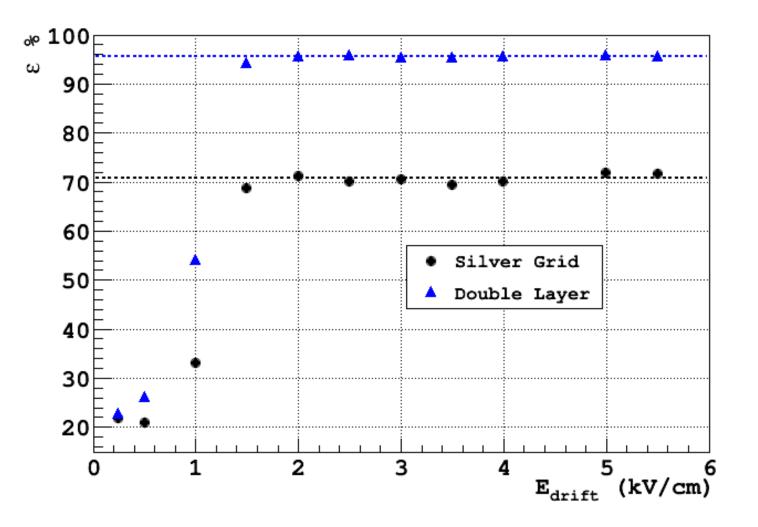


The single resistive layer: H8C test beam (2016)



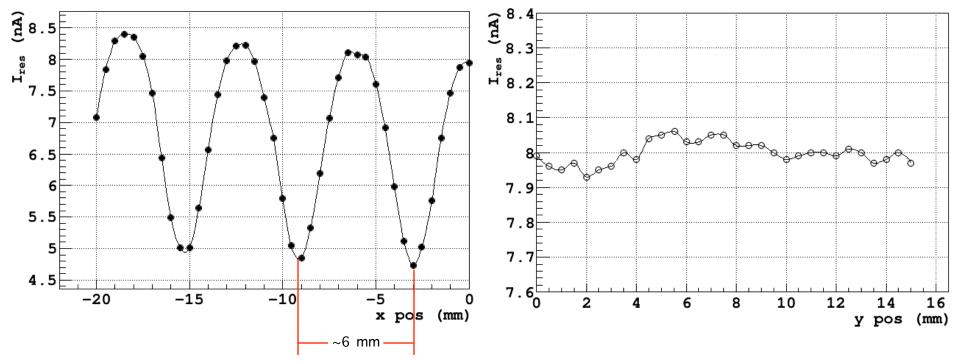
Rate capability measured with pion beam. The beam spot has a transverse section of **about 10 cm²**, one order of magnitude larger than our X-rays spot

Silver Grid: H4 test beam (July 2017)



Silver Grid v1: X-rays measurements

The detectors is mounted on a support moved by a stepper motor. The position is given within few tenths of millimeter



Scan along the coordinate orthogonal to the grid lines direction Scan along the coordinate parallel to the grid lines direction

Silver Grid v1: geometry

