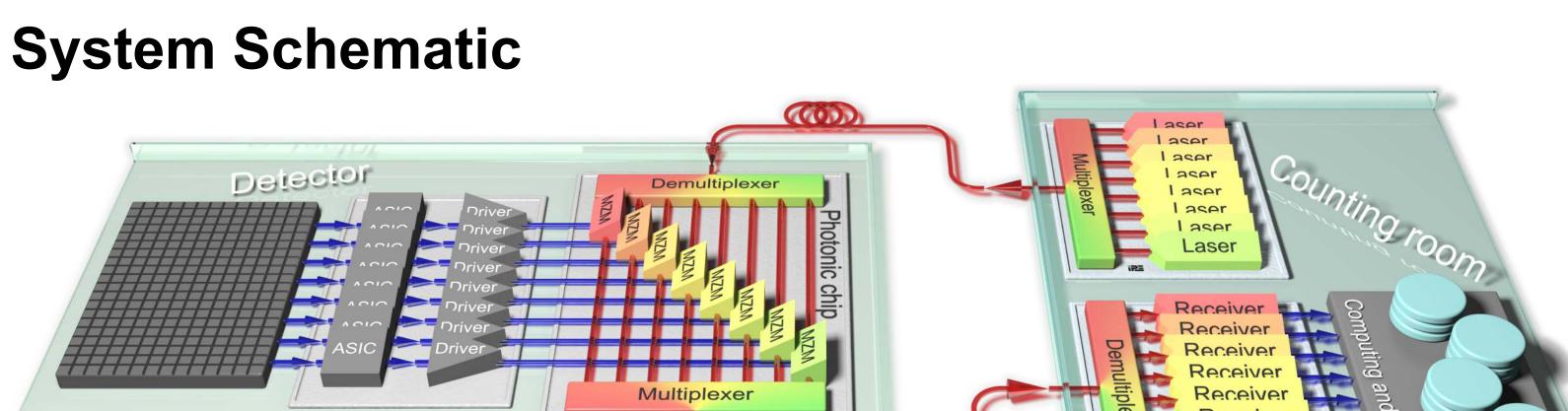


Karlsruhe Institute of Technology

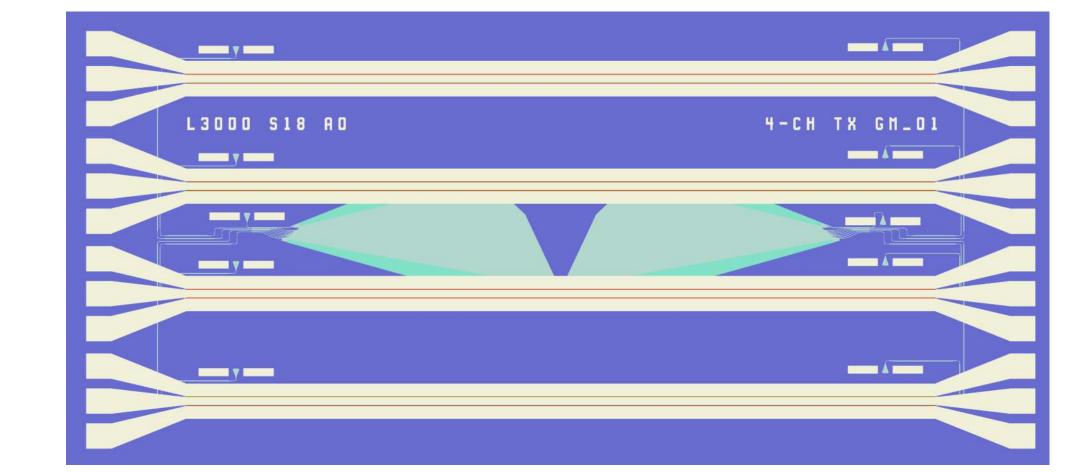
# Silicon photonic wavelength division multiplexed high-speed links

Marc Schneider, Djorn Karnick, Lars Eisenblätter, Yunlong Zhang, Julius Hartmann, Thomas Kühner, Marc Weber

Karlsruhe Institute of Technology, Institute for Data Processing and Electronics, Karlsruhe, Germany



## Integrated WDM System





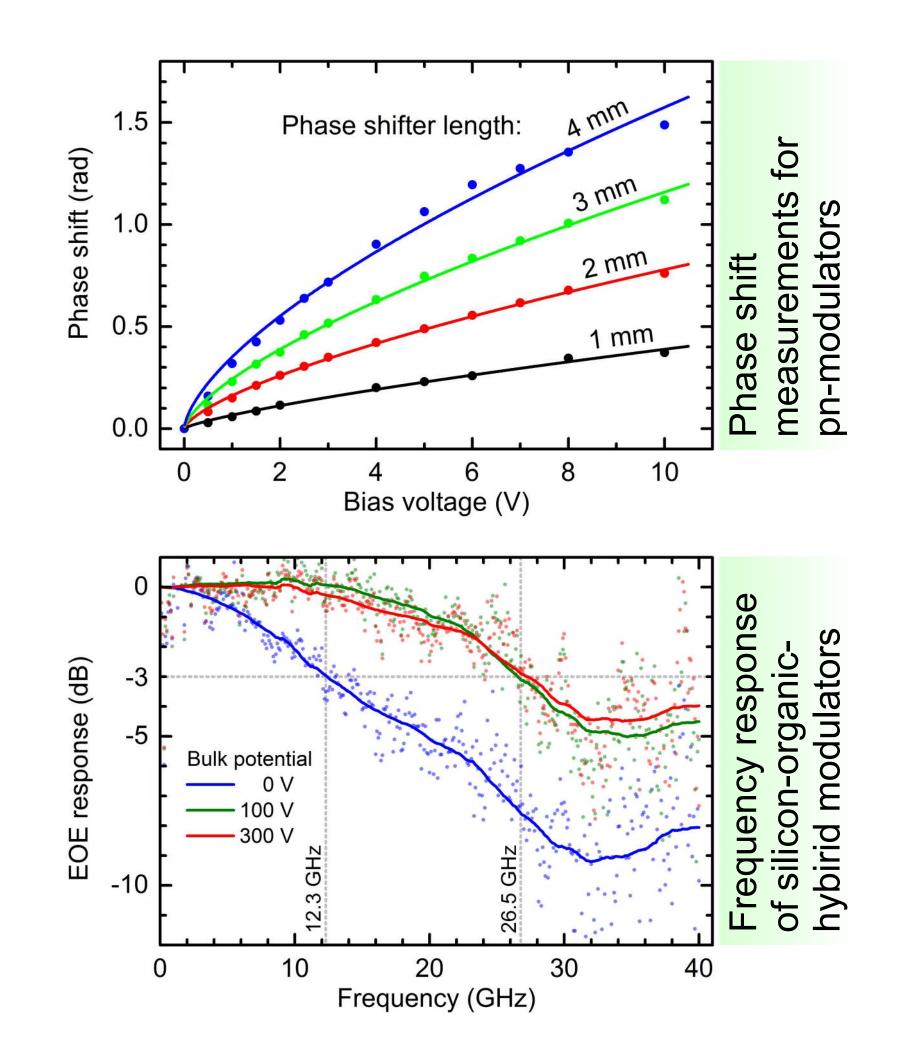


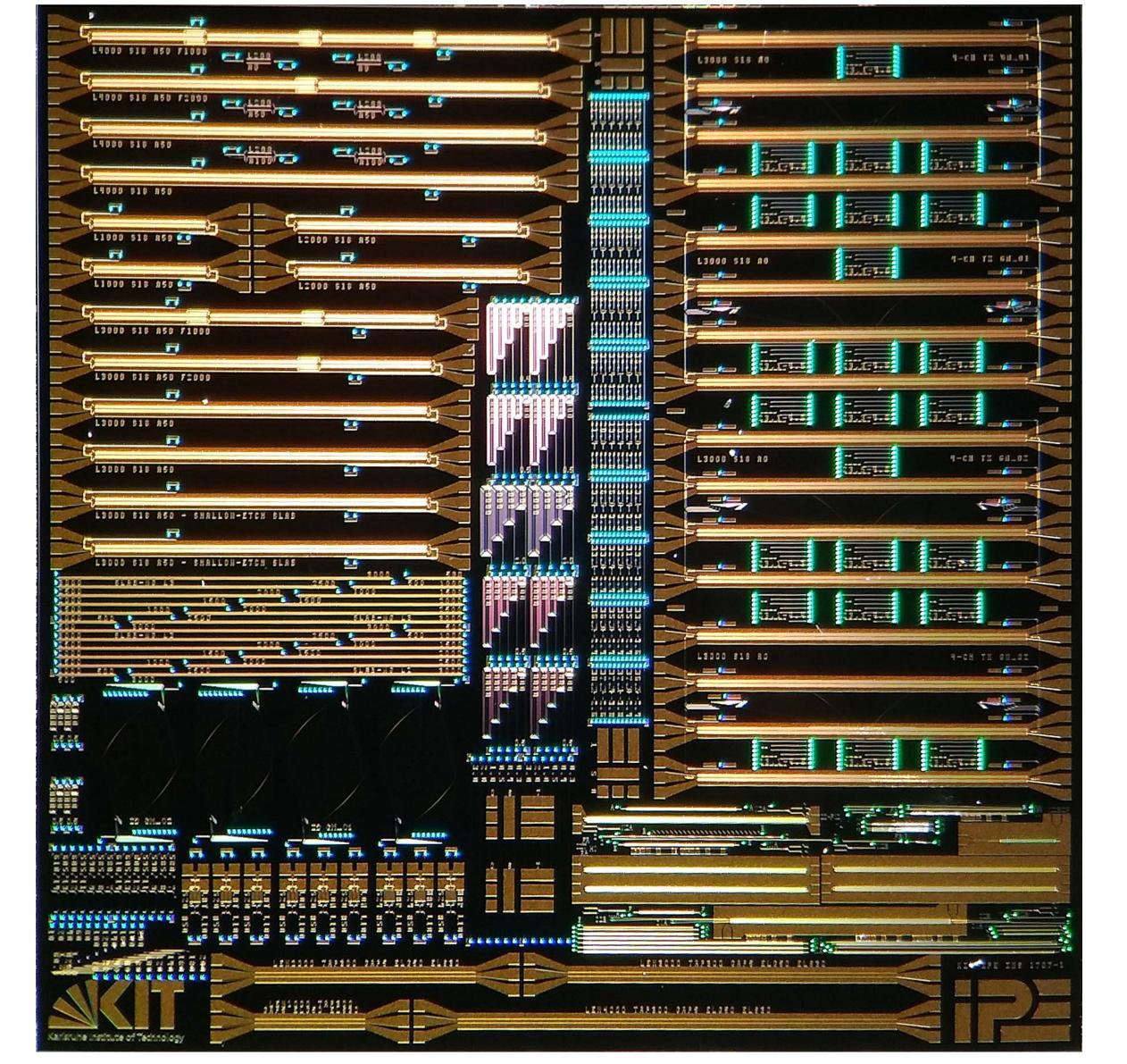
Fast optical data transmission out of detectors for high energy physics, astroparticle physics and photon science

Goal: 40 Gb/s (4×10 Gb/s), potentially up to 128 Gb/s with second generation driver

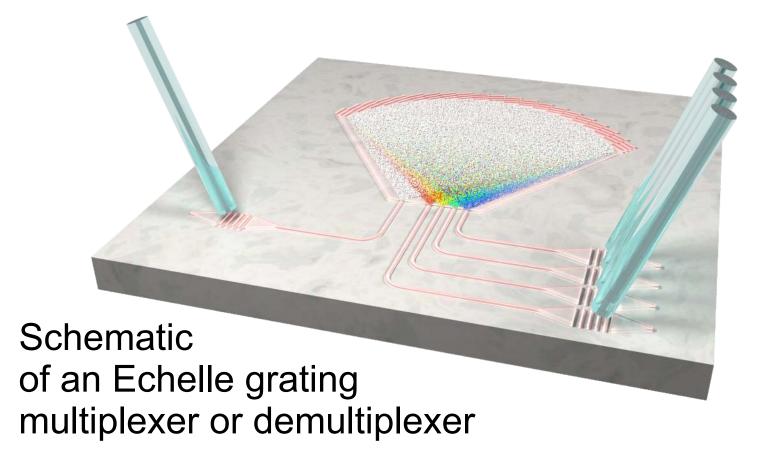
## **Electrooptic Modulators**

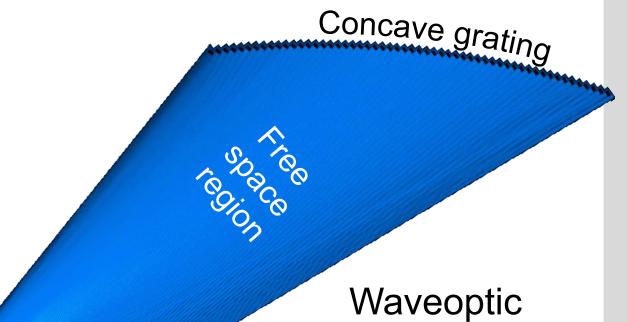
Schematic of a pn-phase-modulator: optical mode concentrated around pn-junction





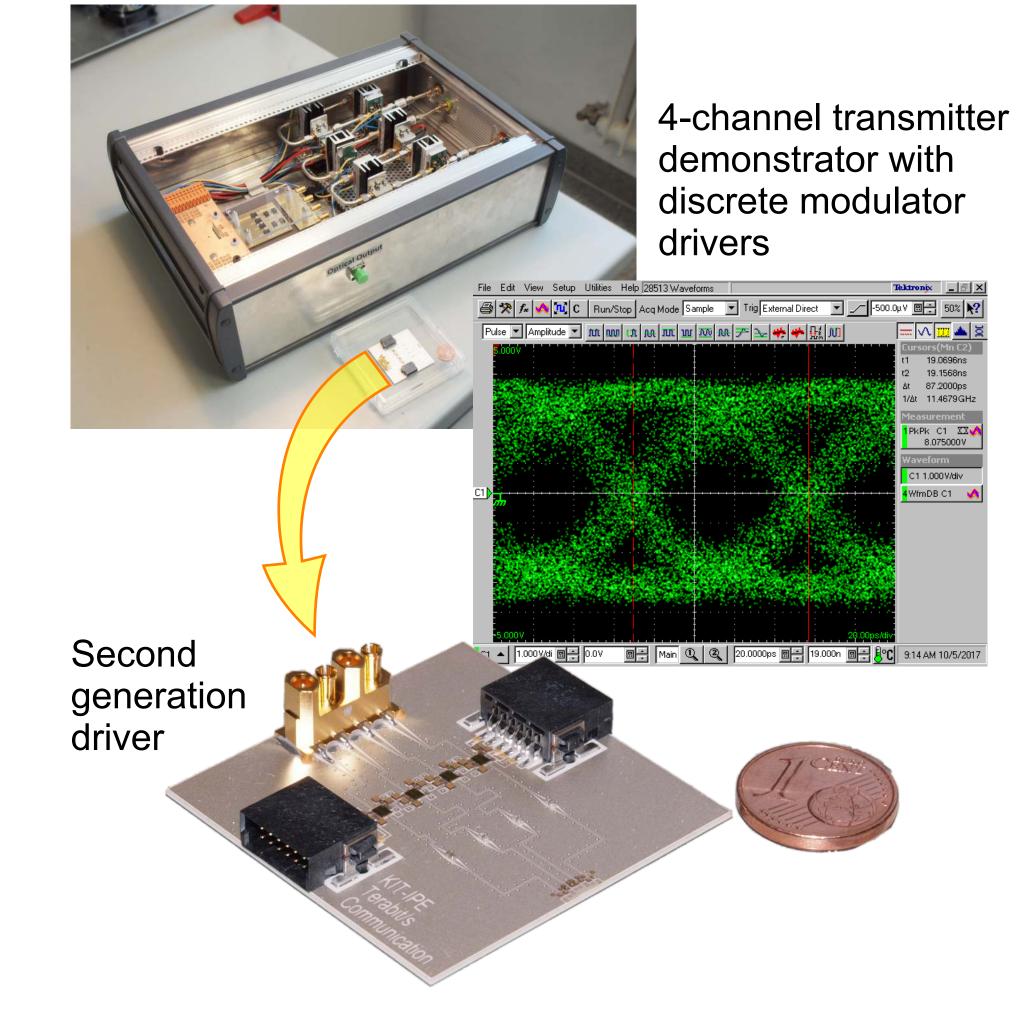
# (De-)Multiplexers



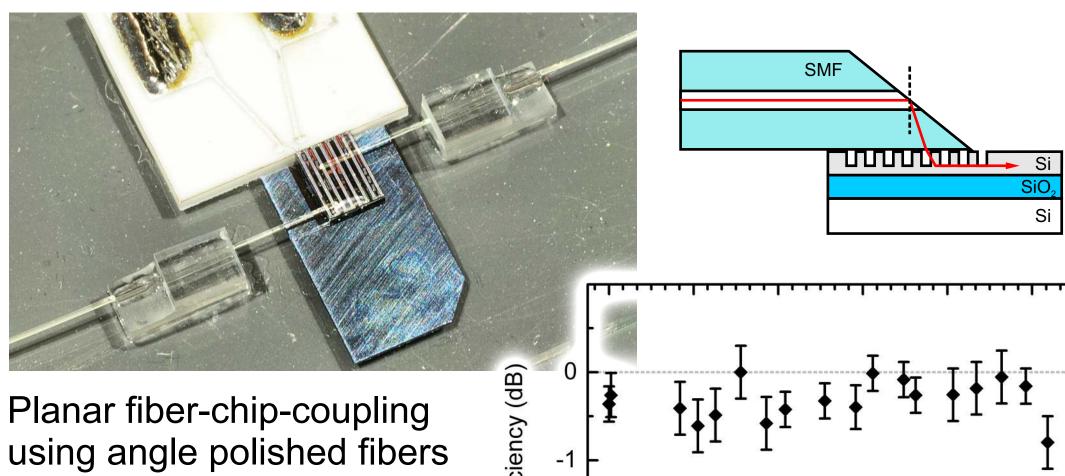


Photonic system chip  $(9.3 \times 9.3 \text{ mm}^2)$  with 4-channel WDM systems, single pn-modulators, thermal modulators, Echelle gratings, and test structures.

# **Modulator Drivers**



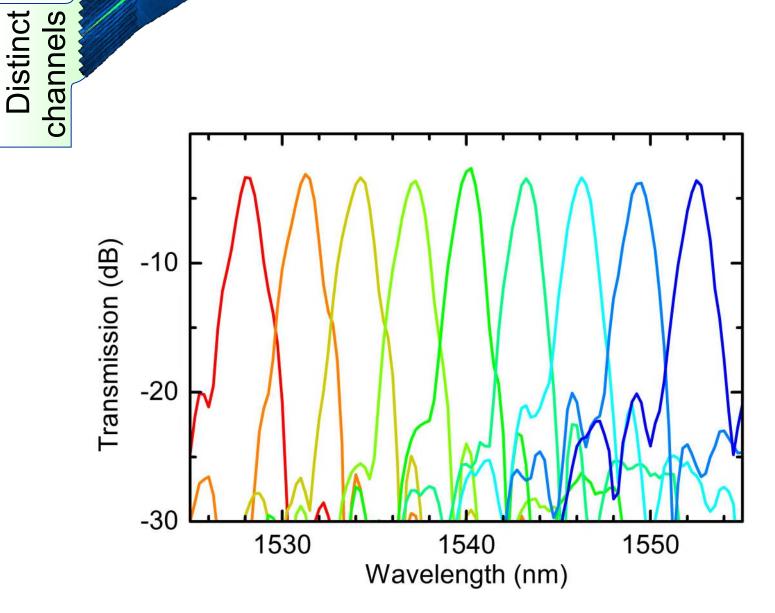
# Packaging



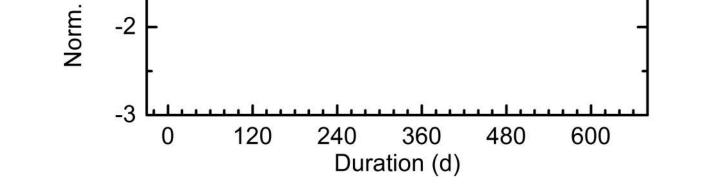
Effi

#### simulation of echelle grating with COMSOL Multiphysics

Common channel



Measured transmission spectrum of 9-channel Echelle grating



#### Vision

- **Downlinks** with monolithically integrated Ge-photodiodes
- Dynamically reconfigurable Rx and Tx channels
- Advanced modulation formats for higher speed
- Monolithic integration of sensors, ASICs, and photonics



**Partners** 

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KIT – The Research University in the Helmholtz Association

