



## 3D Integrated Circuits Lab Sessions

Contact Robert Patti

Phone +1-630-561-6813

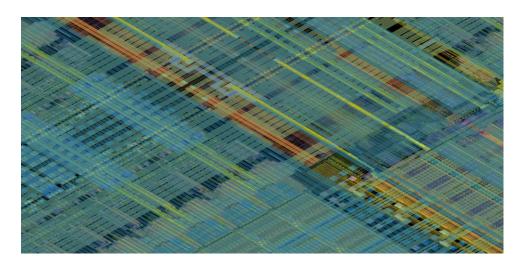
Email rpatti@nhanced-semi.com

Website www.nhanced-semi.com



## Introduction to Chip Design and Semiconductor Technologies

- Available days May 13<sup>th</sup> through May 20<sup>th</sup>
  - Basics of schematic capture
    - Using MicroMagic SUE
  - Basics of physical design
    - Using MicroMagic MAX
  - 3D integrated circuit design
    - Using MicroMagic MAX3D
- The focus of this labs will be introduction to chip design concepts and basic design tools.
- We will discuss how chips are made and why the industry is nearing the end of Moore's Law
- No previous experience is necessary.





## What Can Be Expected...

- Understanding basics of how chips are designed and manufactured
  - What is scaling and what are the limits
- What 2.5D and 3D integration is and how this advanced packaging technology can be employed
- Basic understanding of CAD flow for chip design

 Its an open forum to ask questions about semiconductor development and technology

NHanced Semiconductors 3