

# The LHCb Run 2 trigger as a benchmark for the upgrade

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The LHCb upgrade trigger represents a new paradigm in high energy physics: A fully software trigger operating at the LHC bunch crossing frequency with a triggerless readout. The existing level-0 hardware trigger in Run 2 has allowed us to test much of the upgrade strategy at 1MHz. In this talk, we will describe the performance of the Run 2 trigger in pp and special data taking configurations, and the implications for the deployment of the upgrade trigger at 30MHz.

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