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Development of Interconnect Technologies for HEP Applications

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HEP detectors are continually advancing towards higher segmentation of elements and higher complexity in assembly architectures. The task of interconnecting detector elements with readout electronics and with readout buses for such detectors poses new challenges and opportunities. We will address progress on several fronts: gold stud bonding, anisotropic conducting films, conductive epoxy stencils and flexible cable attachments. Some speculative concepts such as use of PDMS for single-die photolithography and as low-mass interposers will also be presented. Our work is aimed at generic R&D but focused towards detectors for the ILC and SuperLHC.

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