WIT2010 Workshop on Intelligent Trackers

Thursday, 4 February 2010

Electronic circuits (3D and conventional) - **Perseverance Hall (14:00 - 17:20)**

time	[id] title	presenter
14:00	[24] Application of Vertically Integrated Electronics to Intelligent Trackers	Dr LIPTON, Ronald
14:30	[27] Fast Readout Logic Interfacing a 256-Pixel Matrix of a Dual-Layer 3D Device	GABRIELLI, Alessandro
15:00	[36] Present and future inter pixel communication architectures in Timepix/Medipix derived read out chips	Mr LLOPART CUDIE, Xavier
15:30	[14] Towards a high performance vertex detector based on 3D integration of Deep N-Well MAPS	RE, Valerio
15:55	Coffee and discussion	