Session Program

20–25 May 2018

Front-End Electronics 2018

Readout Electronics

Wednesday 23 May

11:55

Readout Electronics

Session | **Location:** Jouvence 131 De Jouvence road, Orford, QC,J1X 6R2, Canada

11:55–12:20 **Large area SiPM readout**

Speaker
Alessadro Razeto

12:20

13:40

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13:40–14:05 **Dynamic compression of signals in charge sensitive amplifiers**

Speaker
Massimo Manghisoni

14:05–14:30
Development virtual-frisch grid ASIC (AVG2) for 3D position sensitive detectors

Speaker
Emerson Vernon

14:30–14:55 **Multi-channel 10b SAR ADCs in 65/130 nm**

Speaker
Marek Idzik

14:55–15:20 **Data readout strategies for pixel detectors**

Speaker
Farah Fahim

15:20

15:50

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15:50–16:15 **Fast-Gated Multi-Frame CMOS Sensors and ASICs for HED Programs**

Speaker
Liam Claus

16:15