



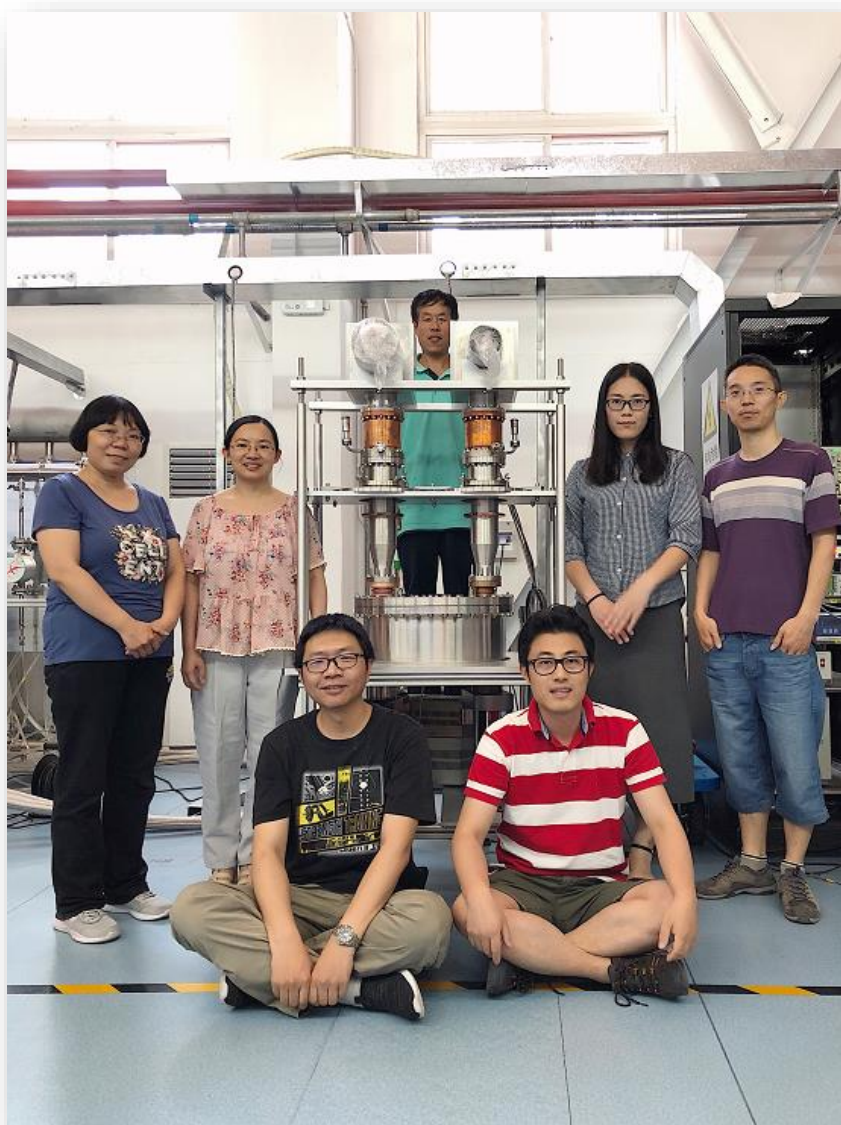
中国科学院高能物理研究所  
INSTITUTE OF HIGH ENERGY PHYSICS  
CHINESE ACADEMY OF SCIENCES

# The HPRF system for a new 6 GeV synchrotron light source in Beijing

Pei Zhang  
(RF group, IHEP)



# The HEPS HPRF team



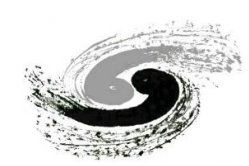
## Power coupler & power source

Researcher (3): T.M. Huang, W.M. Pan, P. Zhang

Engineer (3): Q. Ma, H.Y. Lin, Q.Y. Wang

Technician (1): L.S. Feng

Postgraduate (2): F. Bing, Y.L. Luo



# Outline

**HEPS**

**HPRF for HEPS main ring (166MHz)**

**HPRF for HEPS booster (500MHz)**

**Power coupler**

**Summary & Questions**

# High Energy Photon Source





# The new light source in 7 years







# Location in Beijing



HEPS

IHEP campus







# Beam parameters

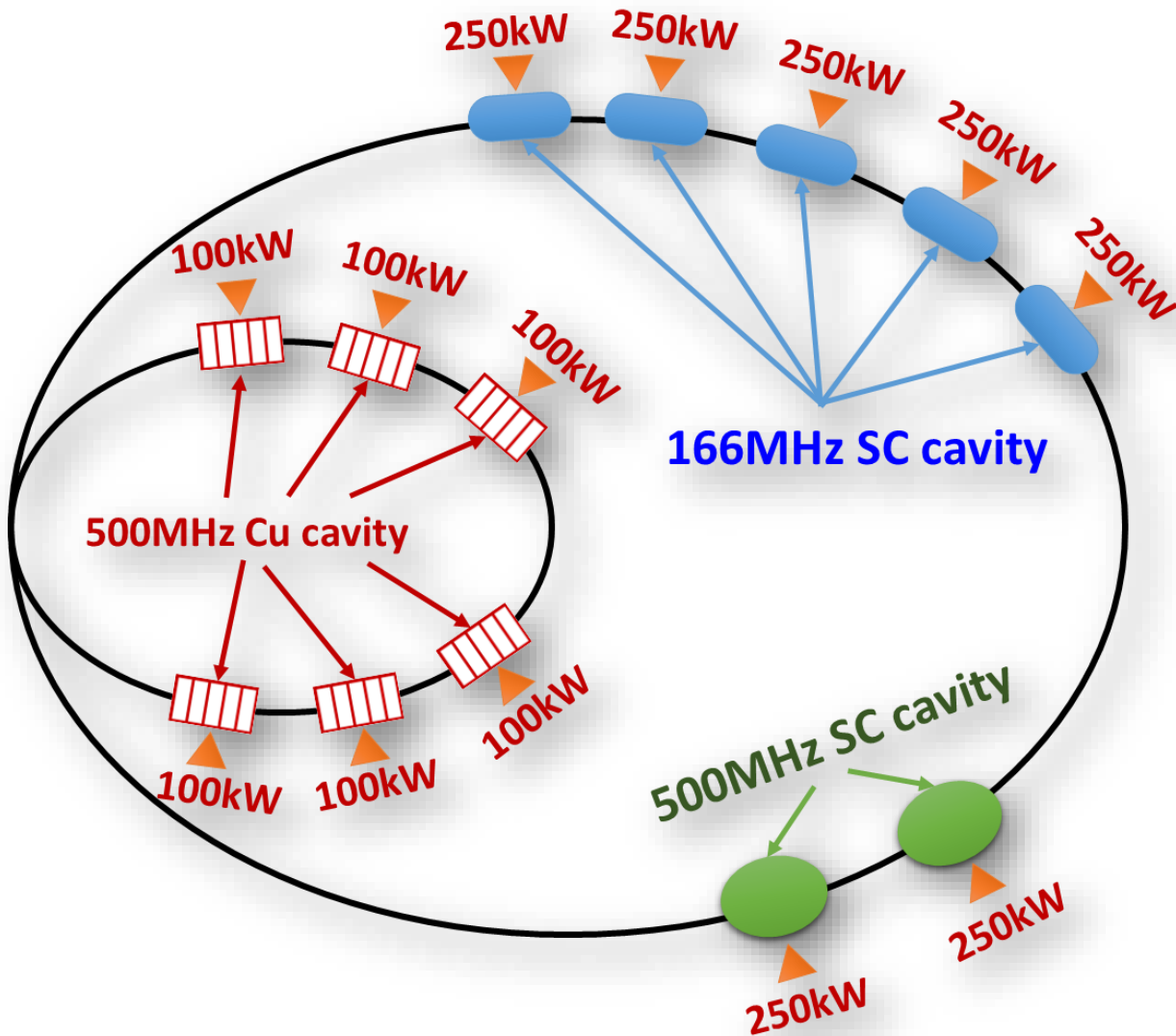
## High Energy Photon Source (HEPS)

Energy	6 GeV
Circumference	~1300 m
Beam Current	200 mA
Natural emittance	<60 pm·rad
$U_0$ (w/ ID)	4.5 MeV
Total SR power	900 kW

2016 – 2018 HEPS-TF: R&D phase

2018 – 2025 HEPS (CD0 passed, CD1 review soon)

# RF system



## Fundamental SRF (SR)

- RF frequency: 166.6 MHz
- RF voltage in total: 5.5 MV
- Min. power per cavity: 180 kW

## Third harmonic SRF (SR)

- RF frequency: 499.8 MHz
- RF voltage in total: 1 MV/3.2 MV
- Passive/Active cavity

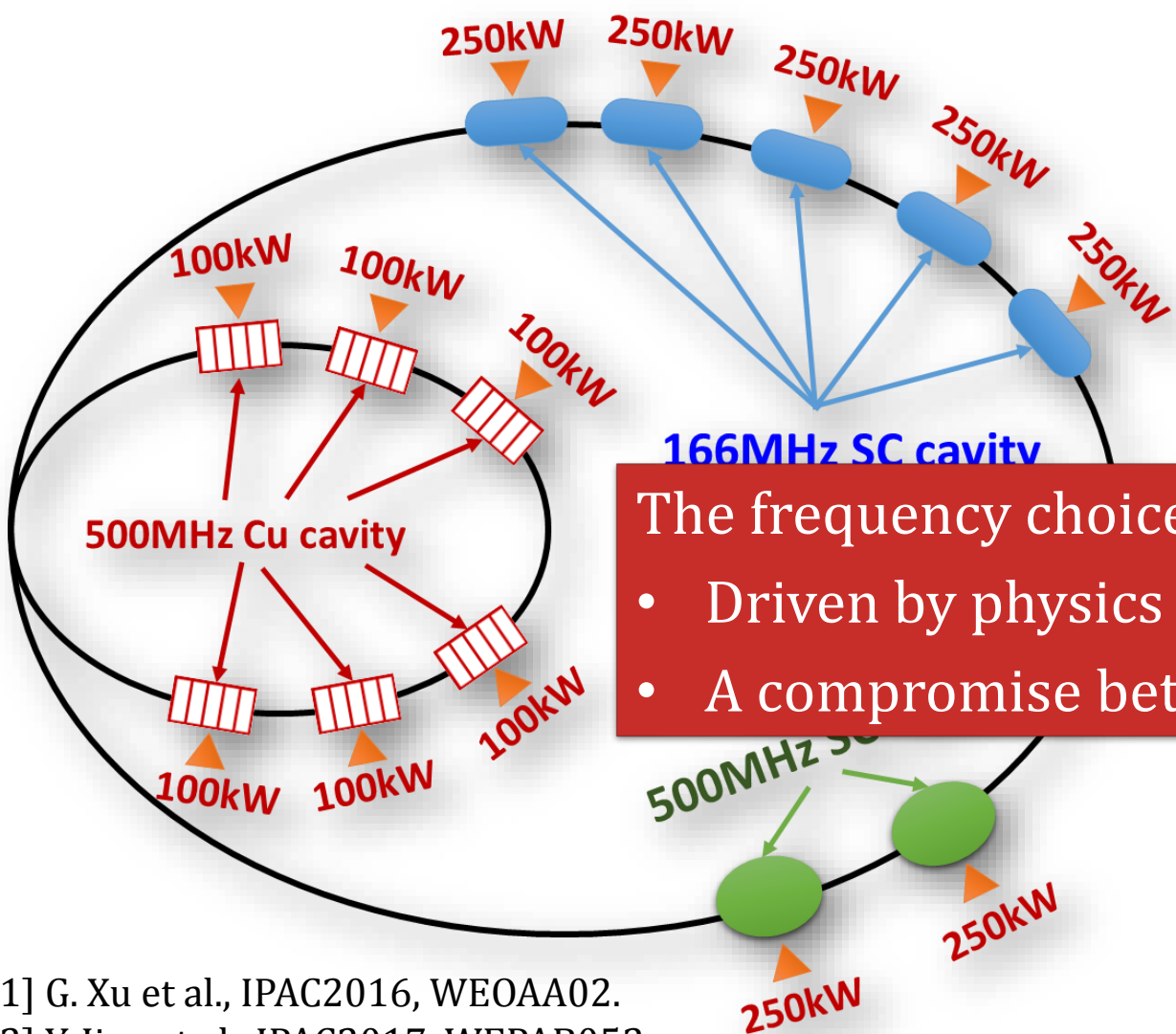
## Booster NC RF

- RF frequency: 499.8 MHz
- RF voltage in total: 8 MV
- Min. power per cavity: 80 kW





# RF system



## Fundamental SRF (SR)

- RF frequency: 166.6 MHz
- RF voltage in total: 5.5 MV
- Min. power per cavity: 180 kW

## Third harmonic SRF (SR)

- RF frequency: 499.8 MHz
- RF voltage in total: 8 MV
- Min. power per cavity: 80 kW

The frequency choice was

- Driven by physics (mainly injection scheme)
- A compromise between kicker and RF

## Booster NC RF

- RF frequency: 499.8 MHz
- RF voltage in total: 8 MV
- Min. power per cavity: 80 kW

[1] G. Xu et al., IPAC2016, WEOAA02.  
 [2] Y. Jiao et al., IPAC2017, WEPAB052.



# Power source

Accelerator	Frequency	Technology	RF power /station	# of RF station	Total RF power
Main ring	166.6 MHz	Solid-state	250 kW	5	1.25 MW
	499.8 MHz		250 kW (active) ~10 kW (passive)	2	500 kW 20 kW
Booster	499.8 MHz		100 kW	6	600 kW

- **Technology readiness**

- 166MHz: broadcasting and television frequency
- 500MHz: popular frequency for light sources

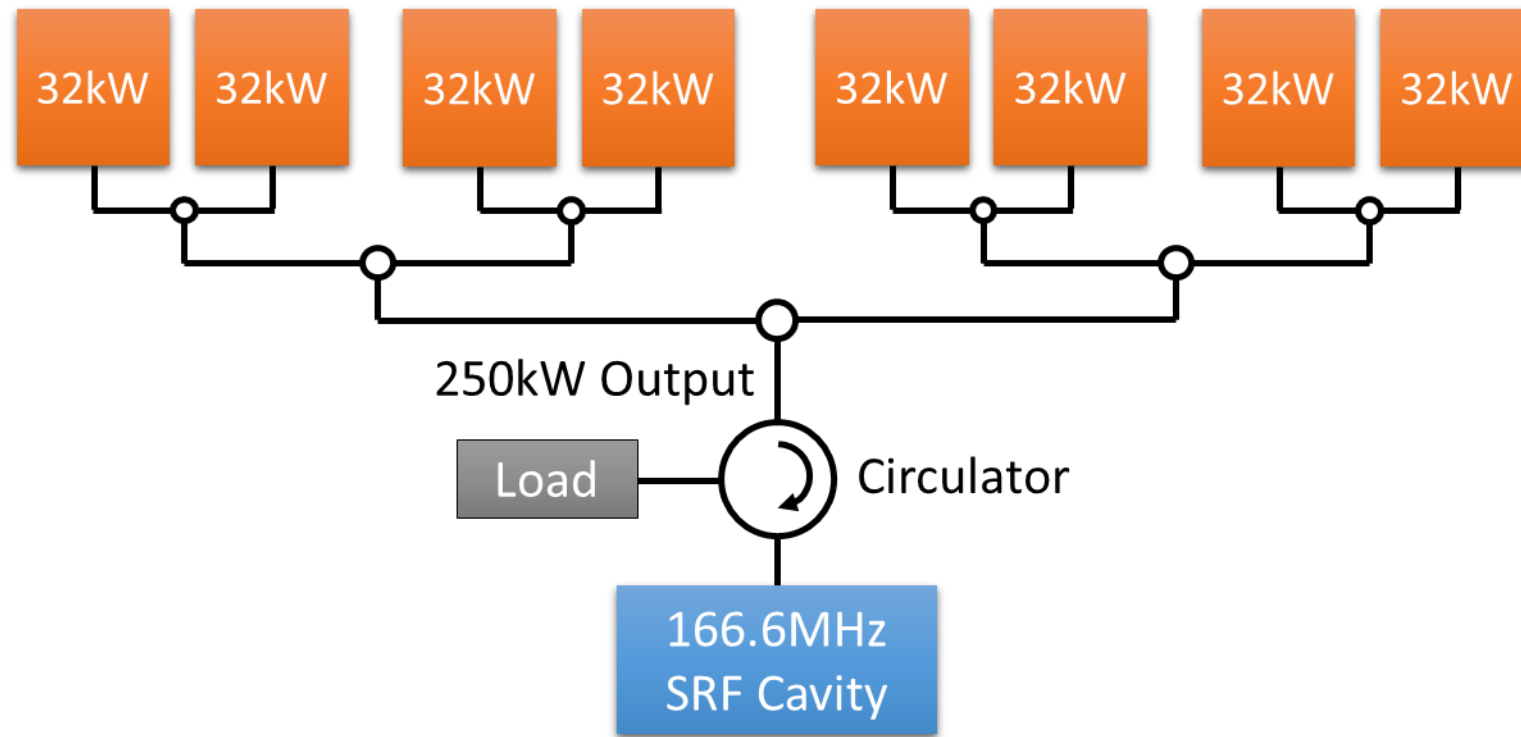
# 166.6MHz SSPA

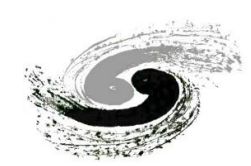




# Architecture

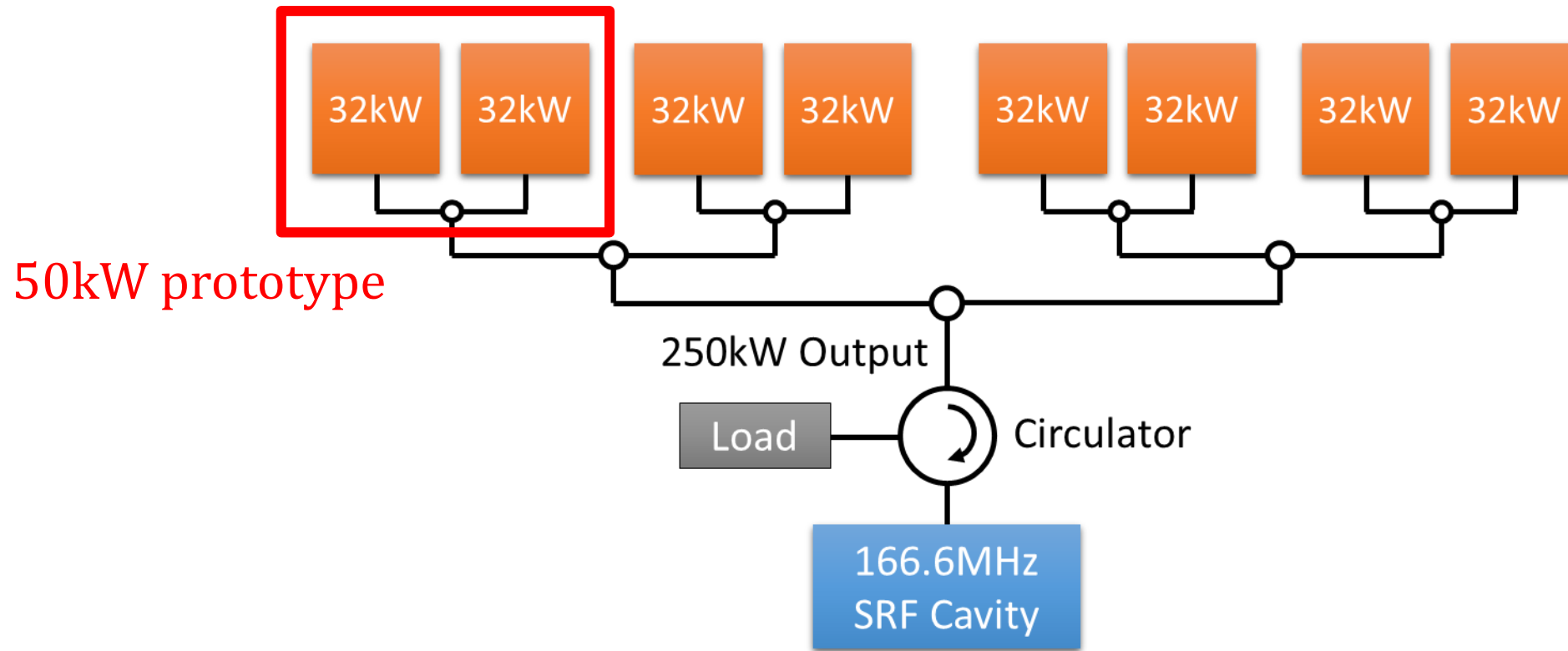
- 4-stage coaxial combiner following the 1<sup>st</sup> stage strip-line combiner
- Final stage circulator between cavity and SSPA

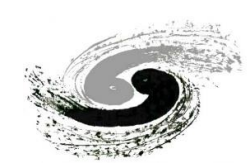




# Architecture

- 4-stage coaxial combiner following the 1<sup>st</sup> stage strip-line combiner
- Final stage circulator between cavity and SSPA





# 166.6MHz 50kW prototype



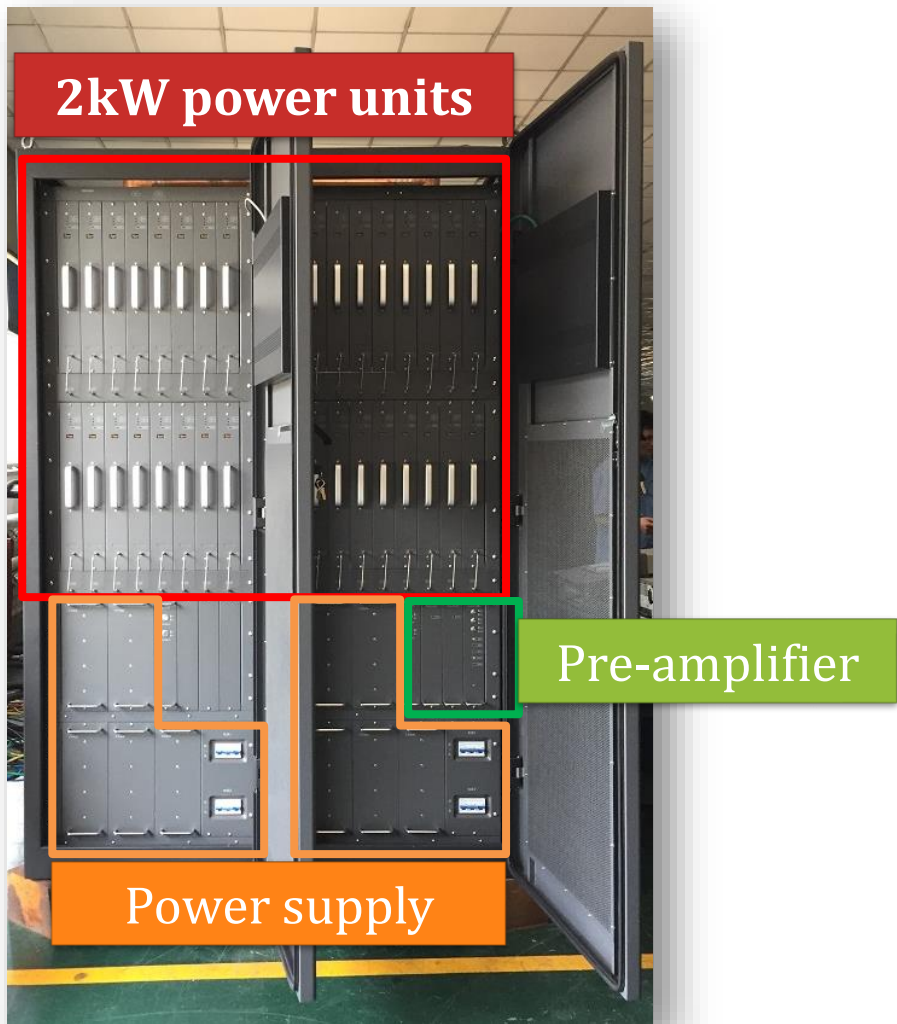
成都凯腾四方数字广播电视设备有限公司  
Chengdu Weingarten Quartet digital radio and Television Equipment Company Limited



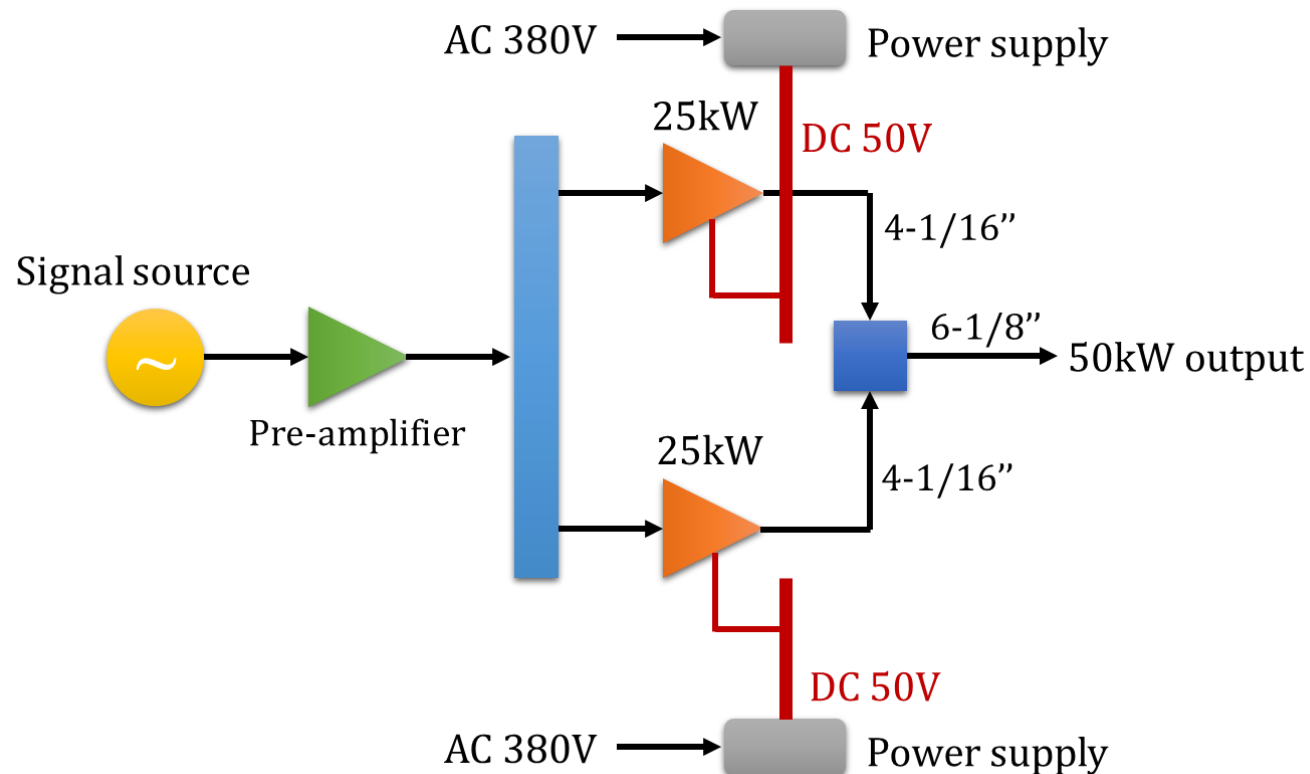
- 07.2016 Call for tender
- 08.2016 Contract awarded to KTSF630
- 02.2017 2kW unit passed factory acceptance test
- 06.2017 50kW SSPA passed factory acceptance test
- 08.2017 Reception at IHEP
- 10.2017 50kW SSPA passed final tests



# 166.6MHz 50kW prototype

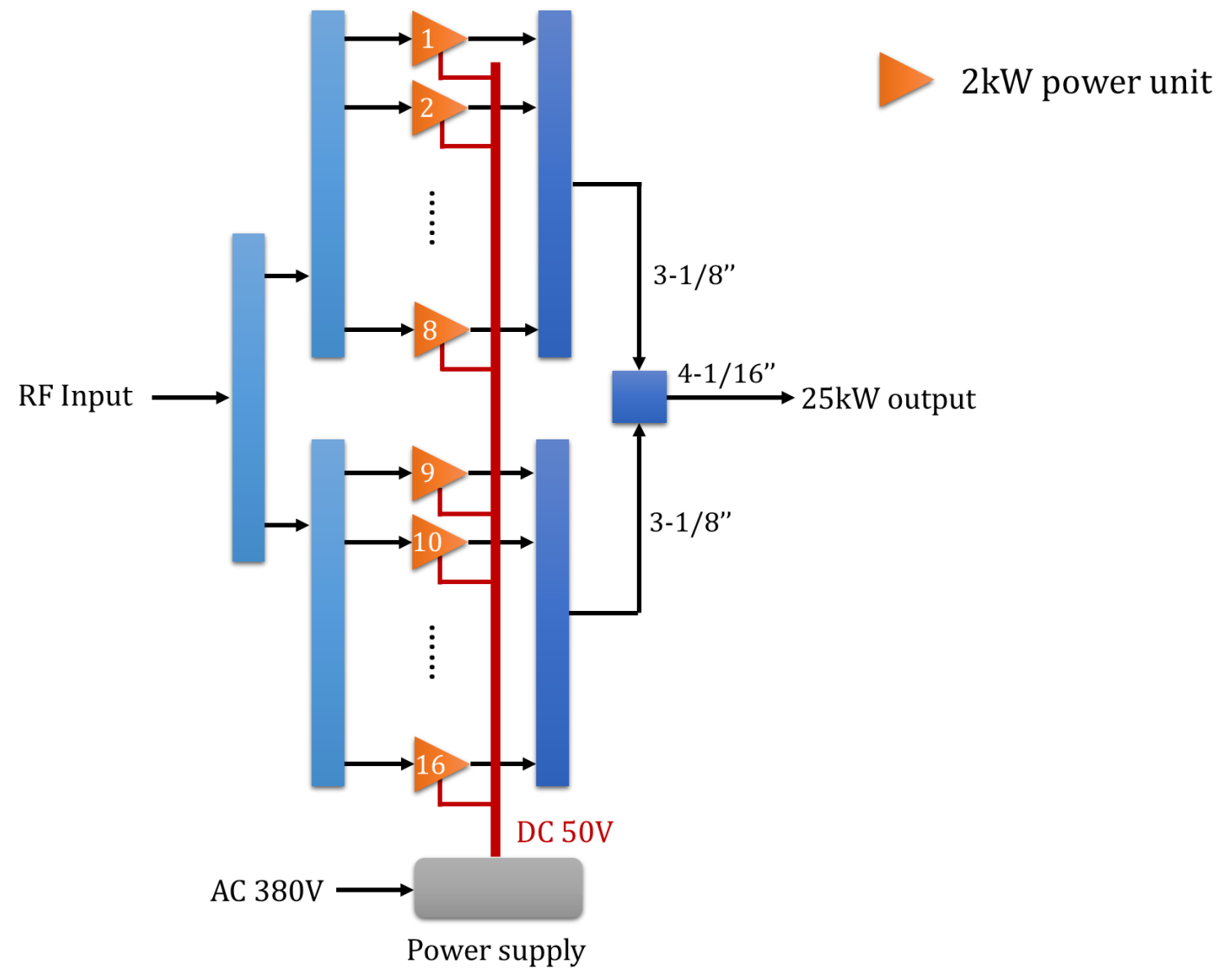


- $2 \times 32\text{kW}$  power cabinet
- $16 \times 2\text{kW}$  power units/cabinet
- 50V uniform power supply
- 2-stage coaxial power combiner





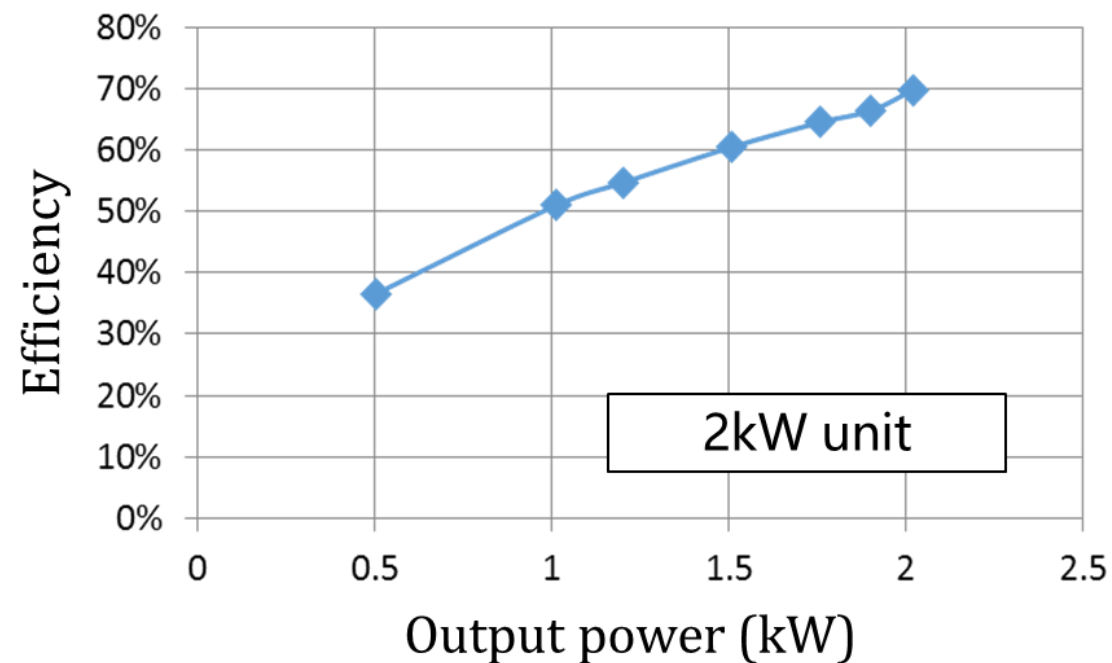
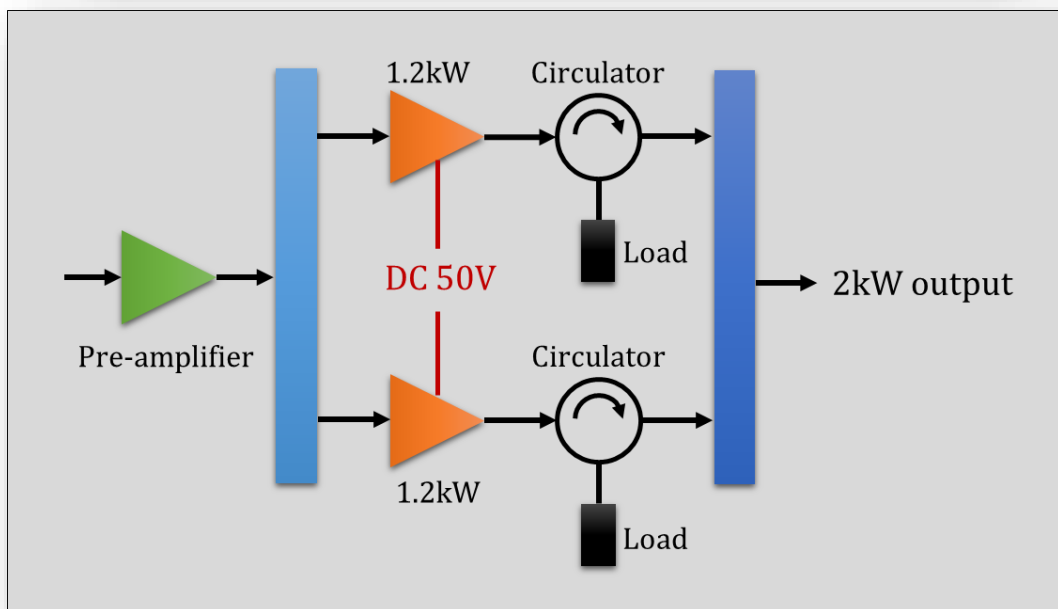
# 166.6MHz 25kW cabinet



# 2kW power unit



- 2kW power unit
  - 2 × LDMOS transistor
  - 1 circulator per transistor
  - water-cooled







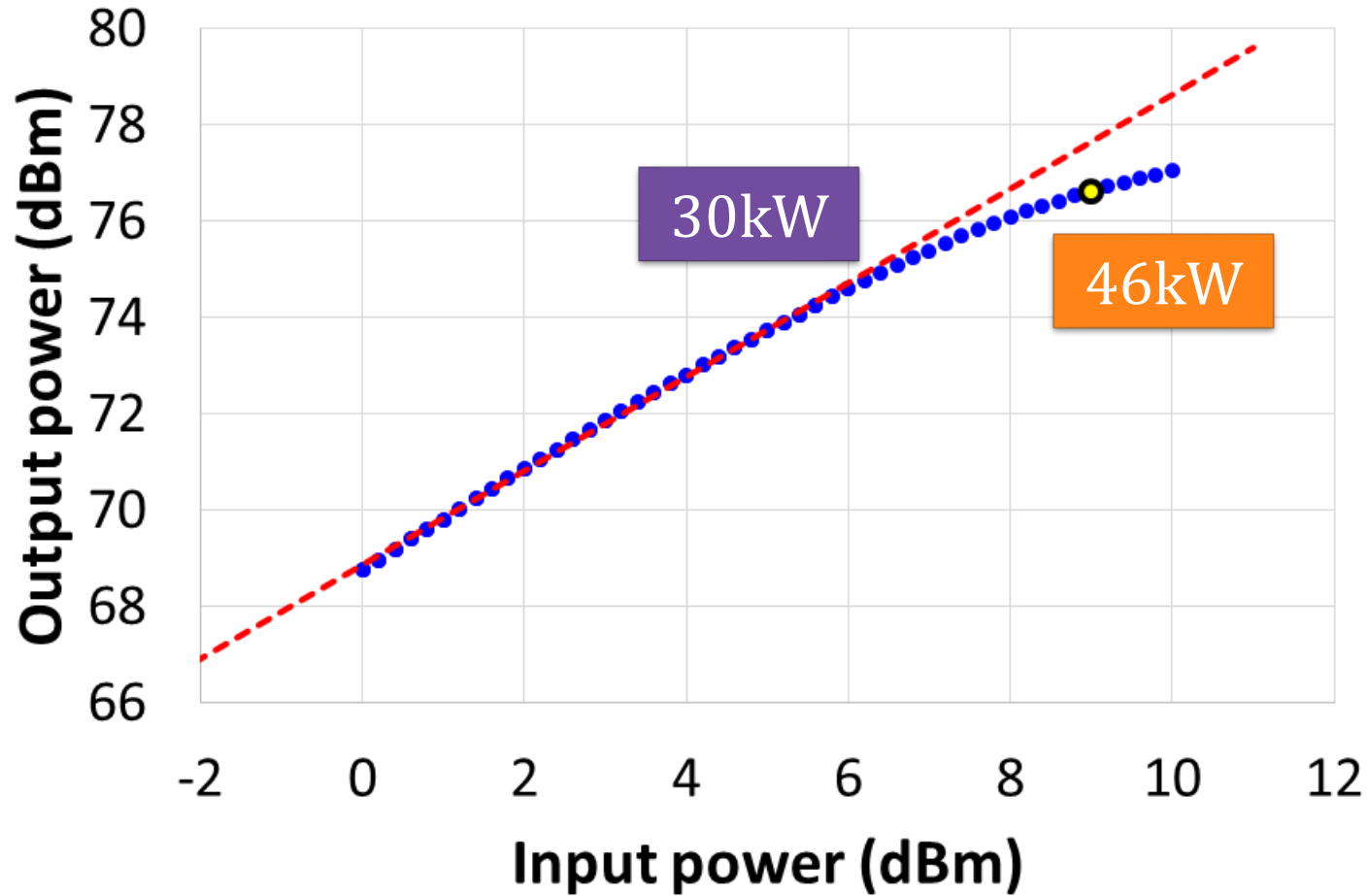
# Main parameters

No.	Parameter	Target value	Measured at IHEP
1	RF frequency	166.6MHz	166.6MHz
2	Bandwidth	$\pm 2$ MHz (50kW output)	9.1MHz (3dB)
3	Mode	CW/Pulse	OK
4	Nominal output power	50kW	50kW
5	Power output at 1dB compression	50kW (linear up to 40kW)	46kW
6	Amplitude stability	$\pm 1\%$ and $\pm 1^\circ$ @50kW	$\pm 0.06\%$ , $\pm 0.15^\circ$
7	Redundancy	6.25%	OK
8	Phase noise (1kHz carrier offset)	$\leq -70$ dBc	-114.2dBc
9	Harmonic suppression	$\leq -30$ dBc	-38.6dBc
10	Spurious suppression (offset > 1kHz)	$\leq -70$ dBc	-98.2dBc
11	Overall efficiency	$\geq 50\%$	57%



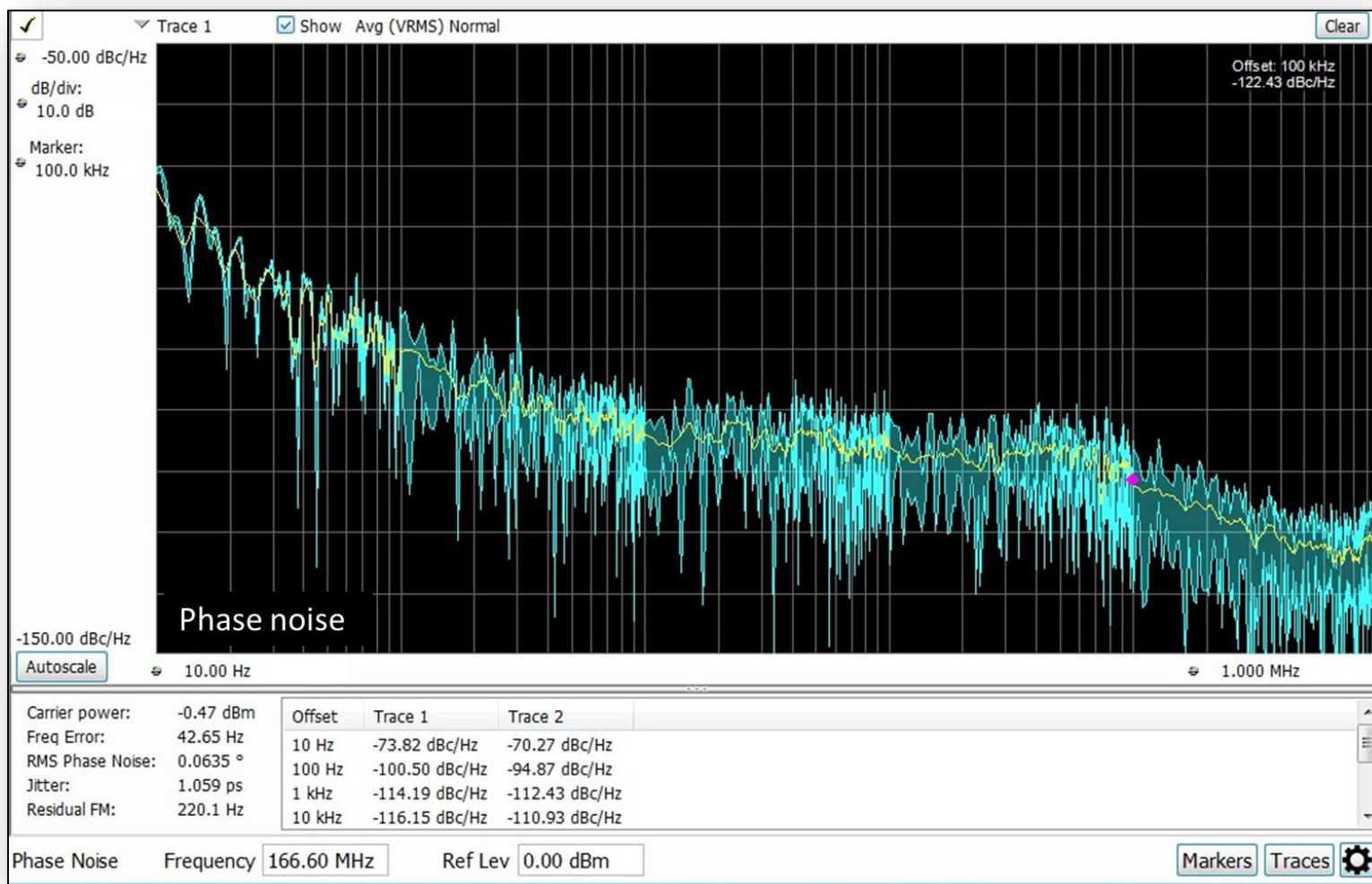
# Output power

Early compression observed





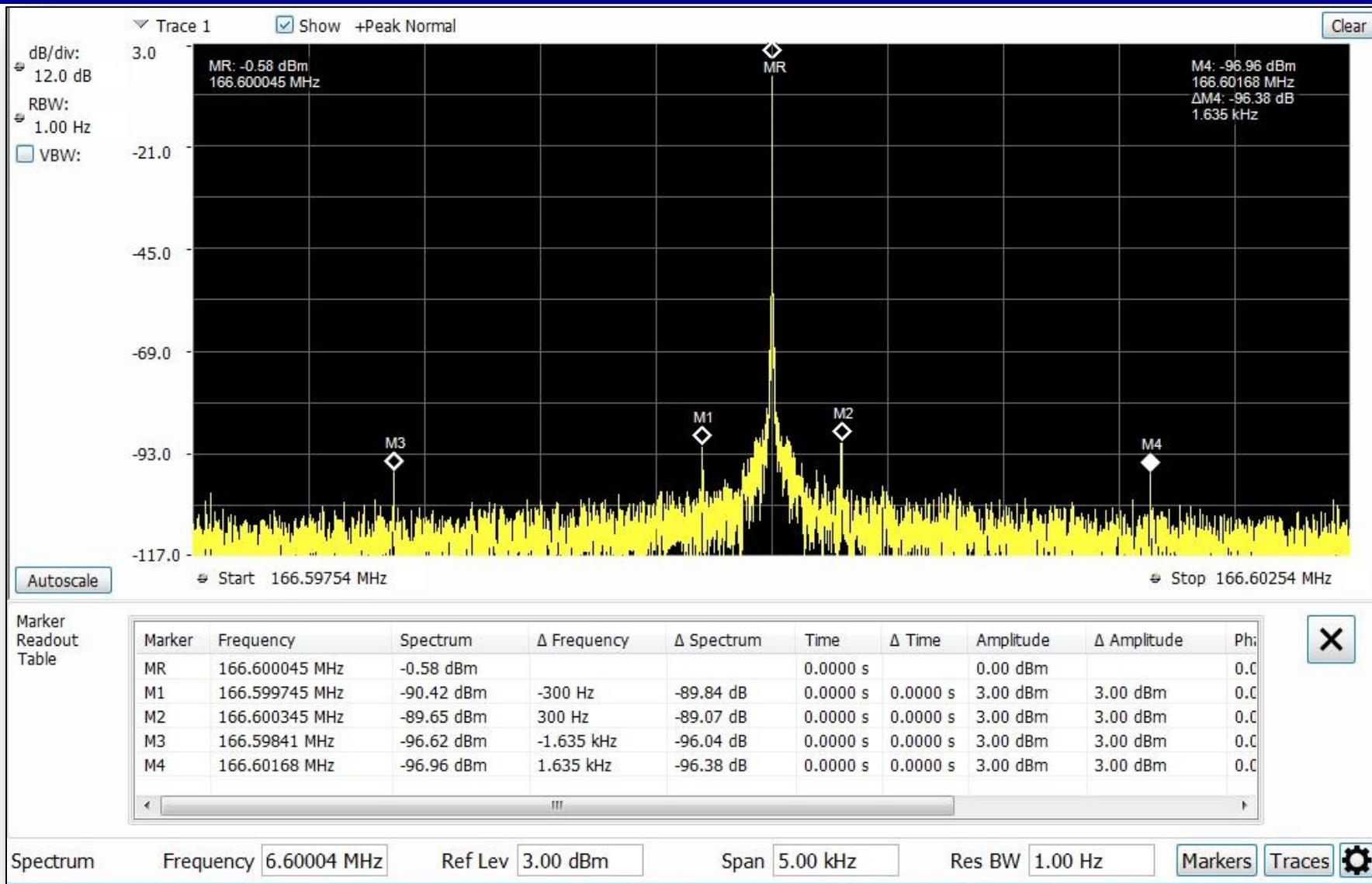
# Phase noise



Carrier offset	Phase noise
10 Hz	-73.8 dBc/Hz
100 Hz	-100.5 dBc/Hz
1 kHz	-114.2 dBc/Hz
10 kHz	-116.2 dBc/Hz
100 kHz	-122.4 dBc/Hz



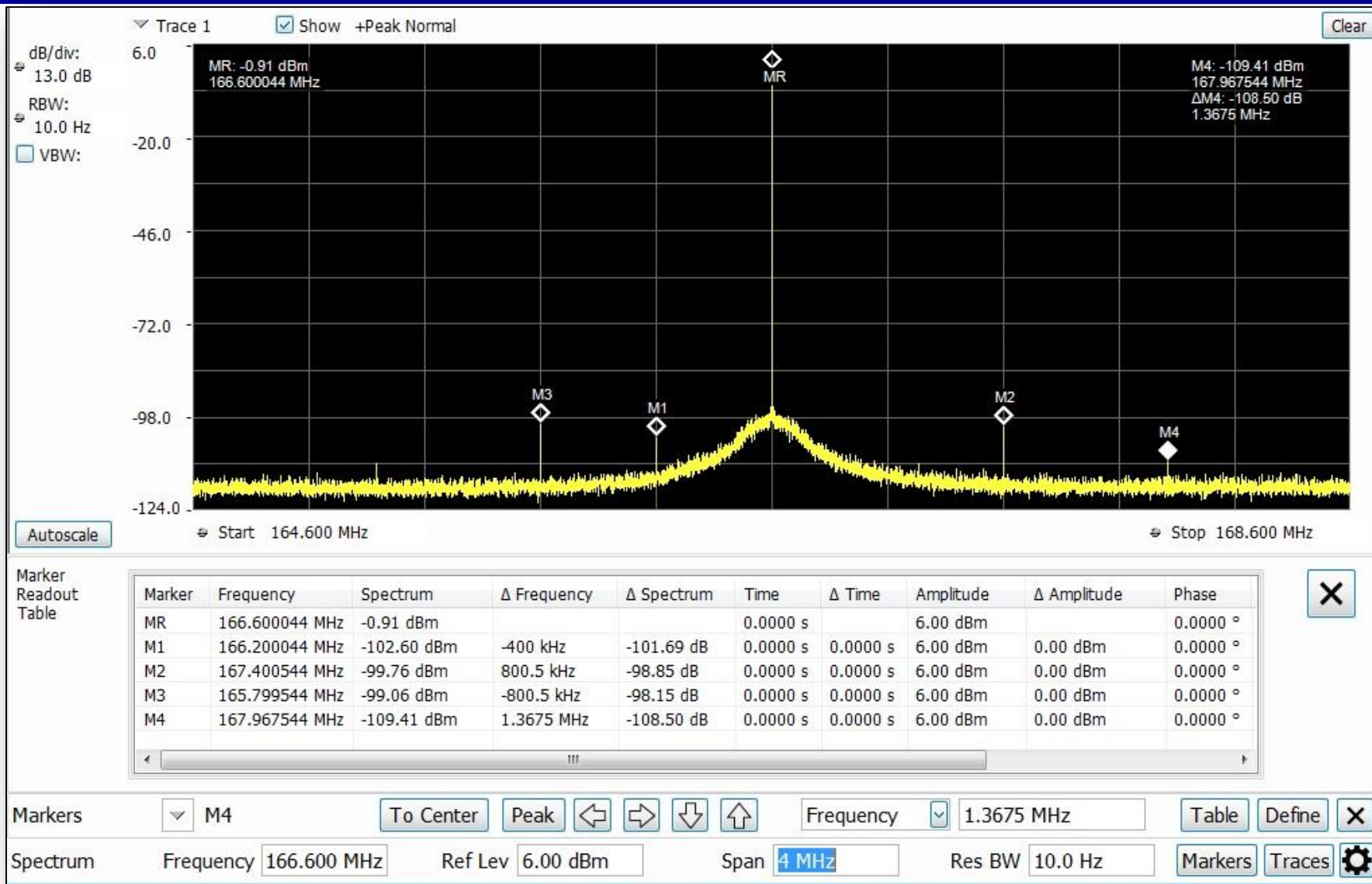
# Spurious suppression





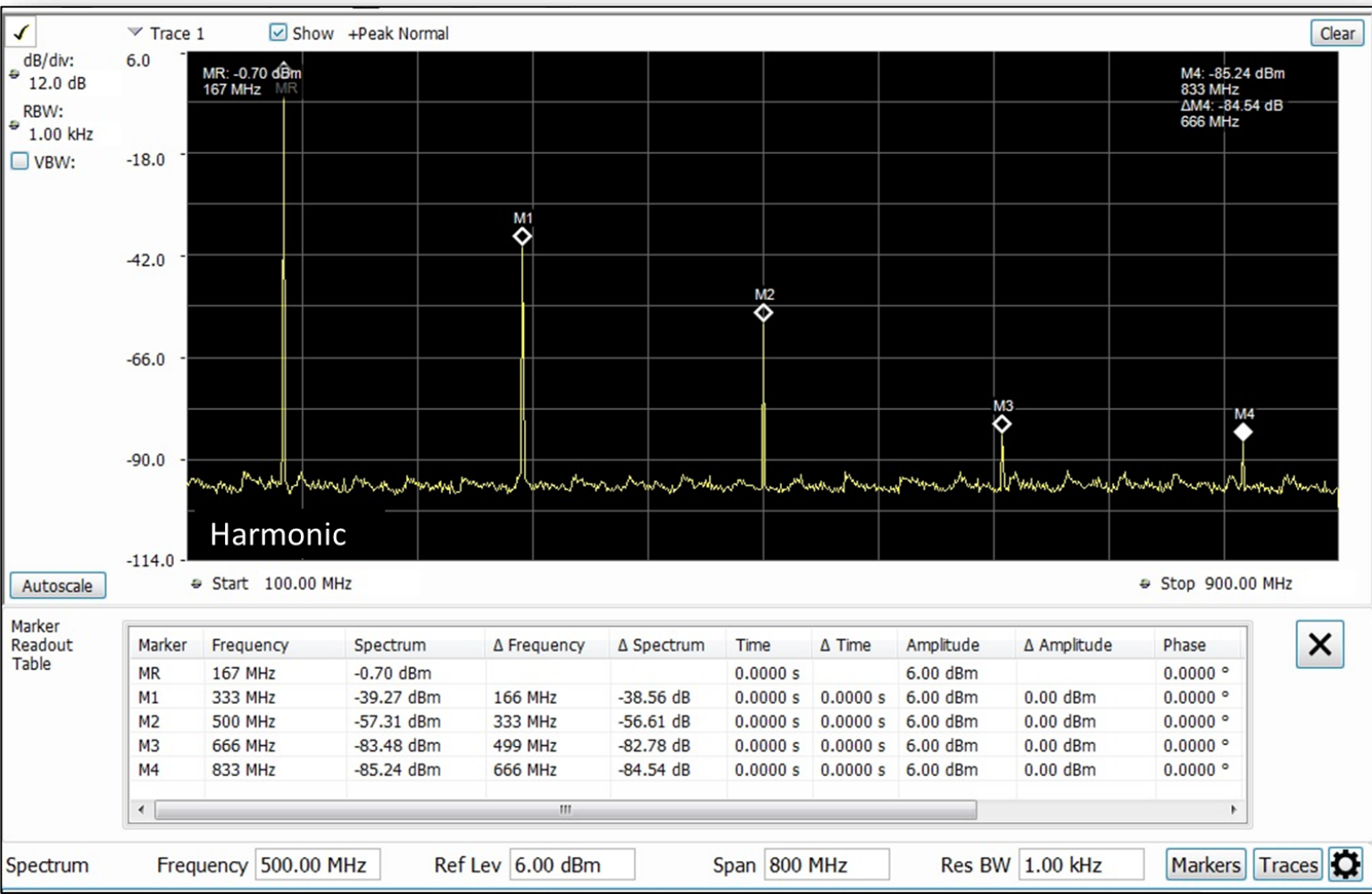


# Spurious suppression

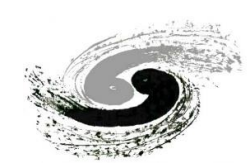




# Harmonic suppression



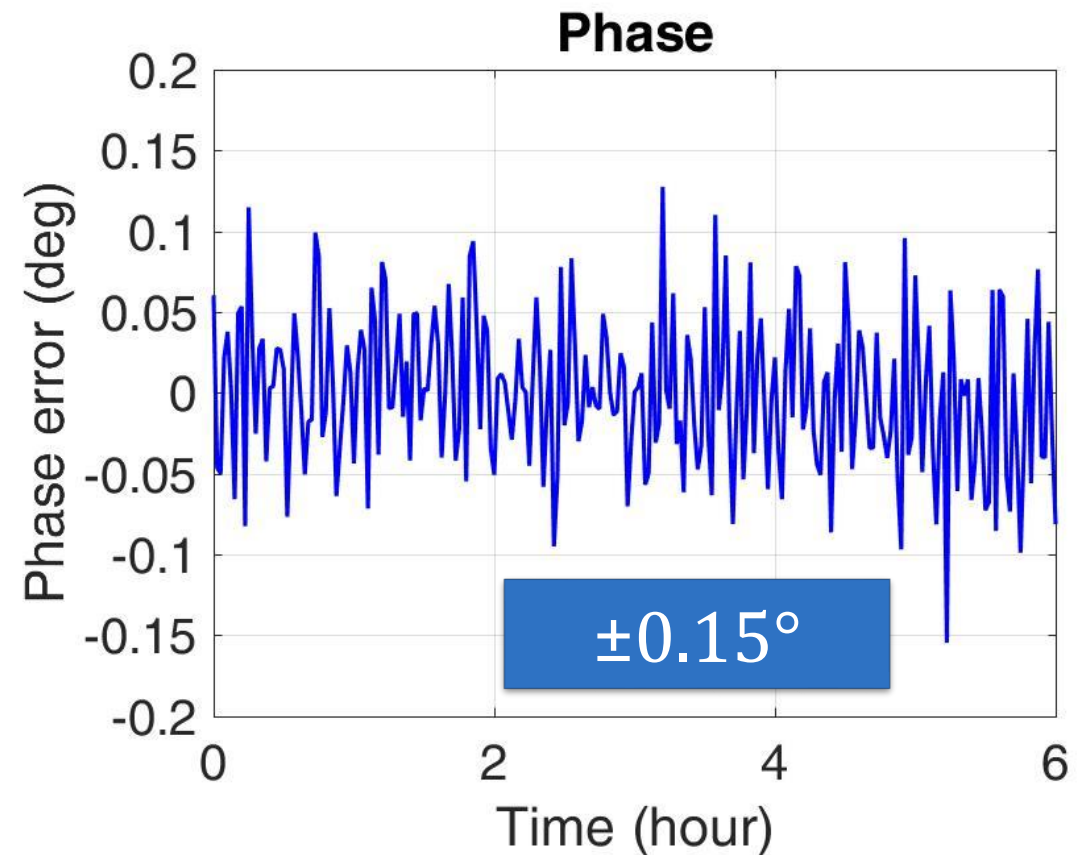
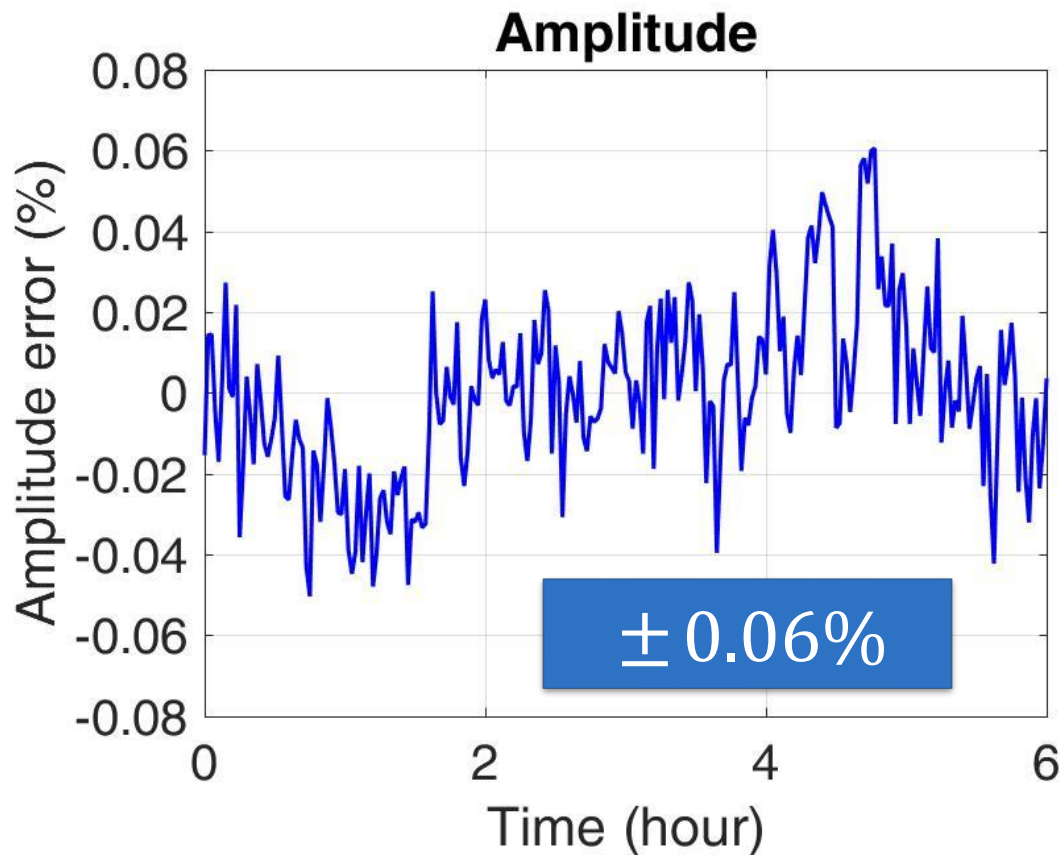
Harmonic $f_0$	Δamplitude
333 MHz (2 <sup>nd</sup> )	-38.6 dB
500 MHz (3 <sup>rd</sup> )	-56.6 dB
666 MHz (4 <sup>th</sup> )	-82.8 dB
833 MHz (5 <sup>th</sup> )	-84.5 dB



# Amp & Phase stability

Output power: 50kW

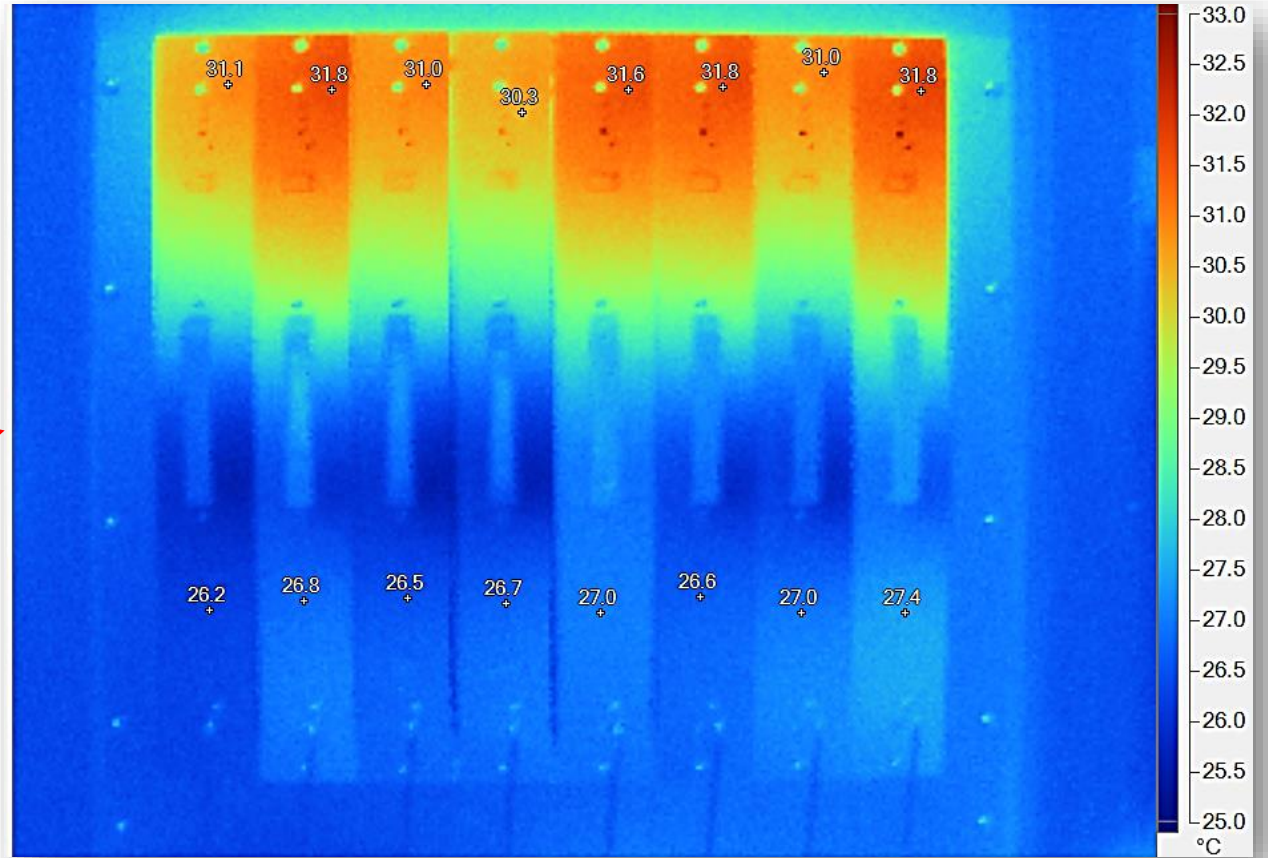
Water inlet temperature stability:  $\pm 0.4^{\circ}\text{C}$





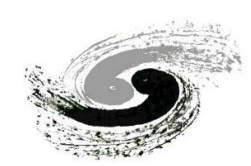


# Temperature distribution



Output power: 50kW keep for 1 hour  
Termination: matched load





# Redundancy

Cabinet 2

Cabinet 1

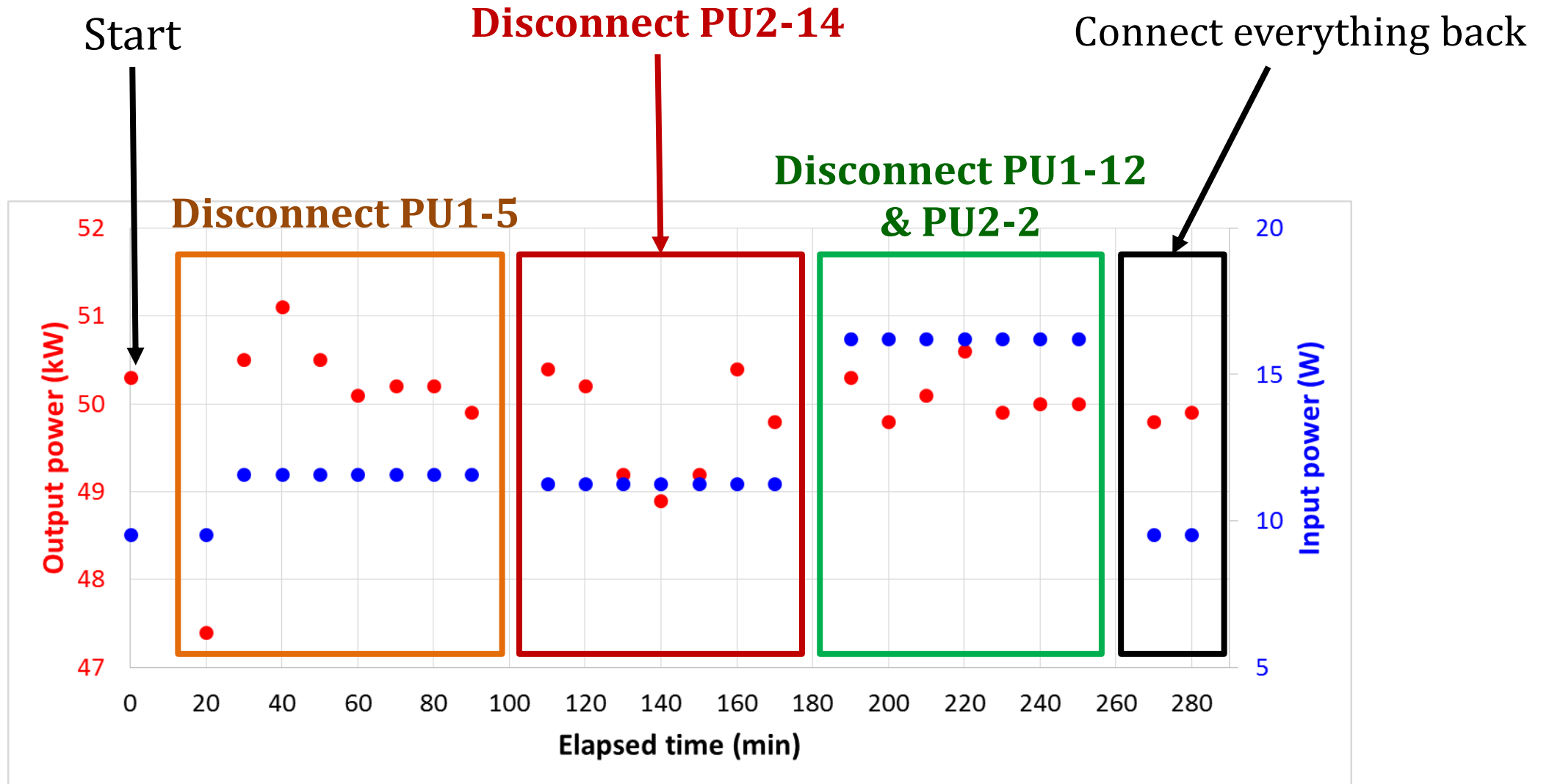


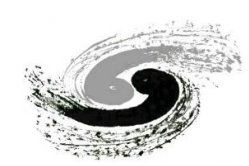
- Disconnect PU1-5
- Disconnect PU2-14
- ● Disconnect PU1-12 & PU2-2

- Redundancy requirement: >6%
- One 2kW power unit allowed to fail per cabinet to maintain 50kW output



# Redundancy



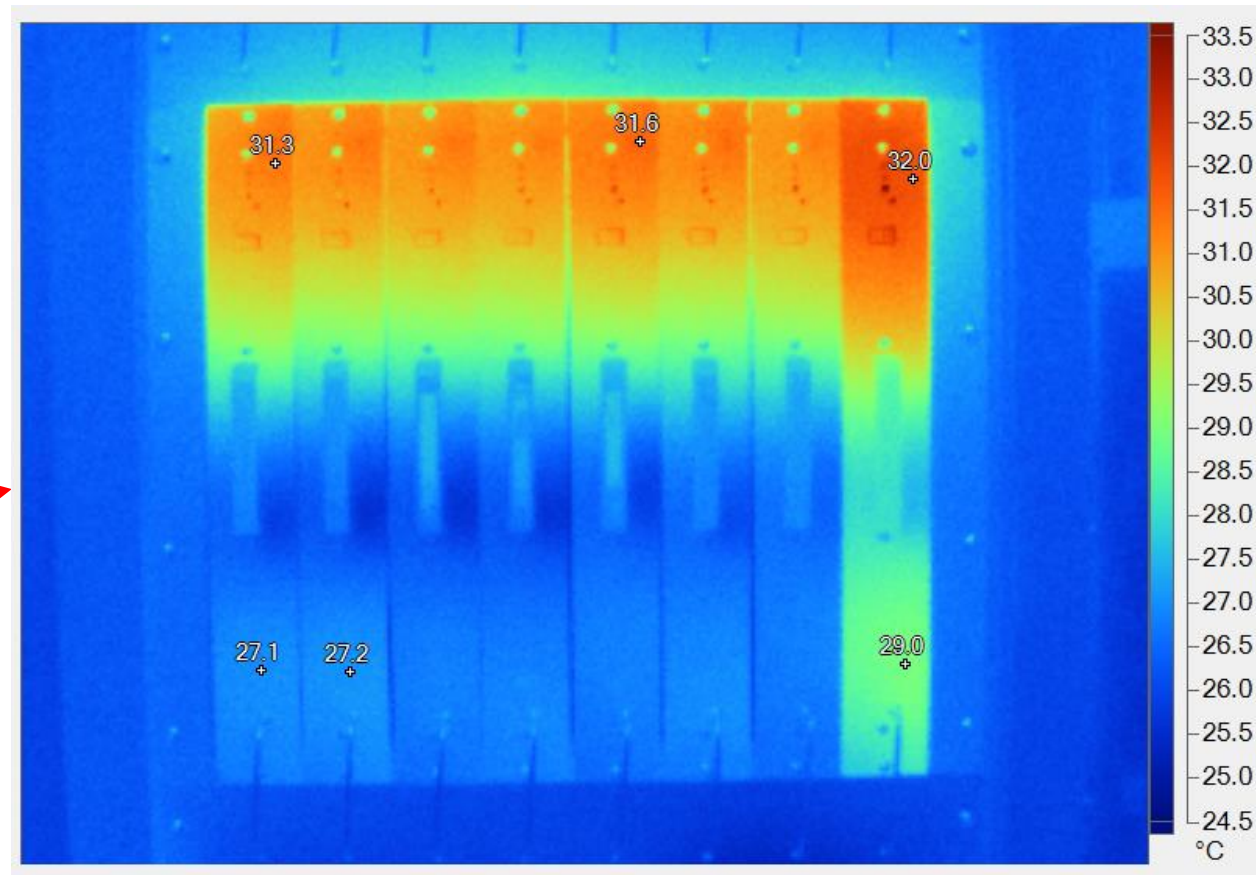


# Control

- **Monitoring**
  - **2kW power unit:** temperature, voltage, current, output power
  - **Power supply**
  - **Pre-amplifier:** temperature, voltage, current, input/output power
  - **50kW SSPA:** water flow, forward/reflected power, etc.
- **Interlock**
  - Temperature, water flow, power supply (2kW RF power unit)
  - Input overdrive, output overload
  - Other external signal (4 channels), etc.

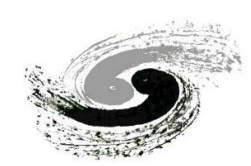


# Abnormal temperature



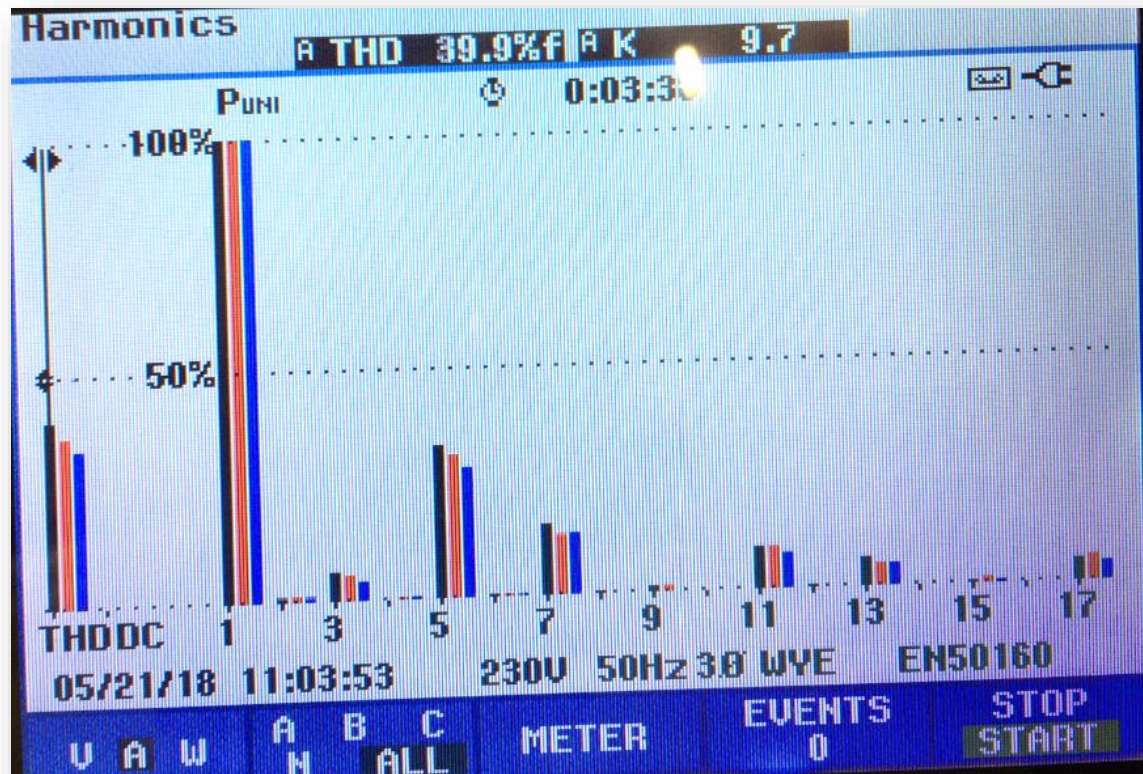
Output power: 50kW keep for 1 hour  
Termination: matched load





# Current harmonics

- Substantial 5<sup>th</sup> order current harmonic observed
- Need to be reduced



P<sub>UNI</sub> 0:05:34

Amp	A	B	C	N
H2% <i>f</i>	2.0	2.3	1.8	3.2
Amp	A	B	C	N
H3% <i>f</i>	5.9	4.8	3.9	55.6
Amp	A	B	C	N
H4% <i>f</i>	0.7	0.6	0.2	2.3
Amp	A	B	C	N
H5% <i>f</i>	32.9	30.6	28.2	34.9

05/21/18 11:05:50 230V 50Hz 3Ø WYE EN50160

UP DOWN HARMONIC GRAPH TREND EVENTS STOP START

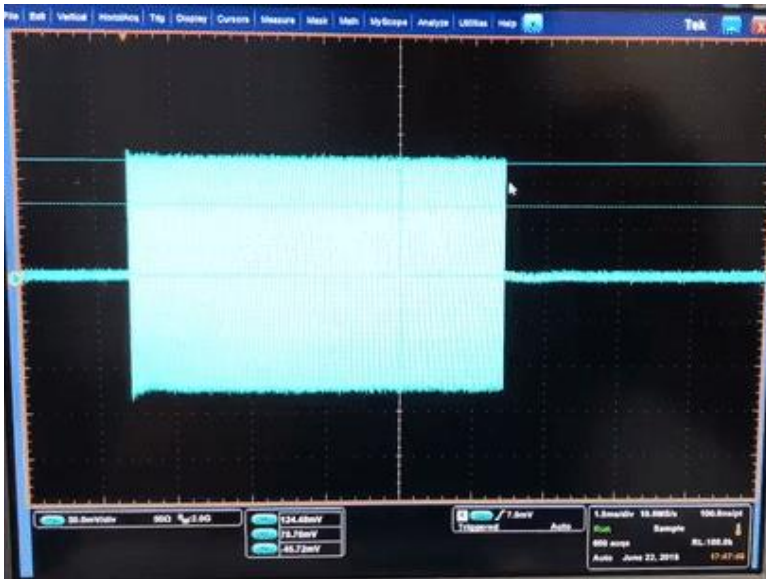
0



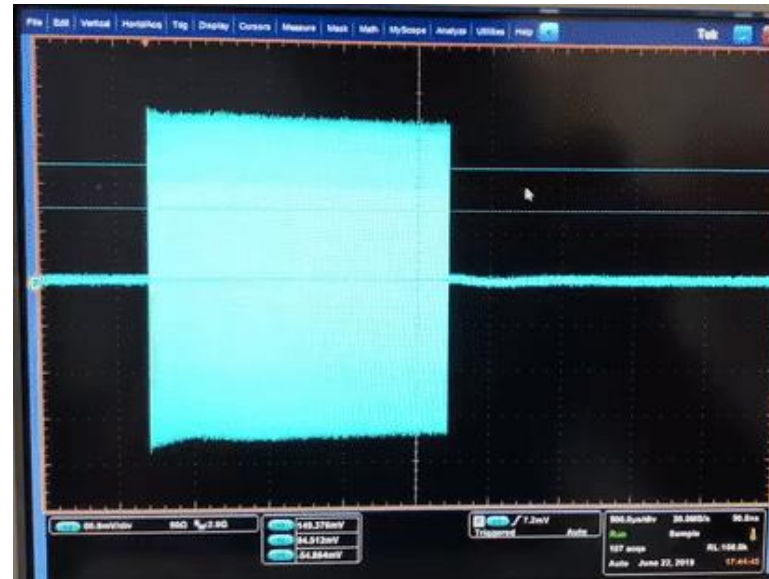


# Long pulse

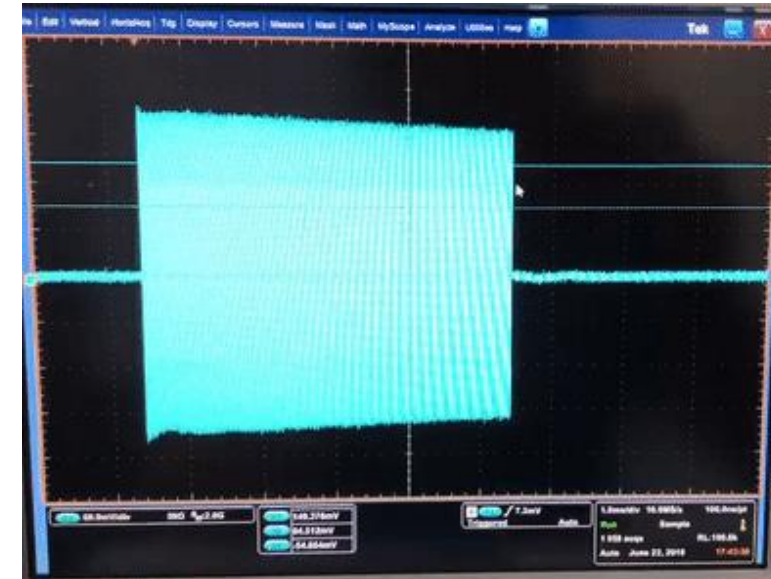
Pulse length = 1ms



Pulse length = 2ms



Pulse length = 5ms



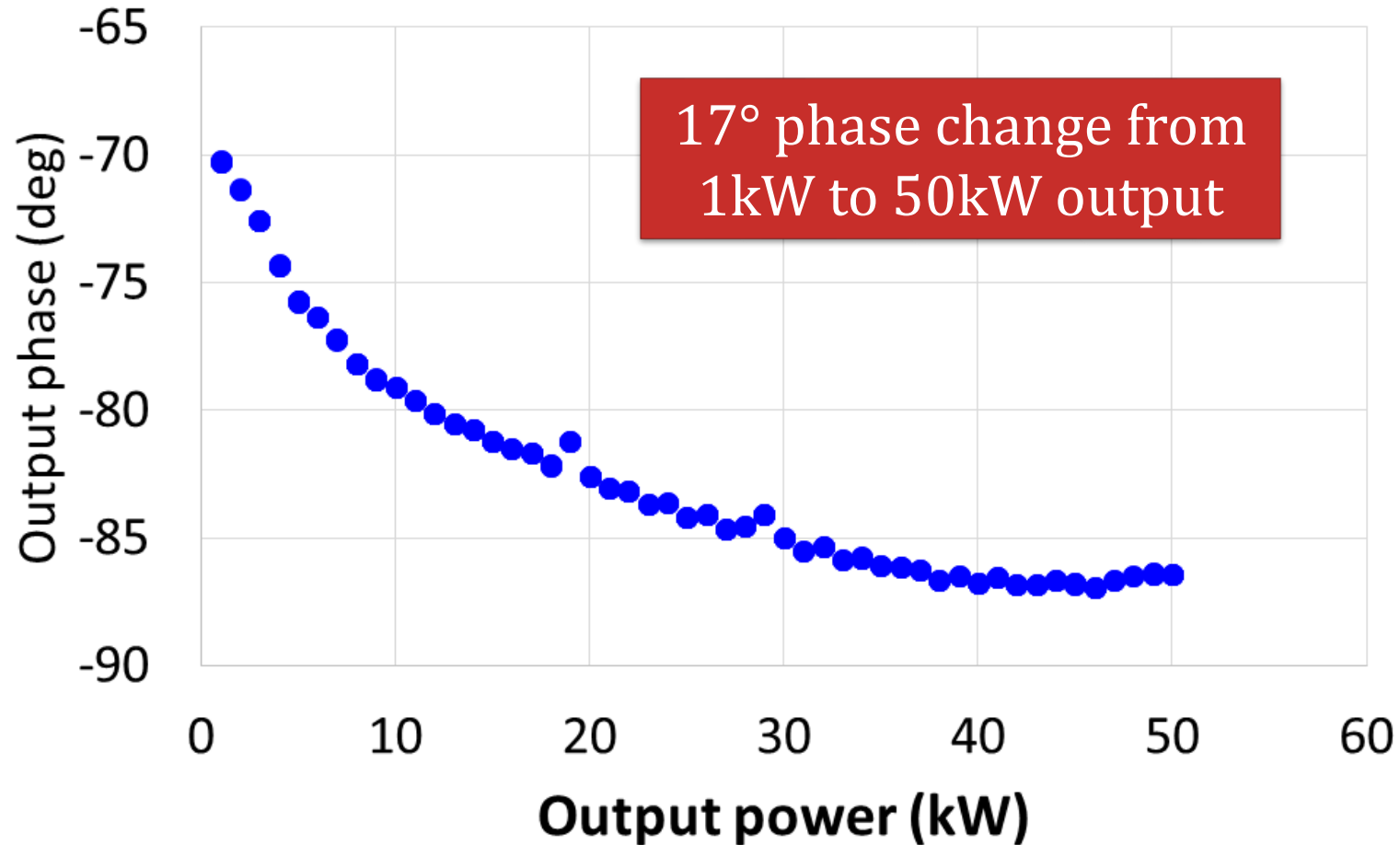
Rep rate: 10Hz

The capacity of energy storage in the power supply is not sufficient.



# Phase distortion

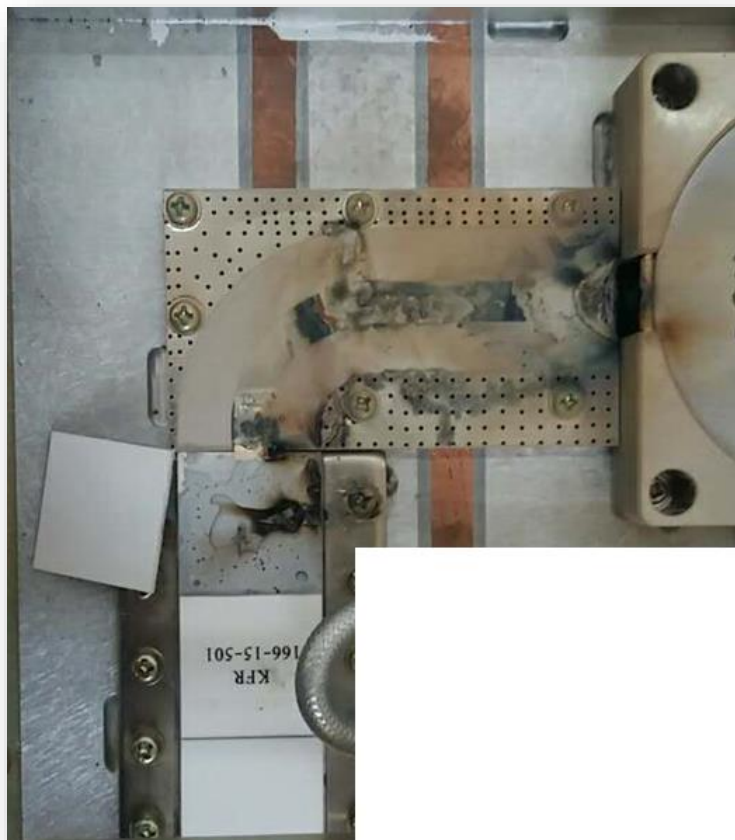
## Phase distortion with power



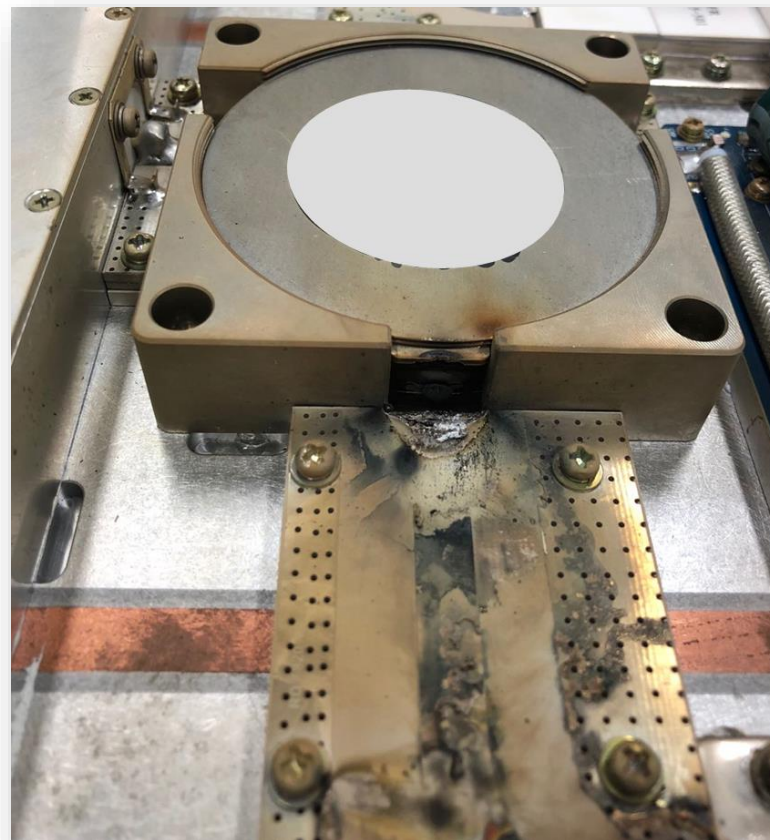


# Something burnt

Load



Circulator

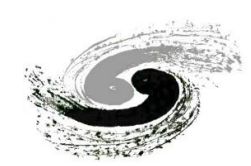


Transistor

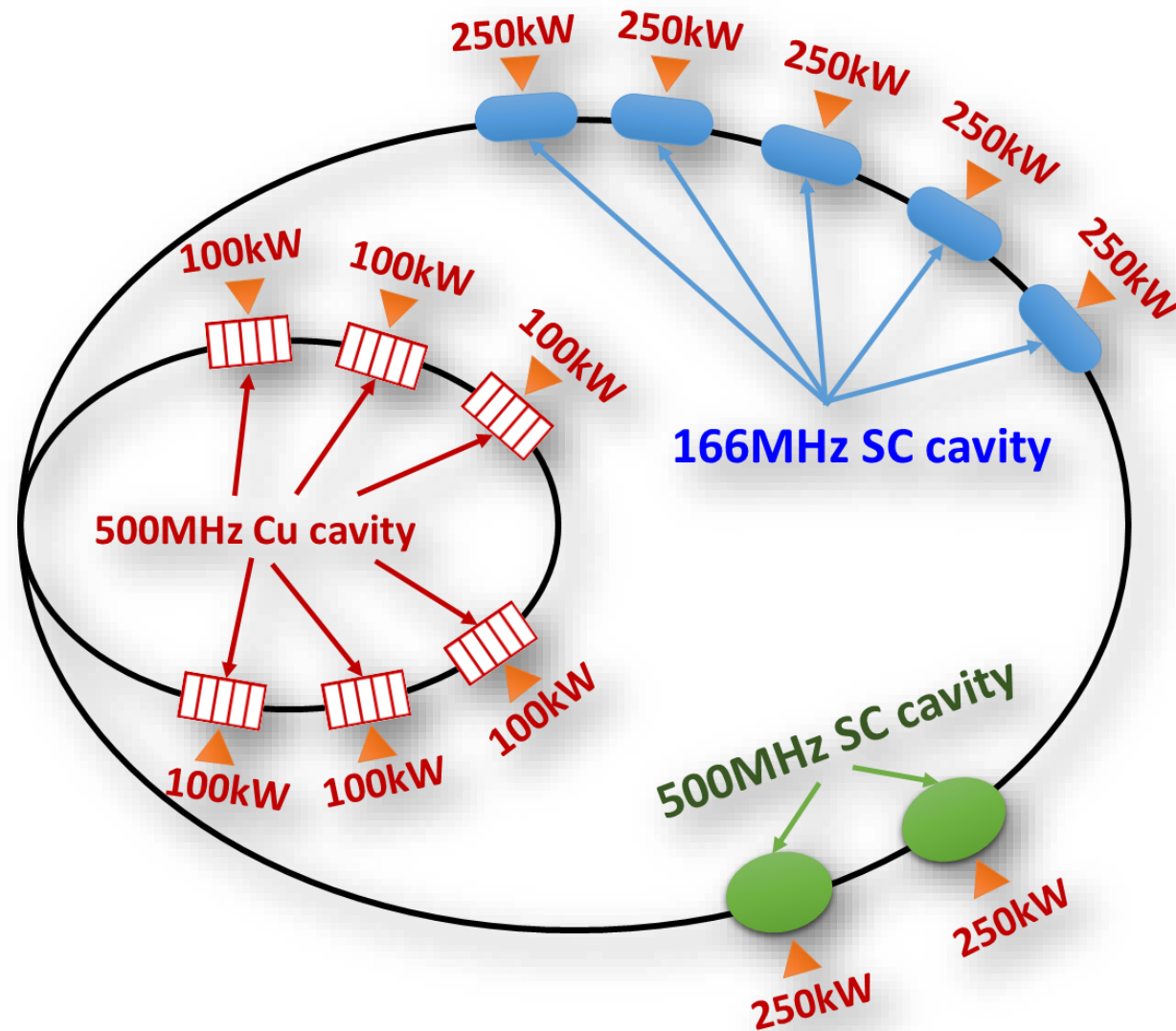


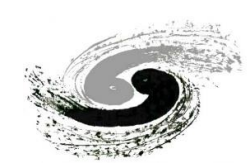


# 500MHz 100kW SSPA



# HEPS RF system





# The prototype



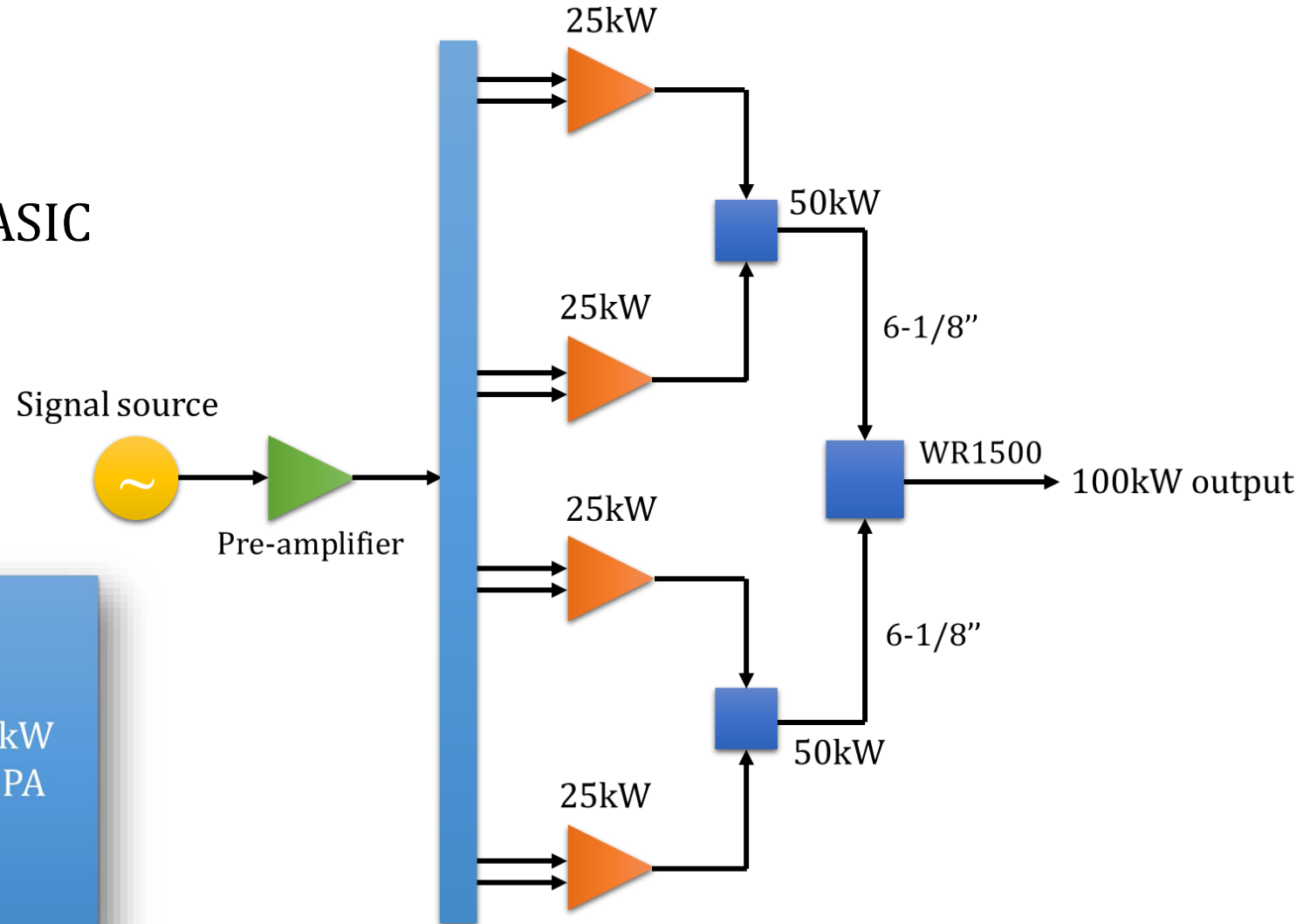
中国航天科工集团第二研究院二十三所

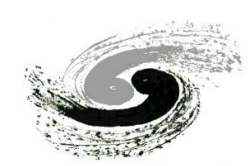
05.2018 Call for tender

06.2018 Contract awarded to CASIC

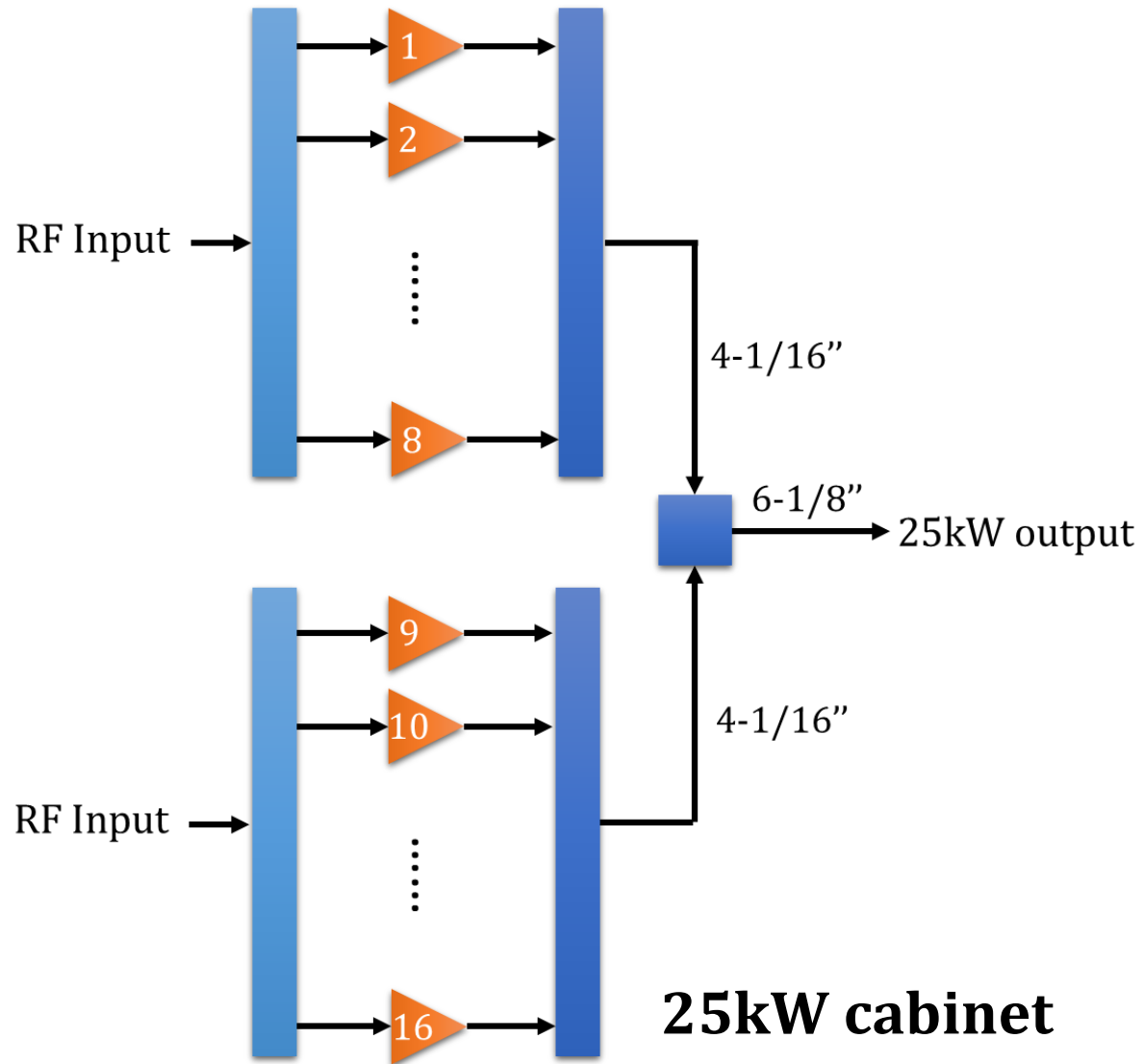
07.2019 Deliver to IHEP

- 2-stage coaxial combiner
- Final stage waveguide combiner

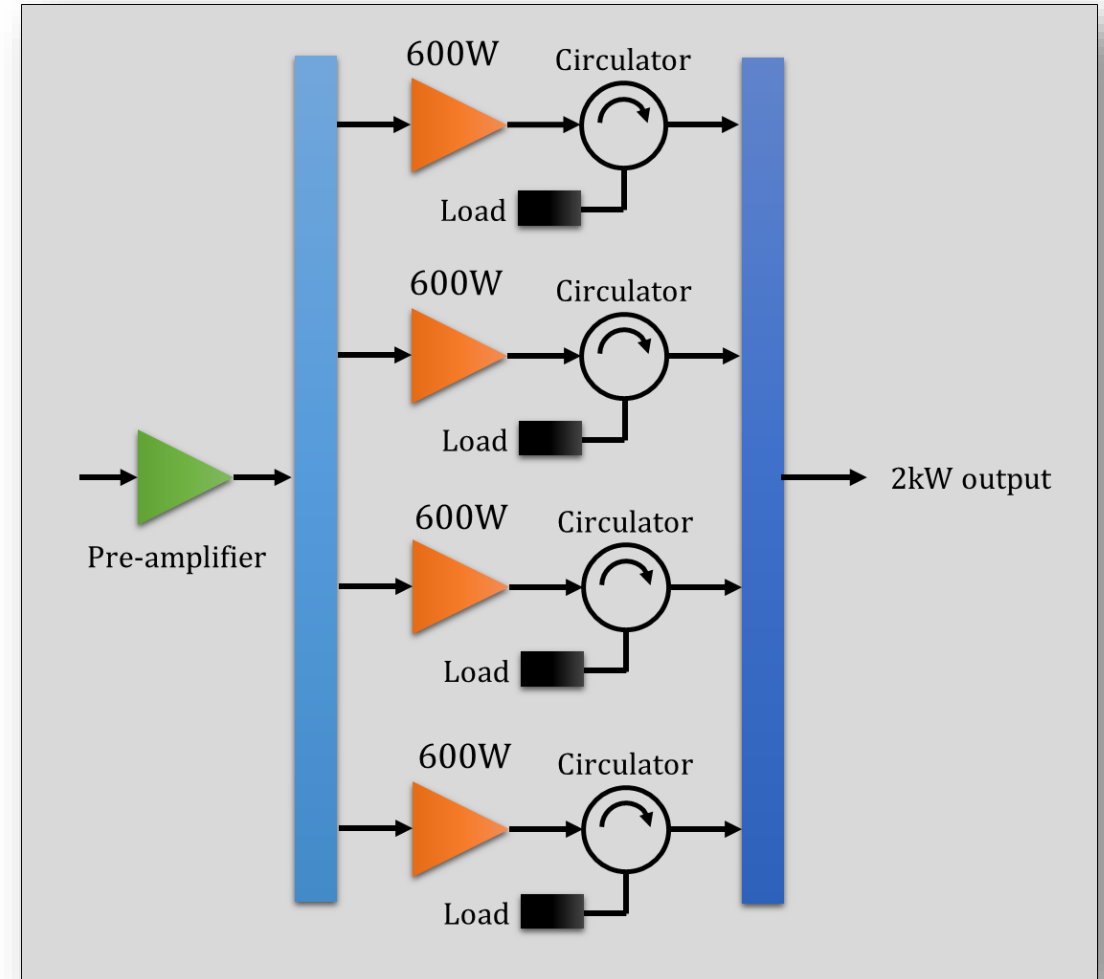




# The design



## 2kW power unit







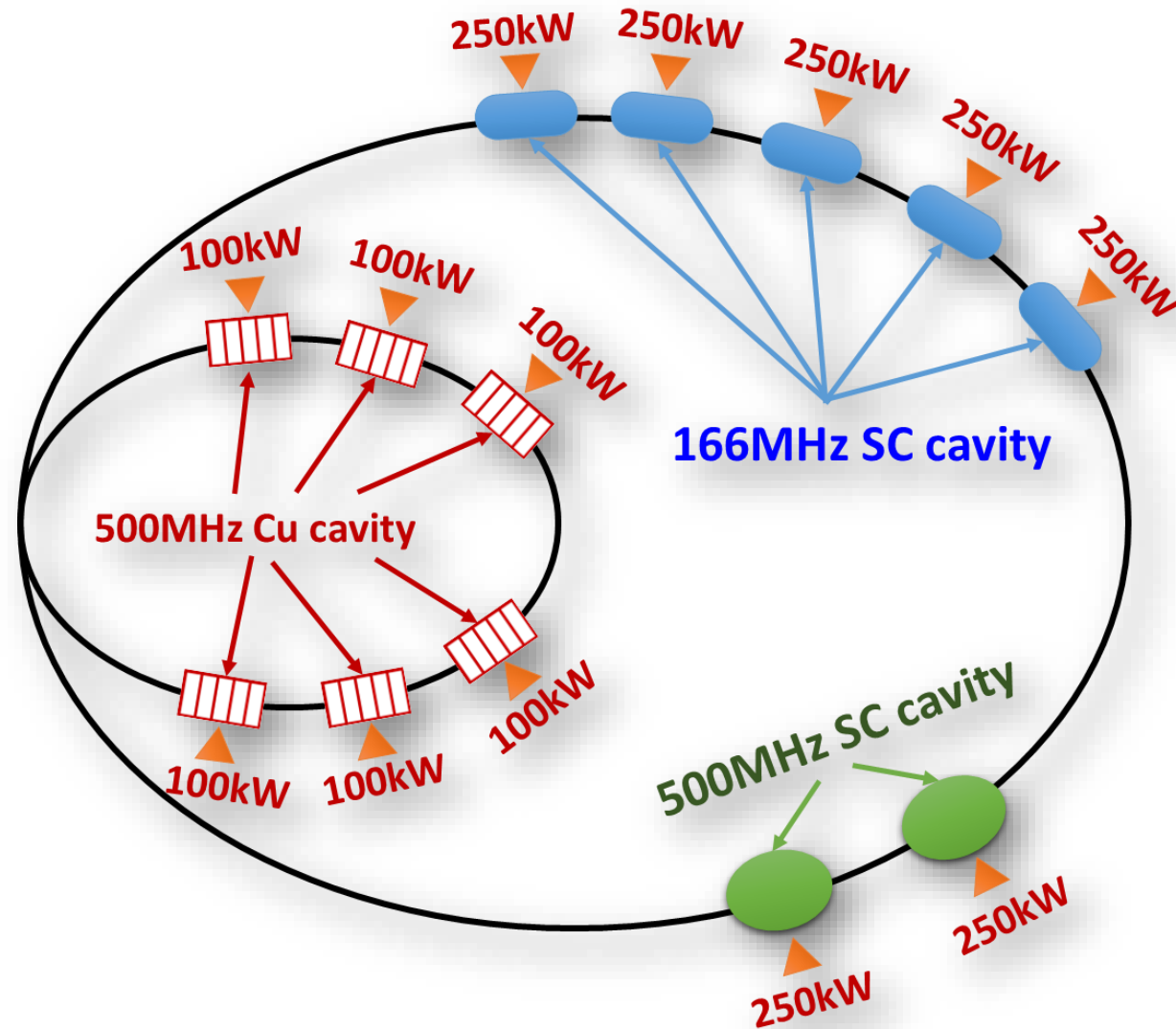
# Design requirements

No.	Parameter	Target value
1	RF frequency	500MHz
2	Bandwidth	$\pm 2$ MHz (50kW output)
3	Mode	CW/Pulse
4	Nominal output power	100kW
5	Power output at 1dB compression	100kW
6	Amplitude & phase stability	$\pm 1\%$ and $\pm 1^\circ$ @100kW
7	Phase noise (1kHz carrier offset)	$\leq -70$ dBc
8	Harmonic suppression	$\leq -30$ dBc
9	Spurious suppression (carrier offset > 10kHz)	$\leq -70$ dBc
10	Overall efficiency	$\geq 50\%$
11	Redundancy	>6%

# Power coupler



# HEPS RF system



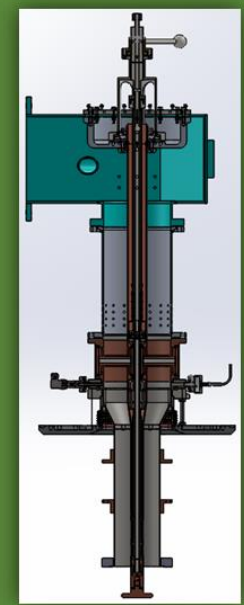


# FPCs developed at IHEP

BEPCII 500MHz



CEPC 650MHz



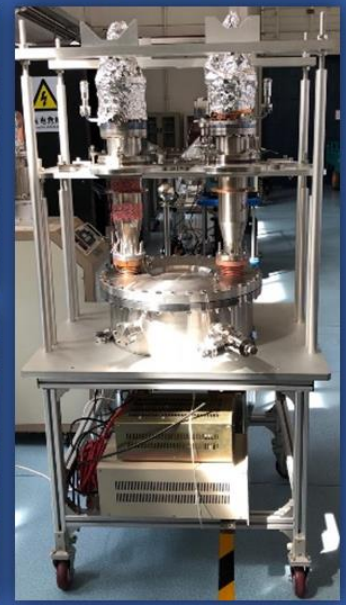
CADS 650MHz



CADS 325MHz (Spoke, RFQ)

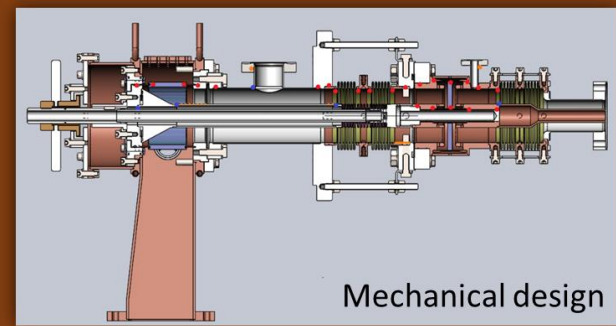


CADS 162.5MHz



The RF group has over a decade of experience on design, fabrication and power testing of FPCs and their beam operations.

ERL 1.3GHz FPC (double-window)



Coax, variable, capacitive, double-window

Test: CW 70 kW (TW & SW)  
Op: Not yet specified

Under production  
To be tested in 2018

Mechanical design

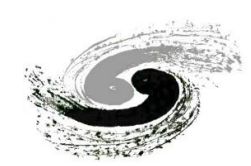
Doorknob





# 166.6MHz FPC for SCC

Parameter	Value
Frequency	166.6 MHz
RF power	200 kW CW
External Q	3.78E4
Impedance of the coaxial line	50 $\Omega$
Ceramic type	coaxial, planar
Cooling type	Window & inner conductor: water-cooled Outer conductor: helium gas cooled
Reflection coefficient	S11 < -20 dB Bandwidth: ~15 MHz
Vacuum	Leak rate: 1E-9 mbar·l/sec
Interface with power source	Coaxial line, 9-3/16"



# Fabrication

Window



T-box



Launched in 03.2017  
Completed in 06.2017

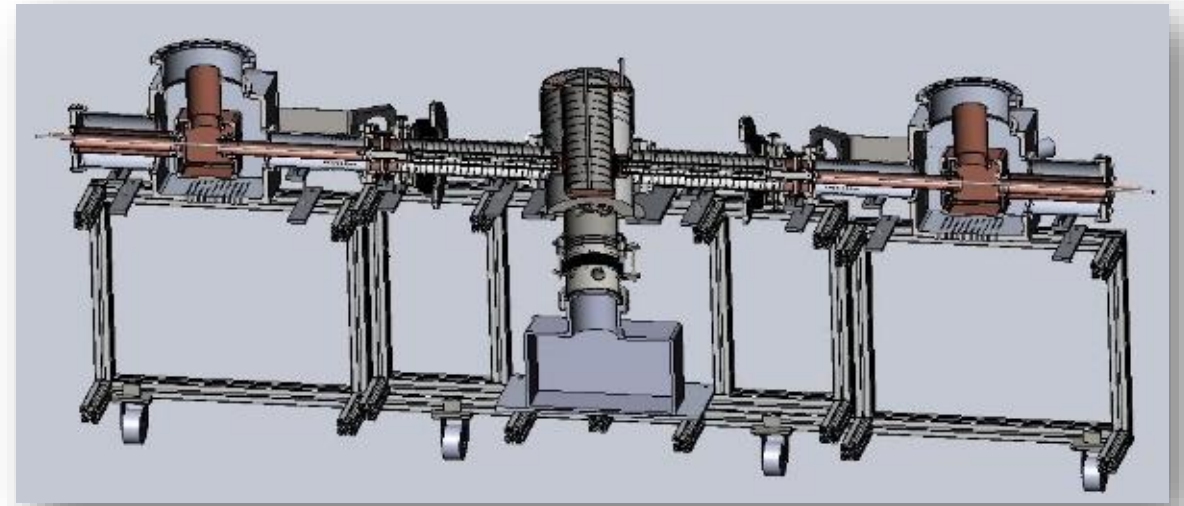
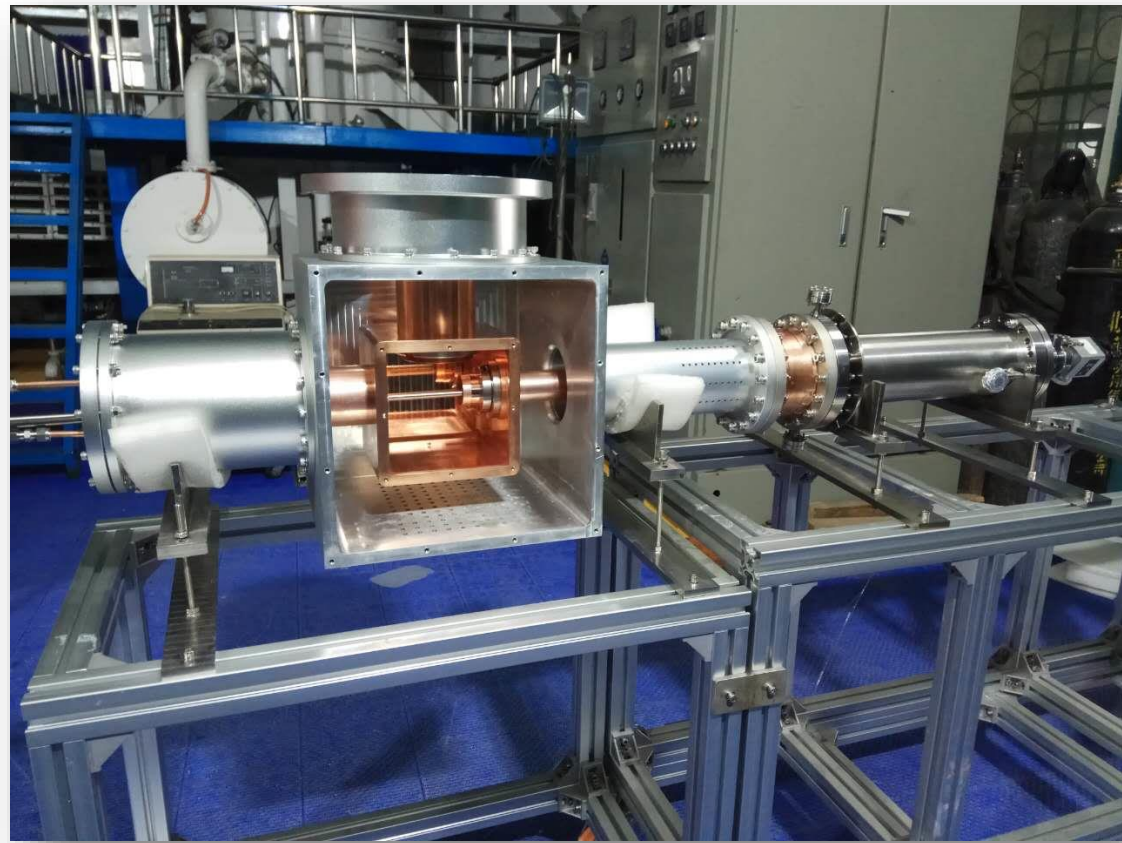
Outer conductor

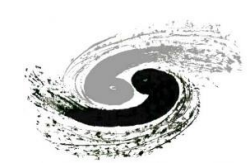






# The FPC





# High power conditioning

**SSPA: Solid-State Power Amplifier**

**TW: Travelling Wave**

**SW: Standing Wave**

	Power source	Conditioning mode	Remarks
Phase I	166.6MHz 50kW solid-state amplifier	TW & SW	Limited by available SSPA power
Phase II	650MHz 150kW solid-state amplifier	TW	Use a modified test stand





# The setup

166MHz SSPA

The complete setup for FPC high power conditioning using 166.6MHz 50kW SSPA

166.6MHz 50kW SSPA

6-1/8" coaxial line



Test bench system

Water load



LLRF control & monitoring



# Automatic conditioning system

Operation mode

Manual control

Control

Power & vacuum reading

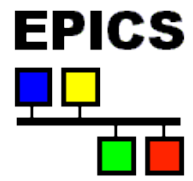
Interlock

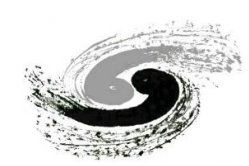
Parameter settings

The screenshot shows a control interface for an automatic conditioning system. It is divided into several functional areas:

- Control (top left):** Includes status indicators for Ready, Aging, and Exit, and control buttons for reset, on, and off.
- Power & vacuum reading (top middle):** Displays real-time data for UP CPLR and DOWN CPLR, including Vacuum (Pa), Inci. power (kW), and Refl. power (kW).
- Interlock (middle left):** Shows interlock status for various parameters like ARC, Vacuum, Temp1-4, SSA-Flow, and CPLR-Flow.
- Parameter settings (bottom left):** Allows setting Power target, Vacuum, and Time delay parameters.
- Manual control (top right):** Features a pulse set control with Pulse and Pulse\_width settings.
- Archive (middle right):** Includes manual set controls for LLRF-ON and Amp\_set, and archive options for LLRF and SSA.
- Sequence state (bottom right):** Shows state status, addpower, interlock, and loop status indicators.
- Graphs (right side):** Two line graphs showing power and vacuum over time. The top graph shows power (incl. power, refl. power) and vacuum (up cplr vacuum, down cplr vacuum). The bottom graph shows temperature (temp1-temp16) and power (incl. power).

Archive Sequence state



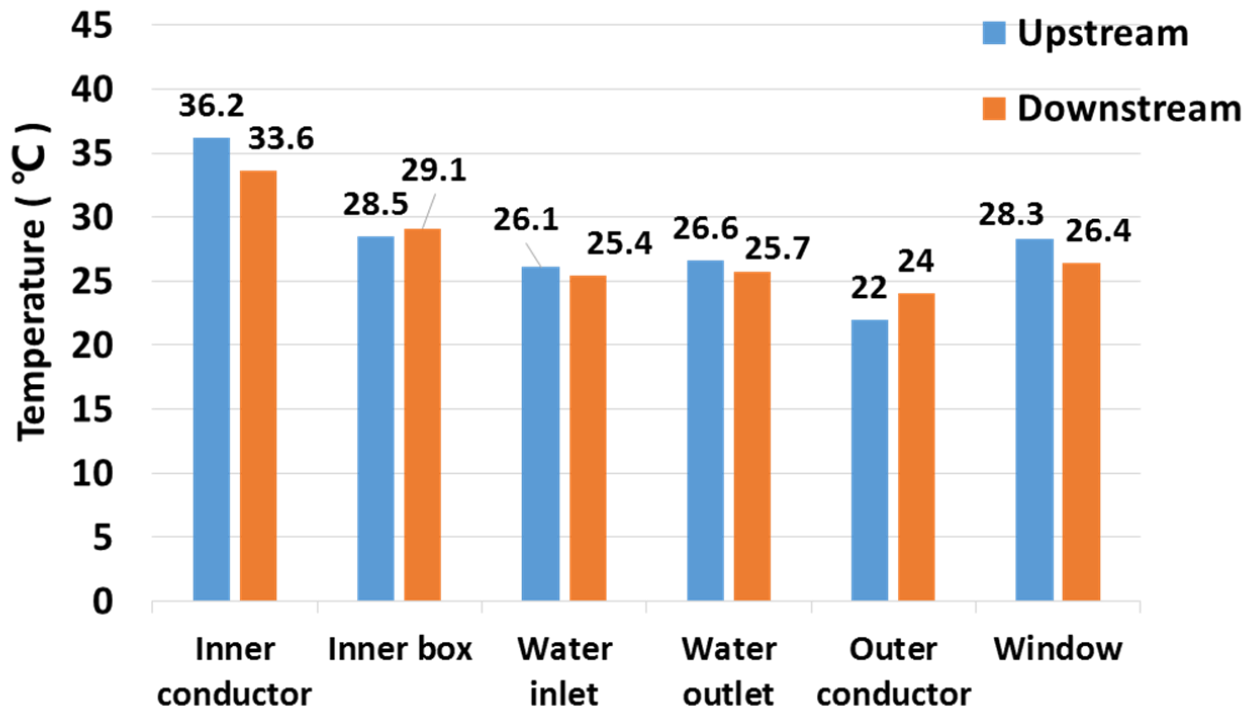


# Conditioning in TW mode

166MHz SSPA

- Maximum power reached: 50kW CW
- After 1-hour power keep at 50kW CW: normal vacuum & temperature readouts

FPC temperature after 1-hour power keep at 50kW CW in TW mode



- **Conditioning method**

- Pulsed mode (20 hours)
- CW mode (10 hours)
- Alternating pulsing and CW

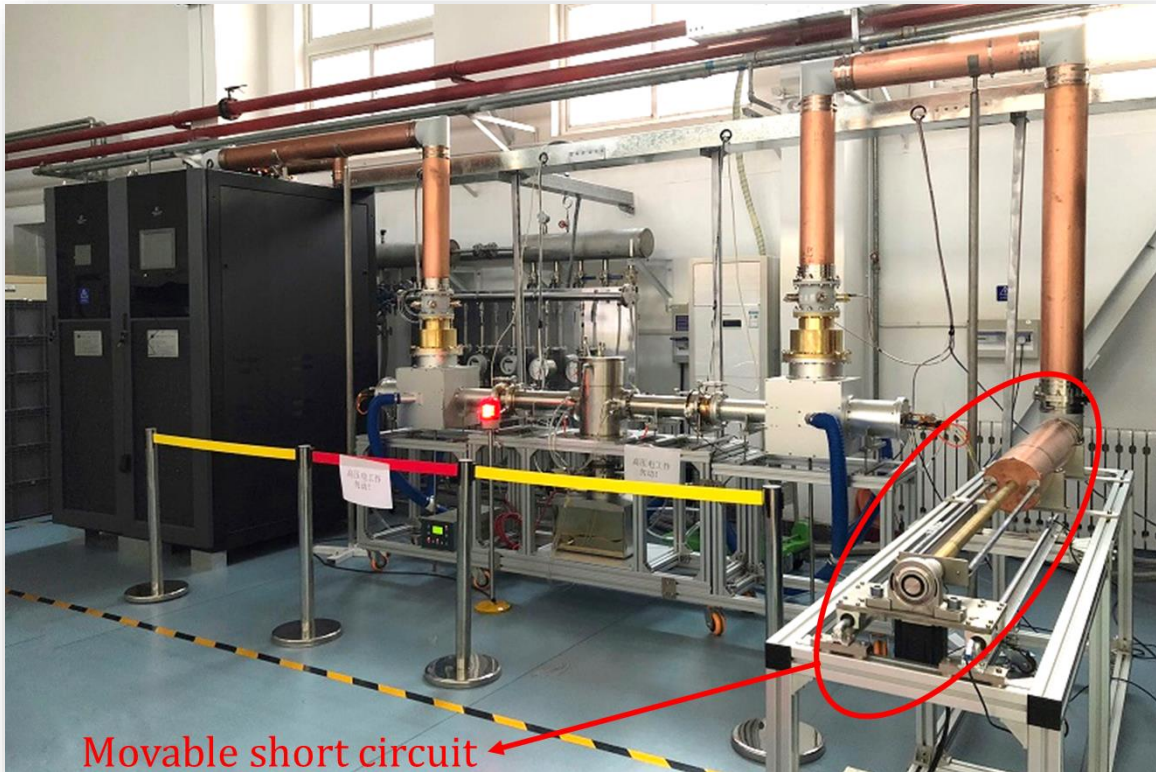




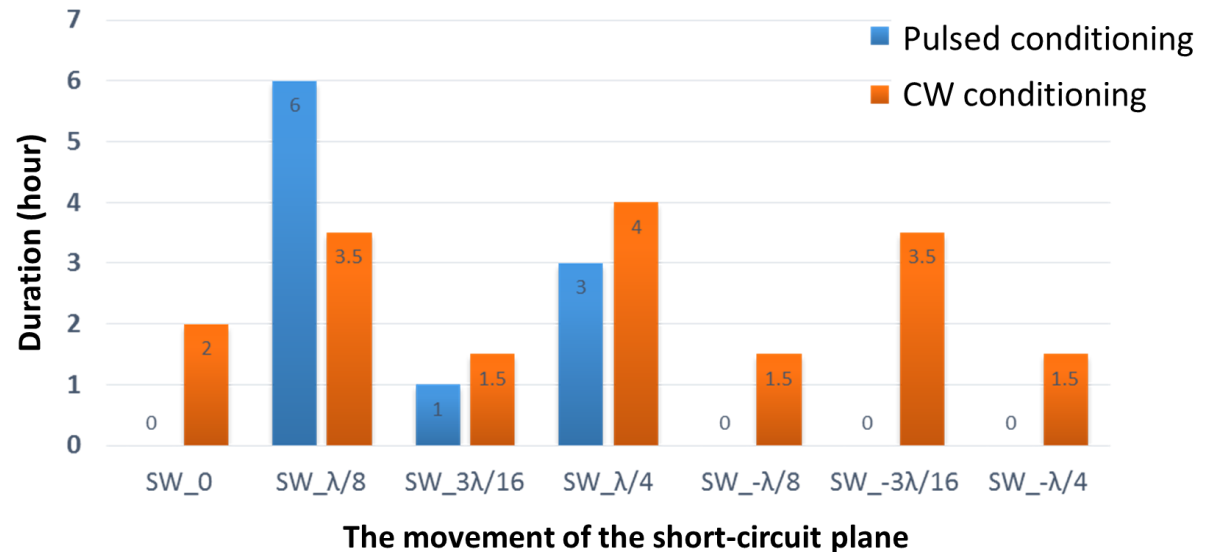
# Conditioning in SW mode

166MHz SSPA

- The short-circuit plane was moved by  $\lambda/8$  ( $\sim 225$  mm) each time
- Maximum power reached: 50kW CW
- Conditioning method: pulsed mode and CW mode



Conditioning time in SW mode





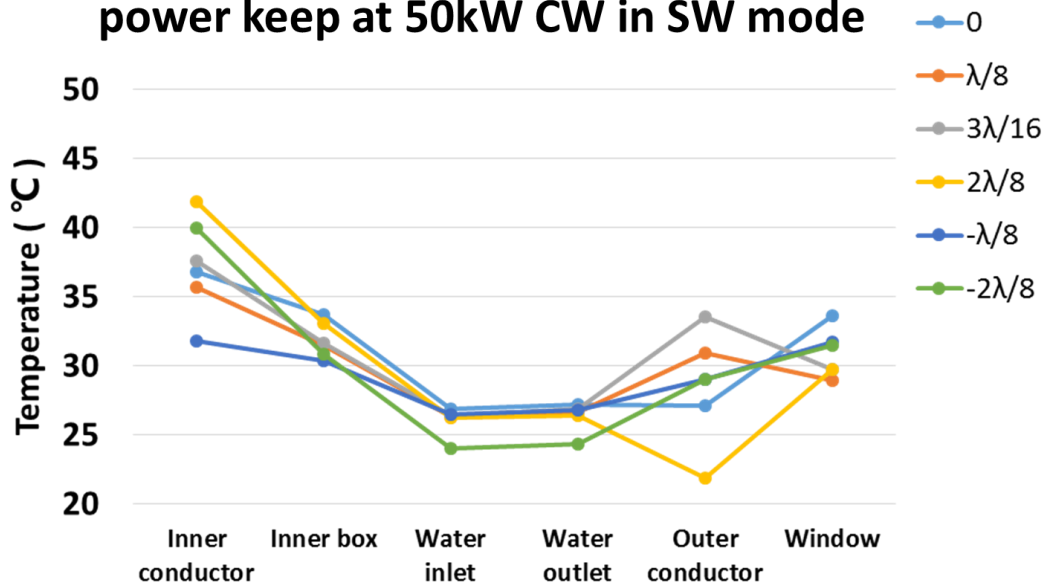


# Conditioning in SW mode

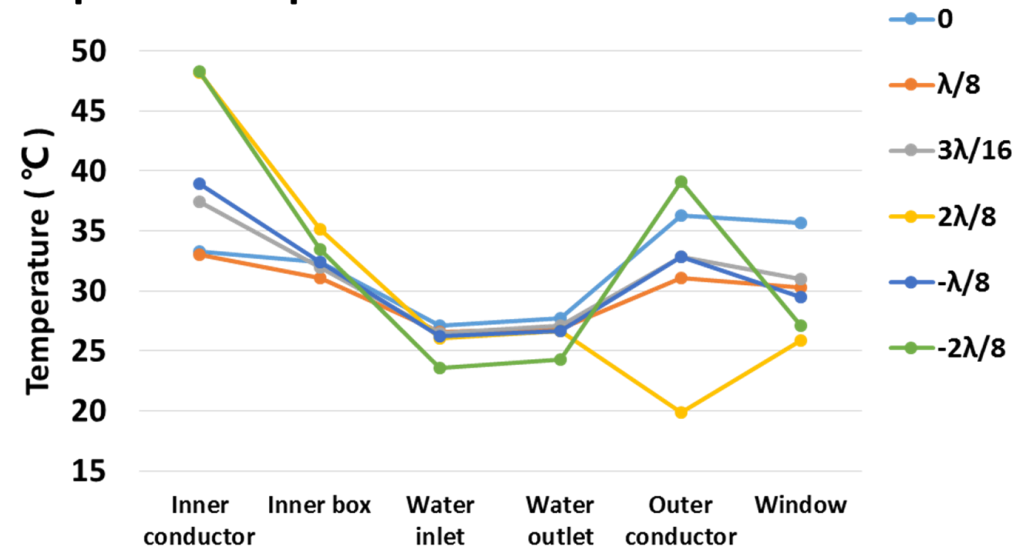
166MHz SSPA

- The electric antinode was moved along the FPC
- After 1-hour power keep at 50kW CW ( $E_{\max}$  on the ceramic window)
  - The temperature rise  $<4^{\circ}\text{C}$  ( $6^{\circ}\text{C}$ ) on the upstream (downstream) window
  - Vacuum and temperature was normal during power keep

Upstream FPC temperature after 1-hour power keep at 50kW CW in SW mode



Upstream FPC temperature after 1-hour power keep at 50kW CW in SW mode

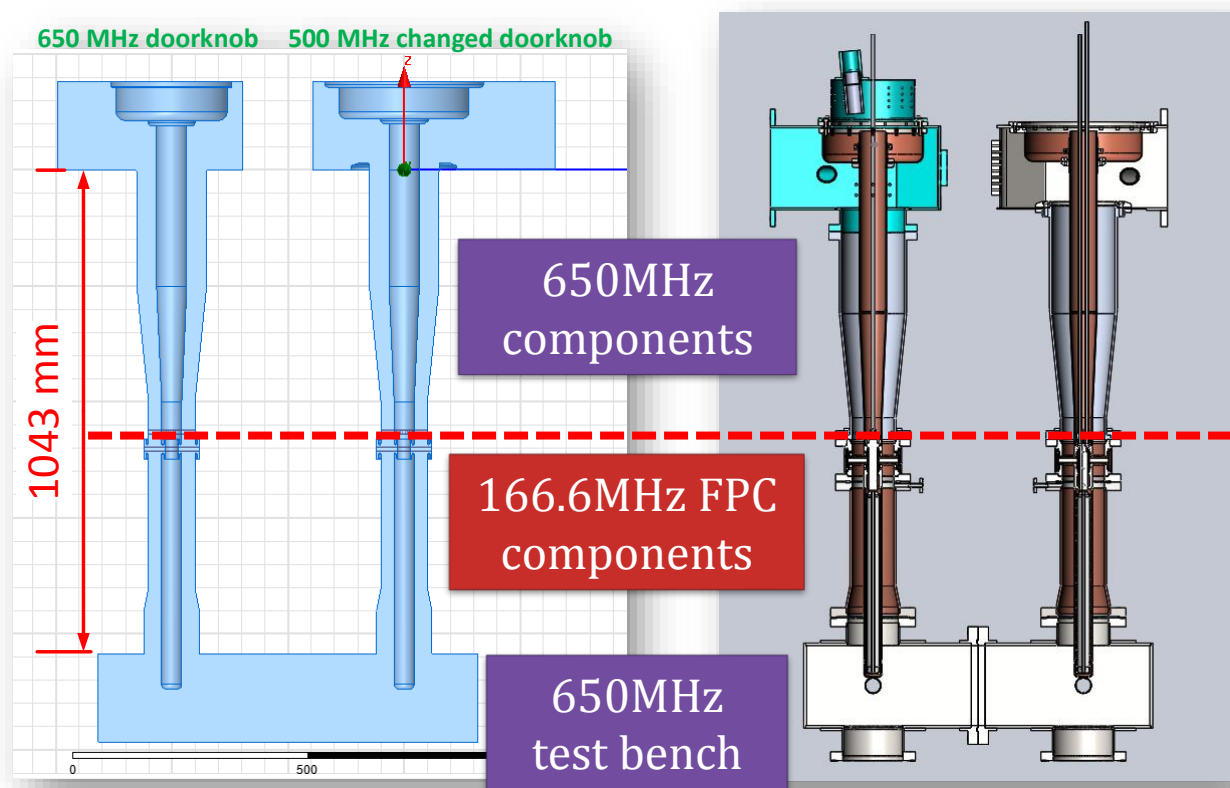




# HPC by 650MHz SSPA

650MHz SSPA

To examine high power handling capability of the window, power conditioning at 150kW CW was implemented by using the existing 650MHz 150kW SSPA with a hybrid test bench setup.



The RF optimization

The mechanical drawing

- The test bench system consists of
  - Two window inner-conductor assemblies of the 166.6MHz FPC
  - One doorknob from existing 650MHz FPC
  - One modified doorknob from existing 500MHz FPC (scaled to 650MHz)
  - One WG-box used for BEPCII 500MHz FPC conditioning



# The setup

650MHz SSPA



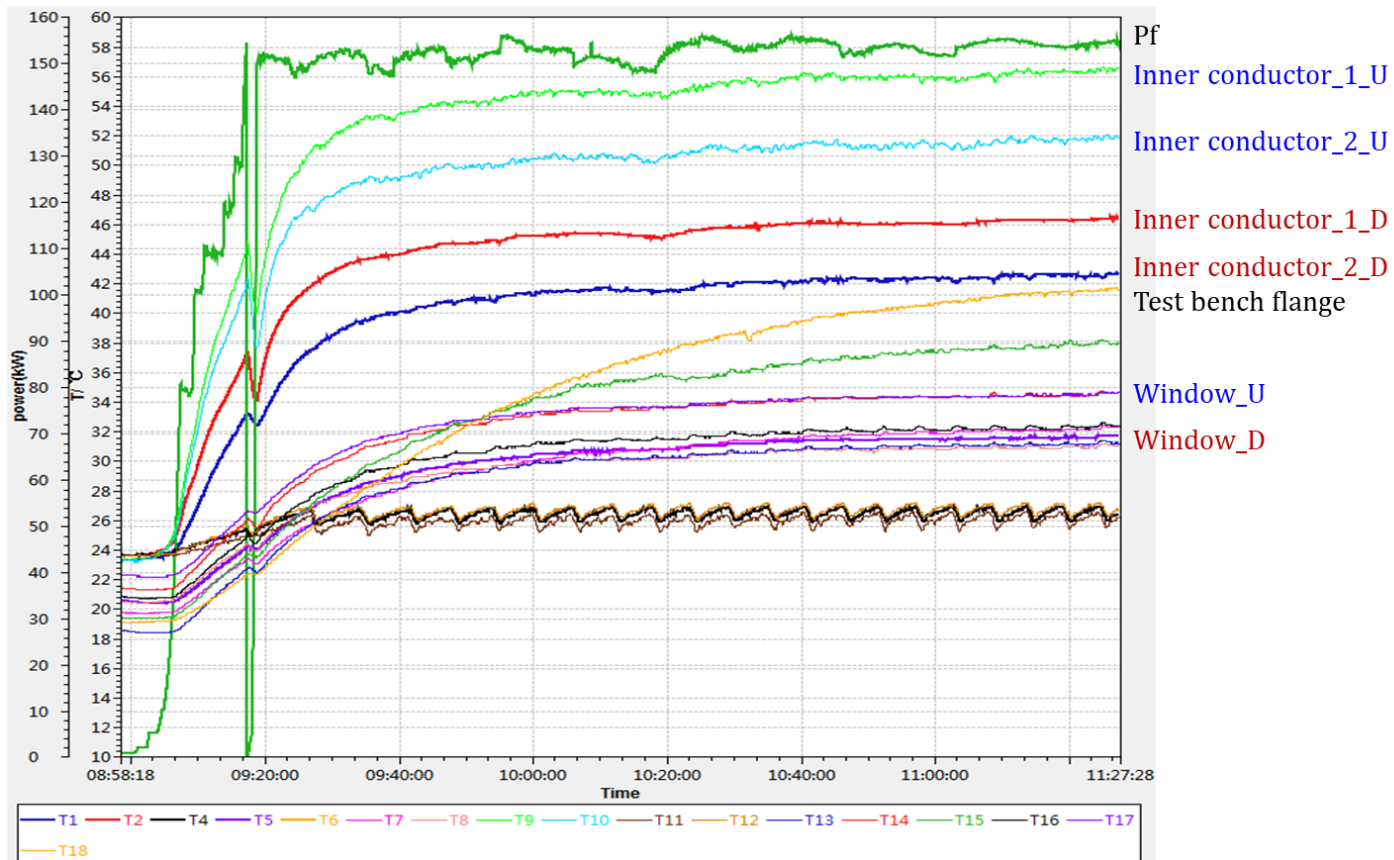
650MHz 150kW SSPA



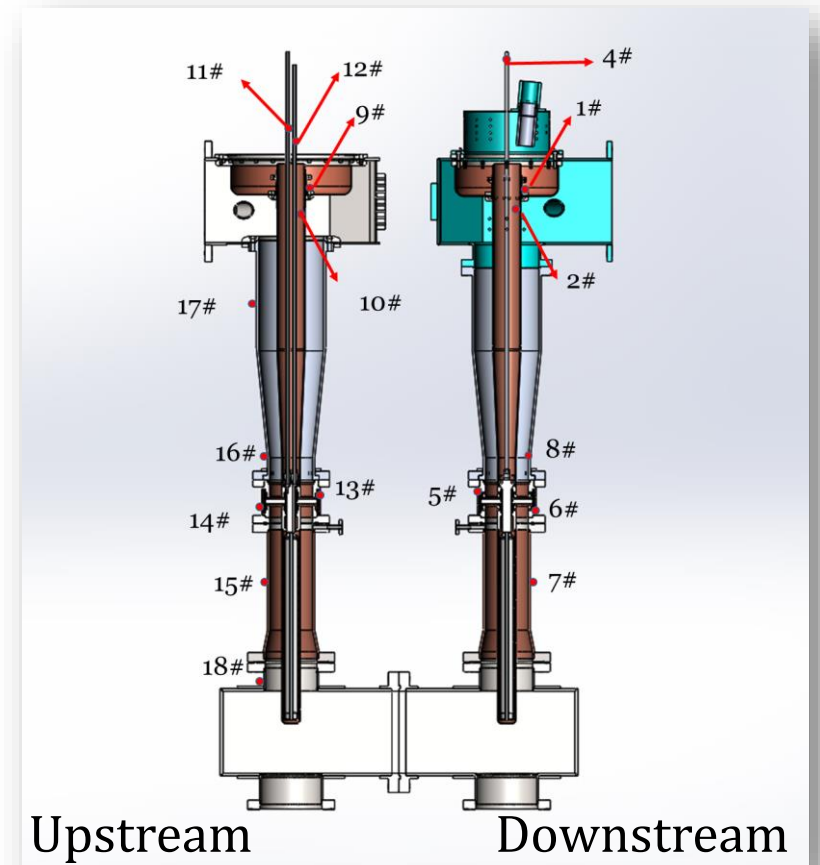
# Temperature

- The temperatures were recorded during power keep at 150kW CW
- Maximum temperature reached 55°C at T-sensor 9# of upstream FPC
- Window temperature below 35°C

650MHz SSPA



## T-sensors

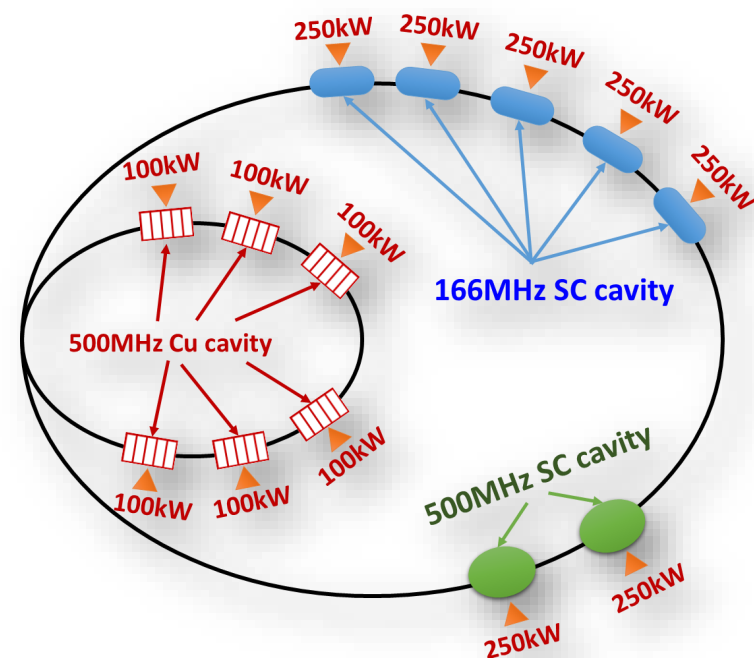






# Summary

- HPRF system for the HEPS project has been designed
- 1.25MW@166.6MHz and >600kW@500MHz power sources are required
- Solid-state technology are adopted
- Power couplers at 166.6MHz for SRF cavity have been successfully conditioned

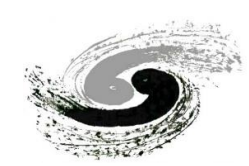




# What's next

- **Working closely with our vendors for both 166.6MHz and 500MHz SSPA**
  - ✓ Better cooling for the 2kW power unit (power supply)
  - ✓ Suppress current harmonics (40% → 10%)
  - ✓ Longer pulse (increase power storage elements in the power supply)
  - ✓ Evaluate power combination schemes
- **Questions?**
  - ✓ Output power linearity, early compression (efficiency)? From operation point of view?
  - ✓ Output signal phase distortion, the cause?

# Backup slides



# Power handling capability

Dielectric loss:  $\sim f$

Metal surface loss:  $\sim f^{0.5}$

$$650\text{MHz}/166.6\text{MHz} = 3.9$$

Scaled to 166.6MHz

	166.6MHz 50kW SSPA [kW]	650MHz 150kW SSPA [kW]
Ceramic window	50	585
Metal part	50	296